Veloxity 120

Board Specifications

FPGA	 AMD Xilinx Virtex Ultrascale Plus XCVU13PFHGA2014-2L-E Speed Grade -2, Low Power Extended temperature range
On-board Flash	2Gbit Flash memory for booting FPGA
DRAM	4x banks chip on board DDR4With ECC, up to 32GB total (4x 8GB)2400MHz
Host interface	PCle Gen3 x 16 interface direct to FPGA, connected PCl-e hard IP
External Power Pin	1 PCle Slot (75W)8-Pin connector (up to 150W)
VCCINT Supply	• 80A
Config Port	PMBus, Soft Processor JTAG

SD Card	Boot drive for soft core processorData storage expansion
Cooling	 Standard: single-width passive heat- sink
Electrical	 PCle slot (75W) or 1 x 8-pin External PCle 12V connectors (150W) Up to 225W total
Environmental	• Operating Temperature: 0°C - 100°C
Quality	 Manufactored to IPC-A-610 Class 2 RoHS compliant CE, FCC, UKCA & ICES approvals
Form factor	 1/2 length, 1/2 height PCle single-slot board Size: 68.9mm x 167.65mm (2.7in x 6.6in)

Development Tools

System development	 Pascaline SDK including PCl-e driver, Host software, libraries, and board utilities
Application development	C High level synthesis, AMD Vivado Tools (HDL and VHDL)

To learn more, visit www.pascalinetechnology.com

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