

IEEE WMED 2026 Technical Program	
7:15: am	Check-In, Registration, and Breakfast (Double R Ranch Room)
8:00am	Welcome Address by General Chair, IEEE WMED 2026
	Double R Ranch Room Chunhua Yao, Principal Engineer, Micron Technology Inc.
	Plenary Session
	Double R Ranch Room, Session Chair: Brian Callaway
8:10am	Visionary Talk Powering the AI Era: Memory Technology at the Center of Compute Scaling Shiratake-san, Senior VP, DRAM Technology and Products Group, Micron Technology Inc.
	Keynotes Session
	Double R Ranch Room, Session Chair: Nidish Vashistha
9:10am	Keynote Talk I: Decomposing At-Scale AI Inference: The Where, What, and Why of Memory Technologies Dave Nellans, Senior Director, Architecture Research Group, Nvidia
10:10am	Keynote Talk II: Hardware Security: The Next Frontier Tamara Schmitz, Director, Product Security Group, Micron Technology Inc.
	Invited Tutorials and Talks Track
	Double R Ranch Room, Session Chair: Tim Hollis
10:50am	Tutorial Machine Learning-Based Interface Design for Equalization-Less (or Minimal-Equalization) Channel Compensation Tejasvi Anand, Ramin Javadi, and Jong Hyun (John) Kim, Oregon State University
12:10pm	Lunch Break (Double R Ranch and Skyline Room)
12:10pm	Poster Session (Double R Ranch)
1:00pm	Invited Talk Lithography Technology for Future Computing Technology Michael Lercel, Senior Director, Strategic Marketing, ASML
1:45pm	Panel Discussion
	Double R Ranch Room, Moderator: Alex Sheldon Hardware Innovation in the AI Era: Memory, Compute and Security Panelists: Tamara Schmitz, Director, Product Security, Micron Technology Inc. Zain Ul Abideen, Assistant Professor, ECE Department, University of Idaho Dave Nellans, Senior Director, Architecture Research Group, Nvidia
2:40pm	Break (Double R Ranch and Skyline Room) - Announcement of Poster Winners
	Special Talk
	Double R Ranch Room, Session Chair: Alex Sheldon
2:50pm	On-chip data-path equalization using distributed and interleaved inverted feedback Timothy Hollis, Chulkyu Lee and Chris Holub, Micron
	Parallel Talks:
	Track 1 (Devices and Process)
	Double R Ranch Room, Session Chair: Amit Ohri
3:20pm	Scaling 2D CMOS: From Transistors to 3D Integrated Systems Saptarshi Das, Ackley Professor of Engineering and MRI Fellow, Pennsylvania State Univ.
	AI-Driven Design and Synthesis of High-Performance Analog Integrated Circuits Mohamed Elamien, Assistant Professor, McMaster University
4:00pm	Study of Access Region Sheet Resistance in Superlattice U-GaN HEMT and its Compact Modeling Vikram Magendar and Hiu Yung Wong, San Jose State University
4:25pm	Machine Learning-Based Flaky Test Detection and Suppression for UVM Verification Regressions Vikash Kumar, Yunus Akhtar and Ravi Gupta, Intel
	Design Tradeoffs of Through-Silicon-Carbide Vias for High-Temperature 3D SiC IC Integration Sahithi Thota, Li Song and Feng Li, University of Idaho
5:00pm	Validation Proportion Matters: Generalization Stability of YOLO-Based Wafer Defect Detection with Limited Data Chih-Kuan Ho and David Parent, San Jose State University
5:00pm	Closing Remarks by General Chair - Announcement for WMED 2027