

**(3<sup>RD</sup> SEMESTER CSE)**

<b>DISCIPLINE:CSE</b>	<b>SEMESTER:3RD</b>	<b>NAME OF THE TEACHING FACULTY: Amod Bagh</b>
<b>SUBJECT:CSA</b>	<b>NO.OF DAYS/PER WEEK CLASS ALLOTTED: 4</b>	<b>NO.OF WEEKS:15</b>
<b>WEEK</b>	<b>CLASS DAY</b>	<b>THEORY/PRACTICAL TOPICS</b>
1 <sup>ST</sup>	1 <sup>ST</sup>	Basic structure of computer hardware
	2 <sup>ND</sup>	Basic Structure of computer hardware
	3 <sup>RD</sup>	Functional Units
	4 <sup>TH</sup>	Computer components
2 <sup>ND</sup>	1 <sup>ST</sup>	Performance measures
	2 <sup>ND</sup>	Memory addressing & Operations
	3 <sup>RD</sup>	Instructions & instruction Sequencing
	4 <sup>TH</sup>	Fundamentals to instructions
3 <sup>RD</sup>	1 <sup>ST</sup>	Fundamentals to instructions
	2 <sup>ND</sup>	Operands
	3 <sup>RD</sup>	Op Codes
	4 <sup>TH</sup>	Instruction formats
4 <sup>TH</sup>	1 <sup>ST</sup>	Addressing Modes
	2 <sup>ND</sup>	Processor System
	3 <sup>RD</sup>	Register Files
	4 <sup>TH</sup>	Complete instruction execution
5 <sup>TH</sup>	1 <sup>ST</sup>	Complete instruction execution
	2 <sup>ND</sup>	Fetch
	3 <sup>RD</sup>	Decode
	4 <sup>TH</sup>	Execution
6 <sup>TH</sup>	1 <sup>ST</sup>	Hardware control
	2 <sup>ND</sup>	Hardware control
	3 <sup>RD</sup>	Micro program control
	4 <sup>TH</sup>	Memory System
7 <sup>TH</sup>	1 <sup>ST</sup>	Memory characteristics
	2 <sup>ND</sup>	Memory characteristics
	3 <sup>RD</sup>	Memory hierarchy
	4 <sup>TH</sup>	Memory hierarchy
8 <sup>TH</sup>	1 <sup>ST</sup>	RAM and ROM organization
	2 <sup>ND</sup>	Interleaved Memory
	3 <sup>RD</sup>	Cache memory
	4 <sup>TH</sup>	Cache memory
9 <sup>TH</sup>	1 <sup>ST</sup>	Virtual memory
	2 <sup>ND</sup>	Input – Output System
	3 <sup>RD</sup>	Input - Output Interface
	4 <sup>TH</sup>	Modes of Data transfer

10 <sup>TH</sup>	1 <sup>ST</sup>	Modes of Data transfer
	2 <sup>ND</sup>	Programmed I/O Transfer
	3 <sup>RD</sup>	Programmed I/O Transfer
	4 <sup>TH</sup>	Interrupt driven I/O
11 <sup>TH</sup>	1 <sup>ST</sup>	Interrupt driven I/O
	2 <sup>ND</sup>	DMA
	3 <sup>RD</sup>	I/O Processor
	4 <sup>TH</sup>	I/O Interface & Bus architecture
12 <sup>TH</sup>	1 <sup>ST</sup>	Bus and System Bus
	2 <sup>ND</sup>	Types of System Bus
	3 <sup>RD</sup>	Data Bus
	4 <sup>TH</sup>	Address Bus Control
13 <sup>TH</sup>	1 <sup>ST</sup>	Bus Structure
	2 <sup>ND</sup>	Bus Structure
	3 <sup>RD</sup>	Basic Parameters of Bus design
	4 <sup>TH</sup>	SCSI
14 <sup>TH</sup>	1 <sup>ST</sup>	USB
	2 <sup>ND</sup>	. Parallel Processing
	3 <sup>RD</sup>	Parallel Processing
	4 <sup>TH</sup>	Linear Pipeline
15 <sup>TH</sup>	1 <sup>ST</sup>	Multiprocessor
	2 <sup>ND</sup>	Multiprocessor
	3 <sup>RD</sup>	Flynn"s Classification
	4 <sup>TH</sup>	Flynn"s Classification