GOPAL KRISHNA COLLEGE OF ENGINEERING AND TECHNOLOGY

GOURAHARI VIHAR, PO: RANIPUT, JEYPORE – 764 005

LESSON PLAN

Name of the Subject: DIGITAL SYSTEM DESIGN
Name of the Faculty: Jagannath Das
Semester: 4TH Semester **Branch:** ETC
Semester From: July to December **No. of Weeks:** 15 Weeks

Week	Day	Theory/ Practical Topics	Classes
1		**Unit 1-Number of systems, Boolen Algebra and Logic Gates**	**6**
	1.	Introduction to various number systems and their Conversion	1
	2.	Arithmetic Operation using 1's and 2`s Compliments	1
	3.	Signed Binary and Floating-Point Number Representation Introduction to Binary codes and their applications.	1
	4.	Boolean algebra and identities, Complete Logic set	1
2	5.	logic gates and truth tables. Universal logic gates	1
	6.	Algebraic Reduction and realization using logic gates.	1
		Unit 2-Combinational Logic Deisgn , Logic Components	**8**
	7.	Sum of product & product of sums	1
	8.	K-Maps: Two, Three and Four variable K-maps	1
3	9.	Quine-McCluskey's method	1
	10.	NAND and NOR Logic Implementations.	1
	11.	Concept of Digital Components, Binary Adders	1
	12.	Subtraction and Multiplication	1
4	13.	An Equality Detector and Comparator, Decoder, Encoders	1
	14.	Multiplexers and De-multiplexers	1
		Unit 3- Synchronous Sequential logic Design	**6**
	15.	sequential circuits, storage elements	1
	16.	Latches (SR, D)	1
5	17.	Storage elements	1
	18.	Flip-Flops inclusion of Master-Slave	1
	19.	characteristics equation and state diagram of each FFs and Conversion of Flip-Flops	1
	20.	Analysis of Clocked Sequential circuits and Mealy and Moore Models of Finite State Machines	1
6		**Unit 4- Binary Counters, Shift Registers**	**6**
	21.	Introduction, Principle and design of synchronous and asynchronous counters	1
	22.	Design of MOD-N counters, Ring counters	1

	23.	Decade counters, State Diagram of binary counters	1
7	24.	Principle of 4-bit shift registers	1
	25.	Shifting principle, Timing Diagram, SISO	1
	26.	SIPO, PISO and PIPO registers	1
		Unit 5- Programmable Logic Devices, IC Logic Families	**4**
	27.	Operation and Circuit implementation of PROM, PAL, PLA.	1
8	28.	Properties DTL, RTL	1
	29.	TTLand CMOS and its gate level implementation	1
	30.	A/D converters and D/A converters	1
	31.		

Course Outcomes:

After the completion of this course, students will be able to:

1. Understand the representation of number systems, binary codes, and Boolean algebra for logic circuit design.
2. Design and simplify combinational logic circuits using tools like K-maps and Quine-McCluskey methods.
3. Analyze and construct sequential circuits using flip-flops, state diagrams, and finite state machine models.
4. Design counters, shift registers, and memory components while understanding programmable logic devices.
5. Develop, simulate, and implement basic digital systems using Verilog/VHDL programming.

Books Recommended:

1. Digital Design, 3rd Edition, Moris M. Mano, Pearson Education.
2. Fundamentals of digital circuits, 8th edition, A. Anand Kumar, PHI
3. Digital Fundamentals, 5th Edition, T.L. Floyd and R.P. Jain, Pearson Education, New Delhi.
4. Digital Electronics, G. K. Kharate, Oxford University Press.
5. Digital Systems – Principles and Applications, 10th Edition, Ronald J. Tocci, Neal S. Widemer and Gregory L. Moss, Pearson Education.
6. A First Course in Digital System Design: An Integrated Approach, India Edition, John P. Uyemura, PWS Publishing Company, a division of Thomson Learning Inc.
7. Digital Systems – Principles and Applications, 10th Edition, Ronald J. Tocci, Neal S. Widemer and Gregory L. Moss, Pearson Education.