

**GOAPAL KRISHNA COLLEGE OF ENGINEERING AND TECHNOLOGY**  
**GOURAHARI VIHAR, PO: RANIPUT, JEYPORE – 764 005**

**LESSON PLAN**

Name of the Subject: CSPC3004 COMPILER DESIGN

Session : 2025-26

Name of the Faculty: Amod Kumar Bagh

Semester: 6<sup>th</sup>

Branch: Computer Sc. & Engg.

Semester From:

No. of Weeks: 10 Weeks

WEEK	DAY	THEORY TOPICS	CLASSES
<b>Module-I: Introduction to Compiler and Lexical Analysis</b>			<b>8</b>
1	1	Language processing systems:	1
	2	preprocessors, compiler, assembler, interpreter, linker, bootstrap loaders	2
	3		
	4	cross compilers. Structure of a compiler and the different phases.	1
2	5	Lexical Analysis: Role of the lexical analyzer, input buffering, regular expressions	2
	6		
	7	construction of NFA and DFA, DFA minimization, transition diagrams, token recognition. Lexical errors and recovery, introduction to lexical analyzer generator (LEX).	2
	8		
<b>Module-II: Syntax Analysis</b>			<b>8</b>
3	9	Role of a parser, derivation,	1
	10	ambiguity, left recursion, left factoring, recursive descent parsing. Bottom-up parsing:	1
	11	Shift-reduce parsing, operator precedence parsing, handles and handle pruning.	2
	12		
4	13	Introduction to LR parsing: SLR, CLR, and LALR parsing tables.	1
	14	Parser conflicts and resolution, dangling else problem,	1
	15	error recovery strategies in parsing. Parser generator (YACC).	2
	16		
<b>Module-III: Connecting devices:</b>			<b>8</b>
5	17	Semantic Analysis and Syntax-Directed Translation	2
	18	Semantic Analysis: Syntax-directed definitions (SDD),	
	19	evaluation of semantic rules. Translation schemes: Syntax-directed translation schemes (SDTS),	1
	20	implementation of S-attributed and L-attributed definitions.	1
6	21	Type checking and conversions. Intermediate Code Generation:	1
	22	Abstract syntax trees, three-address code, quadruples, triples, indirect triples.	1

	23	Translation of expressions, assignment statements, Boolean expressions, case statements, back patching, and procedure calls.	2
	24		
<b>Module-IV: (8 Hrs.) Internetworking:</b>			<b>8</b>
7	25	Symbol Table and Runtime Environment Symbol table:	1
	26	structure, operations (insert, lookup), scope management, activation records. Runtime environment: storage organization, stack allocation,	3
	27		
	28		
8	29	heap management, access to non-local data, parameter passing	2
	30		
	31	mechanisms. Error handling: classification of errors,	1
	32	error recovery strategies.	1
<b>Module-V: (08 Hrs.) Code Optimization and Code Generation</b>			<b>08</b>
9	33	Code optimization: Machine-independent: constant folding, common subexpression elimination,	1
	34	dead code elimination, copy propagation, loop optimization,	1
	35	strength reduction, basic blocks and flow graphs, data flow analysis. Machine-dependent: DAG-based optimization, peephole optimization, register allocation,	2
	36		
10	37	instruction scheduling, inter-procedural optimization,	1
	38	garbage collection using reference counting. Code generation: target machine model, issues in the design of a code generator,	2
	39		
	40	generation of code for expressions and control structures.	1