

# LAB MANUAL PHYSICS

**PRACTICAL** 



# **LIST OF EXPERIMENTS**

- 1. Determination of the specific resistance of the material of the given coil using metre bridge.
- 2. Determination of the value of the horizontal component of the Earth's magnetic field using tangent galvanometer.
- 3. Comparison of emf of two cells using potentiometer.
- 4. Determination of the refractive index of the material of the prism by finding angle of prism and angle of minimum deviation using spectrometer.
- 5. Determination of the wavelength of a composite light by normal incidence method using diffraction grating and spectrometer (The number of lines per metre length of the grating is given).
- 6. Investigation of the voltage-current (V-I) characteristics of PN junction diode.
- 7. Investigation of the voltage-current (V-I) characteristics of Zener diode.
- 8. Investigation of the static characteristics of a NPN Junction transistor in common emitter configuration.
- 9. Verification of the truth table of the basic logic gates using integrated circuits.
- 10. Verification of De Morgan's theorems using integrated circuits.



# 1. SPECIFIC RESISTANCE OF THE MATERIAL OF THE COIL USING METRE BRIDGE

AIM To determine the specific resistance of the material of the given coil

using metre bridge.

**APPARATUS REQUIRED** Meter bridge, galvanometer, key, resistance box, connecting wires,

Lechlanche cell, jockey and high resistance.

**FORMULA**  $\rho = \frac{X\pi r^2}{I} (\Omega m)$ 

where,  $\rho \rightarrow$  Specific resistance of the given coil ( $\Omega$ m)

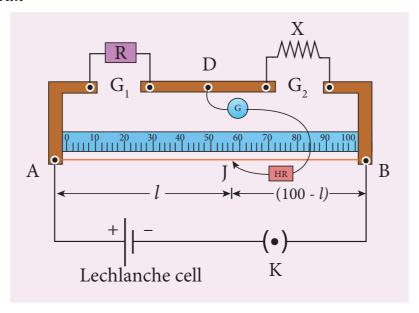
 $X \rightarrow \text{Resistance of the given coil } (\Omega)$ 

 $R \rightarrow \text{Known resistance }(\Omega)$ 

 $L \rightarrow$  Length of the coil (m)

 $r \rightarrow \text{Radius of the wire (m)}$ 

# **CIRCUIT DIAGRAM**



#### **PROCEDURE**

- A resistance box *R* is connected in the left gap and the unknown resistance X in the right gap.
- A Lechlanche cell is connected across the wire of length 1 m through a key.
- A sensitive galvanometer *G* is connected between the central strip and the jockey through a high resistance (HR).
- With a suitable resistance included in the resistance box, the circuit is switched on.
- To check the circuit connections, the jockey is pressed near one end of the wire, say A. The galvanometer will show deflection in one direction. When the jockey is pressed near the other end of the wire B, the galvanometer will show deflection in the opposite direction. This ensures that the circuit connections are correct.

• The balancing length 
$$AJ = l$$
 is noted.

• The unknown resistance 
$$X_1$$
 is found using the formula  $X_1 = \frac{R(100 - l)}{l}$ .

- The experiment is repeated for different values of *R*.
- The same procedure is repeated after interchanging R and X.
- The unknown resistance  $X_2$  is found using the formula  $X_2 = \frac{Rl}{(100-l)}$ .
- The experiment is repeated for same values of *R* as before.
- The resistance of the given coil is found from the mean value of  $X_1$  and  $X_2$ .
- The radius of the wire *r* is found using screw gauge.
- The length of the coil *L* is measured using meter scale.
- From the values of *X*, *r* and *L*, the specific resistance of the material of the wire is determined.

length of the coil, L =\_\_\_\_\_ cm.

Table 1 To find the resistance of the given coil

		Befor	e interchanging	After in	terchanging	Mean	
S.No.	Resistance $R(\Omega)$	Balancing length <i>l</i> (cm)	$X_1 = \frac{R(100 - l)}{l}(\Omega)$	Balancing length <i>l</i> (cm)	$X_2 = \frac{Rl}{(\Omega)} = \frac{100 - l}{(100 - l)}$	$X = \frac{X_1 + X_2}{2}$ (\Omega)	
1							
2							
3							

Mean resistance,  $X = -----\Omega$ 

Table 2 To find the radius of the wire

Zero error

Zero correction = LC = 0.01 mm

Sl.No.	PSR (mm)	HSC (div.)	Total Reading = PSR + (HSC × LC) (mm)	Corrected Reading $= TR \pm ZC$ (mm)
1			(11111)	(IIIII)
2				
3				
4				
5				
6				

Mean diameter  $2r = \dots mm$ Radius of the wire  $r = \dots mm$  $r = \dots m$ 

# **CALCULATION**

(i) 
$$\rho = \frac{X\pi r^2}{L} =$$

#### **RESULT**

The specific resistance of the material of the given coil =  $(\Omega m)$ 

#### Note:

i) To check the circuit connections:

The meter bridge wire is touched near one end (say, end A) with jockey, galvanometer shows a deflection in any one direction. Now the other end (say, end B) is touched. If the galvanometer shows a deflection in the opposite direction, then the circuit connections are correct.

ii) The usage of high resistance (HR):

The galvanometer is a very sensitive device. If any high current flows through the galvanometer, its coil gets damaged. Therefore in order to protect the galvanometer, a high resistance (HR) is used. When HR is connected in series with the galvanometer, the current through it is reduced so that the galvanometer is protected. But the balancing length is not accurate.

iii) To find the accurate balancing length:

The HR is first included in the circuit (that is, the plug key in HR is removed), the approximate balancing length is found. Now HR is excluded in the circuit (that is, the plug key in HR is closed), then the accurate balancing length is found.

# 2. HORIZONTAL COMPONENT OF EARTH'S MAGNETIC FIELD USING TANGENT GALVANOMETER

**AIM** 

To determine the horizontal component of the Earth's magnetic field using tangent galvanometer.

**APPARATUS REQUIRED** 

Tangent galvanometer (TG), commutator, battery, rheostat, ammeter, key and connecting wires.

**FORMULA** 

$$B_H = \frac{\mu_0 nk}{2r}$$
 (Tesla)

$$k = \frac{I}{\tan \theta}$$
 (A)

where,  $B_H \rightarrow$  Horizontal component of the Earth's magnetic field (T)

 $\mu_0 \ \, \Rightarrow \ \, \text{Permeability of free space} \, (4\pi \times 10^{-7} \ \text{H m}^{-1})$ 

 $n \rightarrow Number of turns of TG in the circuit (No unit)$ 

 $k \rightarrow Reduction factor of TG (A)$ 

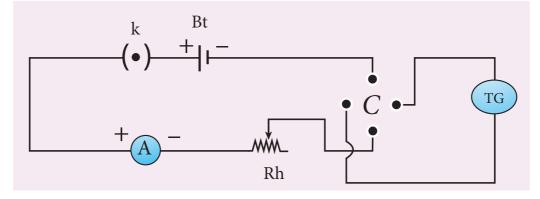
 $r \rightarrow Radius of the coil (m)$ 

# **CIRCUIT DIAGRAM**



Figure (a) Tangent Galvanometer

Figure (b) Number of turns



(c) Circuit diagram

#### **PROCEDURE**

- The preliminary adjustments are carried out as follows.
  - a. The leveling screws at the base of TG are adjusted so that the circular turn table is horizontal and the plane of the circular coil is vertical.
  - b. The circular coil is rotated so that its plane is in the magnetic meridian i.e., along the north-south direction.
  - c. The compass box alone is rotated till the aluminium pointer reads  $0^{\circ} 0^{\circ}$ .
- The connections are made as shown in Figure (c).
- The number of turns *n* is selected and the circuit is switched on.
- The range of current through TG is chosen in such a way that the deflection of the aluminium pointer lies between  $30^{\circ} 60^{\circ}$ .
- A suitable current is allowed to pass through the circuit, the deflections  $\theta_1$  and  $\theta_2$  are noted from two ends of the aluminium pointer.
- Now the direction of current is reversed using commutator C, the deflections  $\theta_3$  and  $\theta_4$  in the opposite directions are noted.
- The mean value  $\theta$  of  $\theta_1$ ,  $\theta_2$ ,  $\theta_3$  and  $\theta_4$  is calculated and tabulated.
- The reduction factor k is calculated for each case and it is found that k is a constant.
- The experiment is repeated for various values of current and the readings are noted and tabulated.
- The radius of the circular coil is found by measuring the circumference of the coil using a thread around the coil.
- From the values of *r*, *n* and *k*, the horizontal component of Earth's magnetic field is determined.

#### **Commutator:**



It is a kind of switch employed in electrical circuits, electric motors and electric generators. It is used to reverse the direction of current in the circuit.

Number of turns of the coil n =

Circumference of the coil  $(2\pi r)$  =

Radius of the coil r =

S.No	Current I	De	flection in	TG (degre	Mean θ	, I	
	(A)	$\theta_{_1}$	$\theta_2^{}$	$\theta_3$	$\theta_{_4}$	(degree)	$k = \frac{1}{\tan \theta}$
1							
2							
3							
4							
						1.6	

Mean

# **CALCULATION**

$$B_{H} = \frac{\mu_{0}nk}{2r} =$$

# **RESULT**

The horizontal component of Earth's magnetic field is found to be \_\_\_\_\_

# Note:

- i) The magnetic materials and magnets present in the vicinity of TG should be removed.
- ii) The readings from the ends of the aluminium pointer should be taken without parallax error.
- iii) The deflections of TG is restricted between 30° and 60°. It is because, the TG is most sensi tive for deflection around 45° and is least sensitive around 0° and 90°. We know that

$$I = k \tan \theta$$

or 
$$dI = k \sec^2 \theta \ d\theta$$

$$\frac{d\theta}{dI} = \frac{\sin 2\theta}{2I}$$

For given current, sensitivity  $\frac{d\theta}{dI}$  is maximum for  $\sin 2\theta = 1$  or  $\theta = 45^{\circ}$ 

# 3. COMPARISON OF EMF OF TWO CELLS USING POTENTIOMETER

**AIM** 

To compare the emf of the given two cells using a potentiometer.

**APPARATUS REQUIRED** 

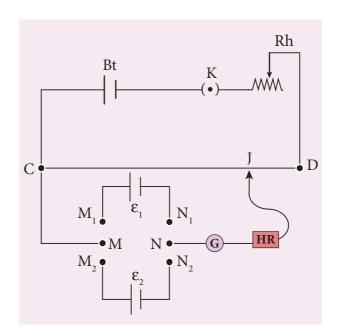
Battery eliminator, key, rheostat, DPDT switch, Lechlanche and Daniel cells, galvanometer, high resistance box, pencil jockey and connecting wires.

**FORMULA** 

$$\frac{\varepsilon_1}{\varepsilon_2} = \frac{l_1}{l_2}$$
 (Tesla)

where,  $\varepsilon_1$  and  $\varepsilon_2$  are the emf of Lechlanche and Daniel cells respectively (V)  $l_1$  and  $l_2$  are the balancing lengths for Lechlanche and Daniel cells respectively (cm)

# **CIRCUIT DIAGRAM**



#### **PROCEDURE**

- The apparatus is arranged as shown in the circuit diagram.
- The primary circuit consisting of battery, key and rheostat is connected to the potentiometer in series.
- The positive poles of the cells are connected to terminals  $M_1 \& M_2$  and the negative poles to terminals  $N_1 \& N_2$  of the DPDT switch. The potentiometer is connected to the common terminals M and N as shown in the circuit.
- Using the two-way key, Lechlanche cell is included in the circuit. By sliding the jockey on the potentiometer wire, the balancing point is found and the corresponding balancing length is measured.
- Similarly, the balancing length is found by including Daniel cell in the circuit.
- The experiment is repeated for different sets of balancing lengths by adjusting the rheostat.
- From different values of  $l_1$  and  $l_2$ , the ratio of emf of the two cells is calculated.

Table: To find the ratio of emf of two cells

S.No	Balancing length for Lechlanche cell, $l_1$ (cm)	Balancing length for Daniel cell, $l_{\scriptscriptstyle 2}$ (cm)	$\frac{\varepsilon_{_1}}{\varepsilon_{_2}} = \frac{l_{_1}}{l_{_2}}$
1			
2			
3			
4			
5			

Mean 
$$\frac{\varepsilon_1}{\varepsilon_2}$$
 =

# **CALCULATION**

$$\frac{\varepsilon_1}{\varepsilon_2} = \frac{l_1}{l_2}$$

# **RESULT**

Ratio of emf of the given two cells = \_\_\_\_\_ (no unit)



# **DPDT** switch



Double Pole Double Throw (DPDT) switch is a 6-terminal key with a throw switch (metal handle) attached to the common terminals M and N. The two-given cells are connected to the pairs of terminals  $M_1 \& M_2$  and  $N_1 \& N_2$ . When the handle is thrown to one side, say  $M_1 \& M_2$ , the cell that is connected to that

pair of terminals is included with the primary circuit.

# 4. REFRACTIVE INDEX OF THE MATERIAL OF THE PRISM

**AIM** 

To determine the refractive index of the material of a prism using spectrometer.

**APPARATUS REQUIRED** 

Spectrometer, prism, prism clamp, sodium vapour lamp, spirit level.

**FORMULA** 

$$\mu = \frac{\sin\left(\frac{A+D}{2}\right)}{\sin\left(\frac{A}{2}\right)}$$
 (No unit)

where,  $\mu \rightarrow$  Refractive index of the material of the prism (No unit)

 $A \rightarrow Angle of the prism (degree)$ 

D → Angle of minimum deviation (degree)

# **DIAGRAMS**

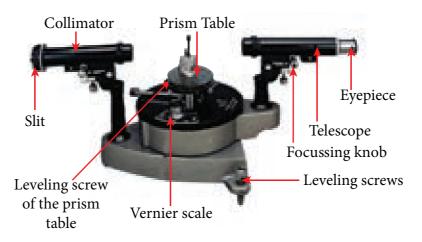


Figure (a) Angle of the prism

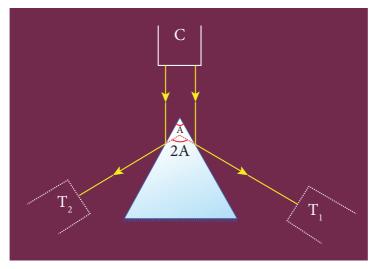


Figure (b) Angle of the prism

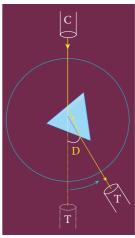


Figure (c) Angle of minimum deviation

#### **PROCEDURE**

# 1) Initial adjustments of the spectrometer

- Eye-piece: The eye-piece of the telescope is adjusted so that the cross-wires are seen clearly.
- Slit: The slit of the collimator is adjusted such that it is very thin and vertical.
- Base of the spectrometer: The base of the spectrometer is adjusted to be horizontal using leveling screws.
- Telescope: The telescope is turned towards a distant object and is adjusted till the clear inverted image of the distant object is seen. Now the telescope is adjusted to receive parallel rays.
- Collimator: The telescope is brought in line with the collimator. Collimator is adjusted until a clear image of the slit is seen in the telescope. Now the collimator gives parallel rays.
- Prism table: Using a spirit level, the prism table is adjusted to be horizontal with the three leveling screws provided in the prism table.

# 2) Determination of angle of the prism (A)

- The slit is illuminated by yellow light from sodium vapour lamp.
- The given equilateral prism is placed on the prism table in such a way that refracting edge of the prism is facing the collimator.
- The light emerging from the collimator is incident on both reflecting faces of the prism and is reflected.
- The telescope is rotated towards left to obtain reflected image of the slit from face 1 of the prism and is fixed.
- Using tangential screws, the telescope is adjusted until the vertical cross-wire coincides with the reflected image of the slit.
- The main scale reading and vernier coincidence are noted from both vernier scales.
- The telescope is now rotated towards right to obtain the reflected image from face 2 of the prism. As before, the readings are taken.
- The difference between the two readings gives 2A from which the angle of the prism A is calculated.

# 3) Determination of angle of minimum deviation (D)

- The prism table is rotated such that the light emerging from the collimator is incident on one of the refracting faces of the prism, gets refracted and emerges out from the other refracting face.
- The telescope is turned to view the refracted image.
- Looking through the telescope, the prism table is rotated in such a direction that the image moves towards the direct ray.





- The telescope is fixed in this position and is adjusted until the vertical cross-wire coincides with the refracted image of the slit.
- The readings are taken from both vernier scales.
- The prism is now removed and the telescope is rotated to obtain the direct ray image and the readings are taken.
- The readings are tabulated and the difference between these two readings gives the angle of minimum deviation D.
- From the values of A and D, the refractive index of the material of the glass prism is determined.

#### Least count

1 MSD = 30'

Number of vernier scale divisions = 30

For spectrometer, 30 vernier scale divisions will cover 29 main scale divisions.

Or 1 VSD = 
$$\left(\frac{29}{30}\right)$$
 MSD

Least count (LC) = 1 MSD - 1 VSD

$$= \left(\frac{1}{30}\right) MSD = \left(\frac{1}{30}\right) \times 30'$$
$$= 1'$$

#### **OBSERVATION**

**Table 1** To find the angle of the prism (A)

Imaga	7	Vernier A	A (Degree)	Vernier B (Degree)			
Image	MSR	VSC	TR	MSR	VSC	TR	
Reflected image from face 1							
Reflected image from face 2							
Difference 2A							

Mean 2A =

Mean A =

**Table 2** To find the angle of minimum deviation (D)

Imaga	7	Vernier <i>P</i>	A (Degree)	Vernier B (Degree)			
Image	MSR	VSC	TR	MSR	VSC	TR	
Refracted image							
Direct image							
Difference D							

Mean D =

# **CALCULATION**

$$\mu = \frac{\sin\left(\frac{A+D}{2}\right)}{\sin\left(\frac{A}{2}\right)}$$

# **RESULT**

- 1. Angle of the Prism (A) = ..... (degree)
- 2. Angle of the minimum deviation of the prism (D) =..... (degree)
- 3. Refractive index of the material of the Prism ( $\mu$ ) =..... (No unit)

#### Note:

- i) Once initial adjustments are done, spectrometer should not be disturbed.
- ii) Total reading  $TR = MSR + (VSC \times LC)$

Where

MSR → Main Scale Reading

VSC → Vernier Scale Coincidence

 $LC \rightarrow Least count (= 1')$ 

# 5. WAVELENGTH OF THE CONSTITUENT COLOURS OF A COMPOSITE LIGHT USING DIFFRACTION GRATING AND SPECTROMETER

**AIM** To find the wavelength of the constituent colours of a composite light

using diffraction grating and spectrometer.

**APPARATUS REQUIRED** Spectrometer, mercury vapour lamp, diffraction grating, grating table,

and spirit level.

FORMULA  $\lambda = \frac{\sin \theta}{nN}$  Å

where,  $\lambda \rightarrow$  Wavelength of the constituent colours of a composite light ( $\mathring{a}$ )

N → Number of lines per metre length of the given grating (No unit) (the value of N for the grating is given)

 $n \rightarrow Order$  of the diffraction (No unit)

 $\theta \rightarrow$  Angle of diffraction (degree)

# **DIAGRAMS**

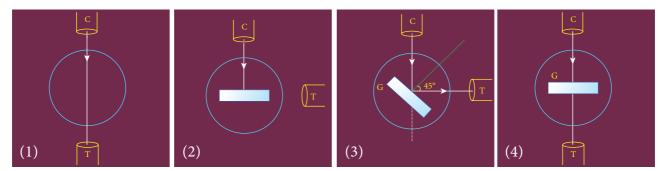


Figure (a) Normal incidence

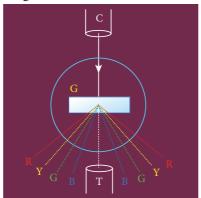


Figure (b) Angle of diffraction

# **PROCEDURE**

# 1) Initial adjustments of the spectrometer

- Eye-piece: The eye-piece of the telescope is adjusted so that the cross-wires are seen clearly.
- Slit: The slit of the collimator is adjusted such that it is very thin and vertical.
- Base of the spectrometer: The base of the spectrometer is adjusted to be horizontal using leveling screws.

- Telescope: The telescope is turned towards a distant object and is adjusted till the clear image of the distant object is seen. Now the telescope is adjusted to receive parallel rays.
- Collimator: The telescope is brought in line with the collimator. Collimator is adjusted until a clear image of the slit is seen in the telescope. Now the collimator gives parallel rays.
- Grating table: Using a spirit level, the grating table is adjusted to be horizontal with the three leveling screws provided in the grating table.

# 2) Adjustment of the grating for normal incidence

- The slit is illuminated with a composite light (white light) from mercury vapour lamp.
- The telescope is brought in line with the collimator. The vertical cross-wire is made to coincide with the image of the slit (Figure (a)1).
- The vernier disc alone is rotated till the vernier scale reads  $0^{\circ}$   $180^{\circ}$  and is fixed. This is the reading for the direct ray.
- The telescope is then rotated (anti-clockwise) through an angle of 90° and fixed (Figure (a)2).
- Now the plane transmission grating is mounted on the grating table.
- The grating table alone is rotated so that the light reflected from the grating coincides with vertical cross-wire of the telescope. The reflected image is white in colour (Figure (a)3).
- Now the vernier disc is released. The vernier disc along with grating table is rotated through an angle of 45° in the appropriate direction such that the light from the collimator is incident normally on the grating (Figure (a)4).

# 3) Determination of wave length of the constituent colours of the mercury spectrum

- The telescope is released and is brought in line with the collimator to receive central direct image. This undispersed image is white in colour.
- The diffracted images of the slit are observed on either side of the direct image.
- The diffracted image consists of the prominent colours of mercury spectrum in increasing order of wavelength.
- The telescope is turned to any one side (say left) of direct image to observe first order diffracted image.
- The vertical cross-wire is made to coincide with the prominent spectral lines (violet, blue, yellow and red) and the readings of both vernier scales for each case are noted.
- Now the telescope is rotated to the right side of the direct image and the first order image is observed.
- The vertical cross-wire is made to coincide with the same prominent spectral lines and the readings of both vernier scales for each case are again noted.
- The readings are tabulated.
- The difference between these two readings gives the value of  $2\theta$  for the particular spectral line.
- The number of lines per metre length of the given grating N is noted from the grating.
- From the values of N, n and  $\theta$ , the wave length of the prominent colours of the mercury light is determined using the given formula.





To find the wave length of prominent colours of the mercury spectrum

		Diffracted Ray Reading (Degree)									Di	fferei	260	θ		
ight			L	eft					Ri	ght				11e1e1 2θ	ice	U
Colour of Light	V	erni'	er A	7	/ern	ier B	V	erni'	er A	Ver	nier	В	(Degree)		e)	(Degree)
Colou	MSR	VSC	TR	MSR	VSC	TR	MSR	NSC	TR	MSR	VSC	TR	VER A	VER B	Mean	
Blue																
Green																
Yellow																
Red																

# **CALCULATION**

(i) For blue, 
$$\lambda = \frac{\sin \theta}{nN}$$
,

(ii) For green, 
$$\lambda = \frac{\sin \theta}{nN}$$

(iii) For yellow, 
$$\lambda = \frac{\sin \theta}{nN}$$
,

(iv) For red, 
$$\lambda = \frac{\sin \theta}{nN}$$

# **RESULT**

- 1. The wavelength of blue line = -----m
- 2. The wavelength of green line = -----m
- 3. The wavelength of yellow line = -----m
- 4. The wavelength of red line = -----m

# Note:

- i) Once initial adjustments are done, spectrometer should not be disturbed.
- ii) Total reading  $TR = MSR + (VSC \times LC)$

Where

MSR → Main Scale Reading

VSC → Vernier Scale Coincidence

 $LC \rightarrow Least count (= 1')$ 

# 6. VOLTAGE-CURRENT CHARACTERISTICS OF A PN JUNCTION DIODE

AIM To draw the voltage-current (V- I) characteristics of the PN junction

diode and to determine its knee voltage and forward resistance.

**APPARATUS REQUIRED** PN junction diode (IN4007), variable DC power supply,

milli-ammeter, micro-ammeter, voltmeter, resistance and

connecting wires.

FORMULA  $R_F = \frac{\Delta V_F}{\Delta I_F} (\Omega)$ 

where,  $R_{\scriptscriptstyle E} \rightarrow \quad$  Forward resistance of the diode ( $\Omega$ )

 $\Delta V_F$   $\rightarrow$  The change in forward voltage (volt)

 $\Delta I_{\scriptscriptstyle F}$   $\to$  The change in forward current (mA)

#### **CIRCUIT DIAGRAM**

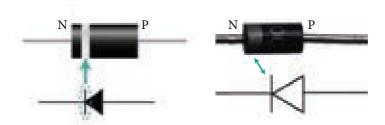


Figure (a) PN junction diode and its symbol (Silver ring denotes the negative terminal of the diode)

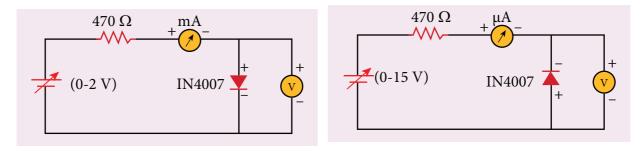


Figure (b) PN junction diode in forward bias

Figure (c) PN junction diode in reverse bias

#### **Precaution**

Care should be taken to connect the terminals of ammeter, voltmeter, dc power supply and the PN junction diode with right polarity.

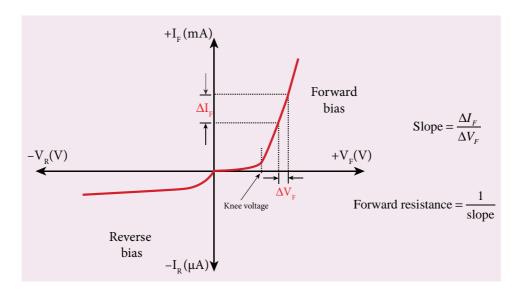
#### **PROCEDURE**

# i) Forward bias characteristics

- In the forward bias, the P- region of the diode is connected to the positive terminal and N-region to the negative terminal of the DC power supply.
- The connections are given as per the circuit diagram.
- The voltage across the diode can be varied with the help of the variable DC power supply.
- The forward voltage  $(V_F)$  across the diode is increased from 0.1 V in steps of 0.1 V up to 0.8 V and the forward current  $(I_F)$  through the diode is noted from the milli-ammeter. The readings are tabulated.
- The forward voltage  $V_p$  and the forward current  $I_p$  are taken as positive.
- A graph is drawn taking the forward voltage  $(V_F)$  along the x-axis and the forward current  $(I_F)$  along the y-axis.
- The voltage corresponding to the dotted line in the forward characteristics gives the knee voltage or threshold voltage or turn-on voltage of the diode.
- The slope in the linear portion of the forward characteristics is calculated. The reciprocal of the slope gives the forward resistance of the diode.

# ii) Reverse bias characteristics

- In the reverse bias, the polarity of the DC power supply is reversed so that the P- region of the diode is connected to the negative terminal and N-region to the positive terminal of the DC power supply
- The connections are made as given in the circuit diagram.
- The voltage across the diode can be varied with the help of the variable DC power supply.
- The reverse voltage  $(V_R)$  across the diode is increased from 1 V in steps of 1 V up to 5 V and the reverse current  $(I_R)$  through the diode is noted from the micro-ammeter. The readings are tabulated.
- The reverse voltage  $V_R$  and reverse current  $I_R$  are taken as negative.
- A graph is drawn taking the reverse bias voltage  $(V_R)$  along negative x-axis and the reverse bias current  $(I_R)$  along negative y-axis.







#### Table 1 Forward bias characteristic curve

S.No.	Forward bias voltage $V_{\scriptscriptstyle F}$	Forward bias current $I_F$
S.1NO.	(volt)	(mA)

#### Table 2 Reverse bias characteristic curve

S.No.	Reverse bias voltage $V_{\scriptscriptstyle R}$	Reverse bias current $I_R$
3.110.	(volt)	(μΑ)

#### **CALCULATION**

- (i) Forward resistance  $R_E$ =
- (ii) knee voltage =

#### **RESULT**

The V-I characteristics of the PN junction diode are studied.

- i) Knee voltage of the PN junction diode =.....V

# **Practical Tips**

- The DC power supply voltage should be increased only up to the specified range in the forward (0 2V) and reverse (0 15V) directions. Forward bias offers very low resistance and hence an external resistance of  $470\Omega$  is connected as a safety measure.
- The voltage applied beyond this limit may damage the resistance or the diode.
- In the forward bias, the current flow will be almost zero till it crosses the junction potential or knee voltage (approximately 0.7 V). Once knee voltage is crossed, the current increases with the applied voltage.
- The diode voltage in the forward direction should be increased in steps of 0.1 V to a maximum of 0.8 V after the threshold voltage to calculate the forward resistance.
- The diode voltage in the reverse direction is increased in steps of 1 V to a maximum of 5 V. The current must be measured using micro-ammeter as the strength of current in the reverse direction is very less. This is due to the flow of the minority charge carriers called the leakage current.





# 7. VOLTAGE-CURRENT CHARACTERISTICS OF A ZENER DIODE

**AIM** To draw the voltage-current (V-I) characteristic curves of a Zener

diode and to determine its knee voltage, forward resistance and

reverse breakdown voltage.

**APPARATUS REQUIRED** Zener diode 1Z5.6V, variable dc power supply (0 – 15V),

milli ammeter, volt meter, 470  $\Omega$  resistance, and connecting wires.

FORMULA  $R_F = \frac{\Delta V_F}{\Delta I_F} (\Omega)$ 

where,  $R_{\rm E} \rightarrow {\rm Forward resistance of the diode} (\Omega)$ 

 $\Delta V_F$   $\rightarrow$  The change in forward voltage (volt)

 $\Delta I_{\scriptscriptstyle F}$   $\to$  The change in forward current (mA)

#### **CIRCUIT DIAGRAM**

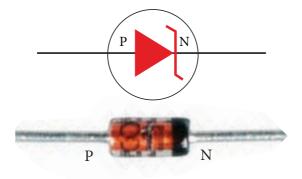
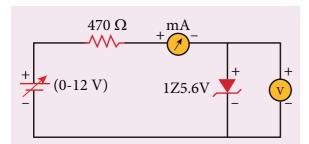


Figure (a) Zener diode and its symbol (The black colour ring denotes the negative terminal of the Zener diode)



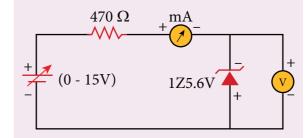


Figure (b) Zener diode in forward bias

Figure (c) Zener diode in reverse bias

#### **Precaution**

Care should be taken to connect the terminals of ammeter, voltmeter, dc power supply and the Zener diode with right polarity.

#### **PROCEDURE**

# i) Forward bias characteristics

- In the forward bias, the P- region of the diode is connected to the positive terminal and N-region to the negative terminal of the DC power supply.
- The connections are given as per the circuit diagram.
- The voltage across the diode can be varied with the help of the variable DC power supply.
- The forward voltage  $(V_F)$  across the diode is increased from 0.1V in steps of 0.1V up to 0.8V and the forward current  $(I_F)$  through the diode is noted from the milli-ammeter. The readings are tabulated.
- The forward voltage and the forward current are taken as positive.
- A graph is drawn taking the forward voltage along the x-axis and the forward current along the y-axis.
- The voltage corresponding to the dotted line in the forward characteristics gives the knee voltage or threshold voltage or turn-on voltage of the diode.
- The slope in the linear portion of the forward characteristics is calculated. The reciprocal of the slope gives the forward resistance of the diode.

# ii) Reverse bias characteristics

- In the reverse bias, the polarity of the DC power supply is reversed so that the P- region of the diode is connected to the negative terminal and N-region to the positive terminal of the DC power supply
- The connections are made as given in the circuit diagram.
- The voltage across the diode can be varied with the help of the variable DC power supply.
- The reverse voltage  $(V_R)$  across the diode is increased from 0.5V in steps of 0.5V up to 6V and the reverse current  $(I_R)$  through the diode is noted from the milli-ammeter. The readings are tabulated.
- Initially, the voltage is increased in steps of 0.5V. When the breakdown region is approximately reached, then the input voltage may be raised in steps of, say 0.1V to find the breakdown voltage.
- The reverse voltage and reverse current are taken as negative.
- A graph is drawn taking the reverse bias voltage along negative x-axis and the reverse bias current along negative y-axis.
- In the reverse bias, Zener breakdown occurs at a particular voltage called Zener voltage  $V_Z$  (~5.6 to 5.8V) and a large amount of current flows through the diode which is the characteristics of a Zener diode.
- The breakdown voltage of the Zener diode is determined from the graph as shown.





# Table 1 Forward bias characteristic curve

S.No.	Forward bias voltage $V_{\scriptscriptstyle F}$	Forward bias current $I_F$
0.110.	(volt)	(mA)

# Table 2 Reverse bias characteristic curve

S.No.	Reverse bias voltage $V_{\scriptscriptstyle R}$	Reverse bias current $I_R$
	(volt)	(mA)

# **CALCULATION**

- (i) Forward resistance  $R_F$ =
- (ii) knee voltage =
- (iii) The breakdown voltage of the Zener diode  $\mathbf{V}_{\mathbf{Z}}\!\!=\,$  ----V

# **RESULT**

The V-I characteristics of the Zener diode are studied.

- (i) Forward resistance  $R_F$ =
- (ii) knee voltage =
- (iii) The breakdown voltage of the Zener diode  $V_z = ----V$

# **Practical Tips**

- The DC power supply voltage should to be increased only up to the specified range in the forward (0 2 V) and reverse (0 15 V) directions.
- The voltage applied beyond this limit may damage the resistor or the diode.
- Zener diode functions like an ordinary PN junction diode in the forward direction. Hence the forward characteristic is the same for both PN junction diode and Zener diode. Therefore, knee voltage and forward resistance can be determined as explained in the previous experiment.
- Unlike ordinary PN junction diode, the reverse current in Zener diode is measured using milli-ammeter due to the large flow of current.





# 8. CHARACTERISTICS OF A NPN-JUNCTION TRANSISTOR IN COMMON EMITTER CONFIGURATION

**AIM** 

To study the characteristics and to determine the current gain of a NPN junction transistor in common emitter configuration.

**APPARATUS REQUIRED** 

Transistor - BC 548/BC107, bread board, micro ammeter, milli ammeter, voltmeters, variable DC power supply and connecting wires.

**FORMULA** 

$$r_i = \left[\frac{\Delta V_{BE}}{\Delta I_B}\right]_{V_{CE}} (\Omega), \quad r_o = \left[\frac{\Delta V_{CE}}{\Delta I_C}\right]_{I_B} (\Omega), \quad \beta = \left[\frac{\Delta I_C}{\Delta I_B}\right]_{V_{CE}}$$
(No unit)

Where,  $r_i \rightarrow$  Input impedance ( $\Omega$ )

 $\Delta V_{\rm BE}$   $\rightarrow$  The change in base-emitter voltage (volt)

 $\Delta I_{\rm B}$  > The change in base current ( $\mu$ A)

 $r_0 \rightarrow \text{Output impedance } (\Omega)$ 

 $\Delta V_{\text{CE}}$  > The change in collector-emitter voltage (volt)

 $\Delta I_C \rightarrow$  The change in collector current (mA)

 $\beta \rightarrow$  Current gain of the transistor (No unit)

#### **CIRCUIT DIAGRAM**

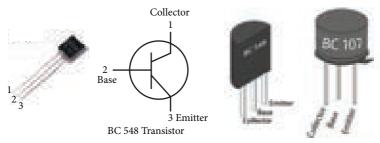


Figure (a) NPN - Junction transistor and its symbol (Transistor is held with the flat surface facing us)

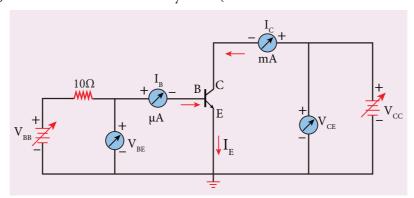


Figure (b) NPN junction transistor in CE configuration

#### **Note**

A resistor is connected in series with the base to prevent excess current flowing into the base.





# **Precautions**

- Care should be taken to connect the terminals of ammeters, voltmeters, and dc power supplies with right polarity.
- The collector and emitter terminals of the transistor must not be interchanged.

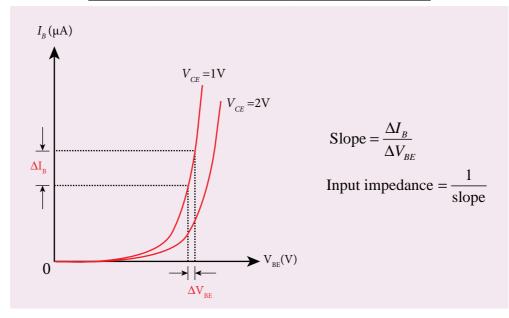
# **PROCEDURE**

- The connections are given as shown in the diagram.
- The current and voltage at the input and output regions can be varied by adjusting the DC power supply.

# (i) Input characteristic curve: $V_{BE}$ vs $I_{B}$ ( $V_{CE}$ constant)

- The collector-emitter voltage  $V_{\scriptscriptstyle CE}$  is kept constant.
- The base-emitter voltage  $V_{BE}$  is varied in steps of 0.1V and the corresponding base current  $(I_B)$  is noted. The readings are taken till  $V_{CE}$  reaches a constant value.
- The same procedure is repeated for different values of  $V_{\rm CE}$ . The readings are tabulated.
- A graph is plotted by taking  $V_{\rm BE}$  along x-axis and  $I_{\rm B}$  along y-axis for both the values of  $V_{\rm CE}$ .
- The curves thus obtained are called the input characteristics of a transistor.
- The reciprocal of the slope of these curves gives the input impedance of the transistor.

	$V_{\scriptscriptstyle CE}$	= 1V	$V_{CE} = 2V$			
S. No	$V_{_{BE}}$	$I_{_B}$	$V_{_{BE}}$	$I_{_B}$		
	(V)	<i>I<sub>B</sub></i> (μA)	(V)	(μΑ)		



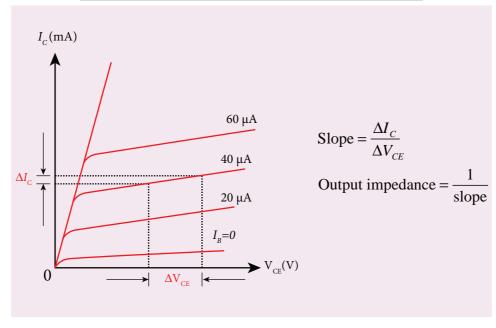




# (ii) Output characteristic curve: $V_{CE}$ vs $I_{C}$ ( $I_{B}$ constant)

- The base current  $I_B$  is kept constant.
- $V_{\it CE}$  is varied in steps of 1V and the corresponding collector current  $I_{\it C}$  is noted. The readings are taken till the collector current becomes almost constant.
- Initially  $I_B$  is kept at 0 mA and the corresponding collector current is noted. This current is the reverse saturation current  $I_{CEO}$ .
- The experiment is repeated for various values of  $I_{\rm B}$ . The readings are tabulated.
- A graph is drawn by taking  $V_{CE}$  along x-axis and  $I_{C}$  along y-axis for various values of  $I_{B}$ .
- The set of curves thus obtained is called the output characteristics of a transistor.
- The reciprocal of the slope of the curve gives output impedance of the transistor.

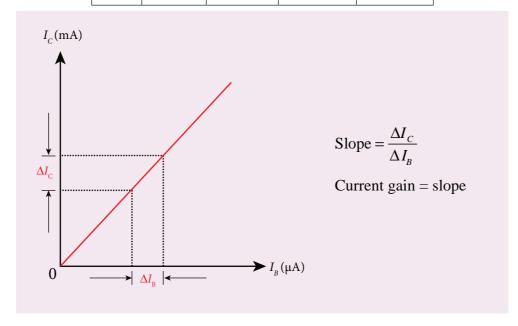
	$I_B = 20 \mu A$		$I_{B} = 40 \mu A$	
S. No	$V_{\scriptscriptstyle CE}$	$I_{C}$	$V_{_{CE}}$	$I_{C}$
	(V)	(mA)	(V)	(mA)



# (iii) Transfer characteristic curve: $I_{\scriptscriptstyle B}$ vs $I_{\scriptscriptstyle C}$ ( $V_{\scriptscriptstyle CE}$ constant)

- The collector-emitter voltage  $V_{\scriptscriptstyle CE}$  is kept constant.
- The base current  $I_B$  is varied in steps of 10  $\mu$ A and the corresponding collector current  $I_C$  is noted.
- This is repeated by changing the value of  $V_{CE}$ . The readings are tabulated.
- The transfer characteristics is a plot between the input current IB along x-axis and the output current  $I_C$  along y-axis keeping  $V_{CE}$  constant.
- The slope of the transfer characteristics plot gives the current gain  $\beta$  can be calculated.

	$V_{\scriptscriptstyle CE}$ =1V		$V_{\scriptscriptstyle CE}$ =2V	
S.No	$I_{\!\scriptscriptstyle B}$	$I_{C}$	$I_{\!\scriptscriptstyle B}$	$I_{C}$
	(μΑ)	(mA)	(μΑ)	(mA)



RESULT

- i) The input, output and transfer characteristics of the NPN junction in common emitter mode are drawn.
- ii) (a) Input impedance =  $\underline{\hspace{1cm}}$ 
  - (b) Output impedance =  $\Omega$
  - (c) Current gain  $\beta =$ \_\_\_(no unit)

# 9. VERIFICATION OF TRUTH TABLES OF LOGIC GATES **USING INTEGRATED CIRCUITS**

**AIM** 

To verify the truth tables of AND, OR, NOT, EX-OR, NAND and NOR gates using integrated circuits

**COMPONENTS REQUIRED** AND gate (IC 7408), NOT gate (IC 7404), OR gate (IC 7432), NAND gate (IC 7400), NOR gate (IC 7402), X-OR gate (IC 7486), Power supply, Digital IC trainer kit, connecting wires.

#### **BOOLEAN EXPRESSIONS**

(i) AND gate 
$$Y = A.B$$

(iv) Ex OR gate 
$$Y = \overline{A}B + A\overline{B}$$

(ii) OR gate 
$$Y = A+B$$

(v) NAND gate 
$$Y = \overline{A.B}$$

(iii) NOT gate 
$$Y = \overline{A}$$

(vi) NOR gate 
$$Y = \overline{A+B}$$

#### **CIRCUIT DIAGRAM**

#### **Pin Identification**



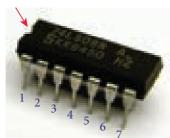


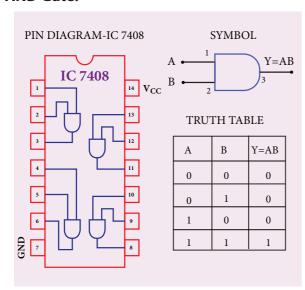


Figure (a) Integrated circuit

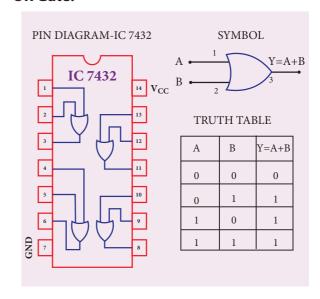
#### Note:

The chip must be inserted in the bread board in such a way that the identification mark should be on our left side. In this position, pin numbers are counted as marked in the picture above. Pin identification is the same for all chips that are mentioned below.

#### **AND Gate:**



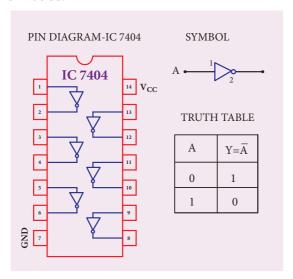
#### **OR Gate:**



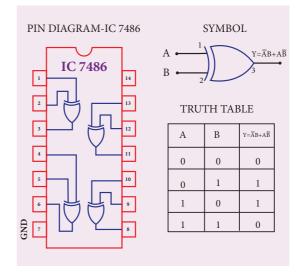




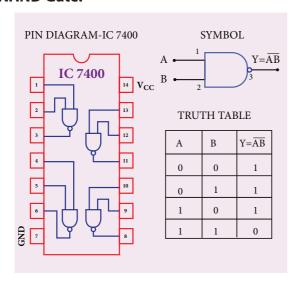
#### **NOT Gate:**



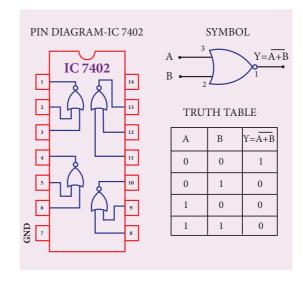
#### X-OR Gate:



#### **NAND Gate:**



# **NOR Gate:**



# **PROCEDURE**

- To verify the truth table of a logic gate, the suitable IC is taken and the connections are given using the circuit diagram.
- For all the ICs, 5V is applied to the pin 14 while the pin 7 is connected to the ground.
- The logical inputs of the truth table are applied and the corresponding output is noted.
- Similarly the output is noted for all other combinations of inputs.
- In this way, the truth table of a logic gate is verified.

# **RESULT**

The truth table of logic gates AND, OR, NOT, Ex-OR, NAND and NOR using integrated circuits is verified.

# **Precautions**

(i)  $V_{\rm CC}$  and ground pins must not be interchanged while making connections. Otherwise the chip will be damaged. (ii) The pin configuration for NOR gate is different from other gates





# 10. VERIFICATION OF DE MORGAN'S THEOREMS

**AIM:** To verify De Morgan's first and second theorems.

**COMPONENTS REQUIRED**: Power Supply (0 – 5V), IC 7400, 7408, 7432, 7404, and 7402, Digital

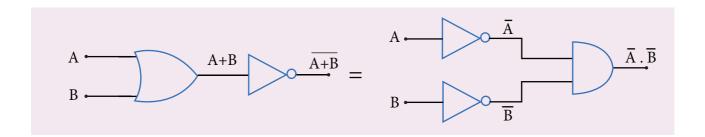
IC trainer kit, connecting wires.

**FORMULA** De Morgan's first theorem  $\overline{A+B} = \overline{A}.\overline{B}$ 

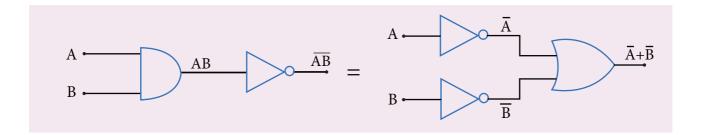
De Morgan's second theorem  $\overline{A.B} = \overline{A} + \overline{B}$ 

#### **CIRCUIT DIAGRAM:**

# De Morgan's first theorem



# De Morgan's second theorem



# **PROCEDURE:**

- i) Verification of De Morgan's first theorem
  - The connections are made for LHS  $\left[\overline{A+B}\right]$  of the theorem as shown in the circuit diagram using appropriate ICs.
  - The output is noted and tabulated for all combinations of logical inputs of the truth table.
  - The same procedure is repeated for RHS  $\lceil \overline{A}.\overline{B} \rceil$  of the theorem.
  - From the truth table, it can be shown that  $\overline{A+B} = \overline{A}.\overline{B}$ .



- ii) Verification of De Morgan's second theorem
  - The connections are made for LHS  $[\overline{A.B}]$  of the theorem as shown in the circuit diagram using appropriate ICs.
  - The output is noted and tabulated for all combinations of logical inputs of the truth table.
  - The same procedure is repeated for RHS  $[\overline{A} + \overline{B}]$  of the theorem.
  - From the truth table, it can be shown that  $\overline{A.B} = \overline{A} + \overline{B}$ .

# De-Morgan's first theorem

# **Truth Table**

A	В	$\overline{A+B}$	$\overline{A}.\overline{B}$
0	0		
0	1		
1	0		
1	1		

# De-Morgan's second theorem

#### **Truth Table**

A	В	$\overline{A.B}$	$\overline{A} + \overline{B}$
0	0		
0	1		
1	0		
1	1		

#### **RESULT**

De Morgan's first and second theorems are verified.

#### **Note**

The pin diagram for IC 7408, IC 7432 and IC 7404 can be taken from previous experiment

# **Precautions**

 $\rm V_{\rm CC}$  and ground pins must not be interchanged while making connections. Otherwise the chip will be damaged.

For the ICs used, 5V is applied to the pin 14 while the pin 7 is connected to the ground.



# SUGGESTED QUESTIONS FOR THE PRACTICAL EXAMINATION

- 1. Determine the resistance of a given wire using metre bridge. Also find the radius of the wire using screw gauge and hence determine the specific resistance of the material of the wire. Take at least 4 readings.
- 2. Determine the value of the horizontal component of the Earth's magnetic field, using tangent galvanometer. Take at least 4 readings.
- 3. Compare the emf of two cells using potentiometer.
- 4. Using the spectrometer, measure the angle of the given prism and angle of minimum deviation. Hence calculate the refractive index of the material of the prism.
- 5. Adjust the grating for normal incidence using the spectrometer. Determine the wavelength of green, blue, yellow and red lines of mercury spectrum (The number of lines per metre length of the grating can be noted from the grating).
- 6. Draw the V-I characteristics of PN junction diode and determine its forward resistance and knee voltage from forward characteristics.
- 7. Draw the V-I characteristics of Zener diode and determine its forward resistance and knee voltage from forward characteristics. Also find break down voltage of the Zener diode from reverse characteristics.
- 8. Draw the input and transfer characteristic curves of the given NPN junction transistor in CE mode. Find the input impedance from input characteristics and current gain from transfer characteristics.
- 9. Draw the output and transfer characteristic curves of the given NPN junction transistor in CE mode. Find the output impedance from output characteristics and current gain from transfer characteristics.
- 10. Verify the truth table of logic gates AND, NOT, Ex-OR and NOR gates using integrated circuits.
- 11. Verify the truth table of logic gates OR, NOT, Ex-OR and NOR gates using integrated circuits.
- 12. Verify De Morgan's first and second theorems.



