Company Overview

Outlogix D.O.O. and its wholly owned subsidiary Radiosoft D.O.O. have been developing cutting edge technology solutions for variety of US based clients, ranging from small companies to some of the industry leaders like SpaceX, Xilinx, FAANG, National Instruments, and others. Being in business for over 20 years, our teams address some of the most challenging problems in digital communications, satellite communications, test and measurement industry, IoT and other fields. Thanks to our successes in the past, strong reputation and impressive track record, we participate in most demanding and interesting engineering projects that require novel approaches and innovative solutions.

Position Overview

We are interested in hearing from candidates with FPGA, ASIC, digital design or Embedded SW experience. Collaborate with a team of highly talented engineers who have created solutions for some of the most advanced technologies in the digital communication space today. You will be working on cutting edge solutions in the area of digital signal processing, digital communication, instrumentation, and test and measurement systems. You will work closely with cross functional teams to create system level solutions for the next generation products in the area of custom FPGA, ASIC and SoC design. If you are a self-motivated professional that is inspired by innovation and continuous self-improvement, you have come to the right place.

Specific Responsibilities:

- Participate in all phases of ASIC/FPGA design, verification, test and deployment
- Develop custom block and subsystem architectures and create specifications, requirements and test plans for RTL blocks targeted for FPGAs and/or ASICs
- Define and implement PS/PL interfaces utilizing industry standard interfacing
- Use HDL (VHDL, Verilog, System Verilog) or Simulink based tools to design logical and functional blocks and subsystems
- Perform algorithm design and implement optimal solution in hardware
- Optimize your designs for area, speed, and performance; analyze architectural trade-offs; perform timing closure and clock domain crossing optimizations
- Develop test benches and test cases for block-level functional verification, emphasizing bit-true, cycle true and self-checking
- Use scripting languages to achieve higher performance and build and test automation
- Run implementation tools such as Xilinx Vivado, or other partners tool suites including NI, Keysight and others

- Maintain the host environment and toolsets required to implement designs
- Interface to and program custom complex DMA engines (using C or RUST)
- Work together with cross functional teams and debug RTL, software and firmware on hardware or emulation platforms
- Proactively collaborate with other remote team members
- Participate in online meetings with customers and colleagues
- Apply proper coding, testing and documentation practices in your daily work
- Flexible work schedule, remotely or from our Belgrade/Terazije office

Required Experience:

- BSEE/CE/CS/EE, MS is a plus
- 3+ years of experience in SystemVerilog, Verilog or VHDL RTL design
- Ability to map ideas, concepts and specifications to working RTL
- Ability to prioritize work, complete multiple tasks and work towards deadlines
- Work rapidly in both independent and team setting
- Self driven, proactive, quick learner, self-initiative
- Exceptional understanding of debug and testing techniques
- Understanding of hardware, processor and SoC architectures, DMA engines
- Excellent English communication skills (written and verbal)
- Strong attention to detail, highly organized, computer literate
- Ability to work well in a fast-paced professional environment
- Willingness to learn and adopt new and emerging technologies
- Willingness to learn state of the art FPGA design flows

Valuable Experience:

- Knowledge of 3G, 4G, 5G, WiFi, BLE, QAM, OFDM, CDMA
- Simulators, synthesis and static timing analysis, timing closures, clock domain crossing techniques, FPGA tools (Vivado, Quartus)
- MATLAB, Python, RUST, C/C++, Tcl, Make, Bash, FreeRTOS/BareMetal, Linux
- JIRA, Confluence, Bitbucket, Bamboo, Jenkins, GitLab, etc.
- Digital signal processing and digital communication block designs, verification and integration - FEC, MultiRate Filtering, FFTs, Recovery Loops
- Experience with cutting edge ADCs and DACs
- Experience with industry standard interfaces, protocols, and architectures: DDR, AMBA AXI, APB, Avalon, PCIe, JESD204 etc.
- Experience with Bash, GIT, SVN
- MAC, PHY Layer processing in a modem; Simulink, System Generator.
- Networking, Ethernet , UDP, TCP/IP, Wireshark
- Baremetal and Embedded Linux Driver Development
- Labview, Labview FPGA; Labview GUIs. TestStand.

- Familiarity with cutting egde test and measurement euquipment – RTSA, DSO

We Offer:

- Permanent employment, private health insurance and additional benefits
- Flexible hours, work from home or on site
- Work on projects for BigTech/FAANG and other tech leaders. Your work will end up in worlds leaders' products that will have global impact
- Join rapidly expanding team whose founding team members have valuable experience and impressive track record
- Highly competitive pay; dynamic and interesting job
- Professional growth environment; we keep pushing the technology frontiers
- Challenging career opportunities for skilled, highly motivated software engineers
- Job Type: Full-time, All Remote