

# Transistor Theory

# Classroom Notes

BJT, MOS

Biasing, Gain, Output Resistance, Cascode

A	α	Alpha	a
B	β	Beta	b
Γ	γ	Gamma	γ
Δ	δ	Delta	d
E	ε	Epsilon	ε
Z	ζ	Zeta	z
H	η	Eta	ē
Θ	θ	Theta	th
I	ι	Iota	i
K	κ	Kappa	k
Λ	λ	Lambda	l
M	μ	Mu	m
N	ν	Nu	n
Ξ	ξ	Xi	x
Ο	ο	Omicron	o
Π	π	Pi	p
Ρ	ρ	Rho	r
Σ	σ	Sigma	s
Τ	τ	Tau	t
Υ	υ	Upsilon	u
Φ	φ	Phi	ph
Χ	χ	Chi	ch
Ψ	ψ	Psi	ps
Ω	ω	Omega	o

## Book Outline - Microelectronics Chits - Jaeger

Device Electronics ch2  $q = 1.06 \times 10^{-19} \text{ C}$

$\rho$  resistivity  $\Omega \text{ cm}$

$\sigma$  conductivity  $(\Omega \text{ cm})^{-1}$

$j = Qv$  drift current = Charge density  $\times$  velocity  $\text{C/cm}^2 \times \text{cm/s} = \text{A/cm}^2$

$n_i$  intrinsic carrier density  $\text{cm}^{-3}$   $10^{10} \text{ cm}^{-3}$  intrinsic Si

$n$  density of free electrons  $n = n_i$  for intrinsic material  $\text{cm}^{-3}$

$n_i^2 = BT^3 e^{-\frac{E_g}{kT}} \text{ cm}^{-6}$   $n_i^2 = 10^{20} \text{ cm}^{-6}$  For Si @ STP

$E_g$  bandgap energy eV  $1.12 \text{ eV}$  For Si

$k$  Boltzmann's constant  $8.62 \times 10^{-5} \text{ eV/K}$

$T$  Absolute temp K

$B$  material parameter  $1.06 \times 10^{31} \text{ K}^{-3} \text{ cm}^{-6}$  For Si

$P$  hole density  $\text{cm}^{-3}$   $= n_i$  for intrinsic material

$n = n_i = P$  intrinsic,  $p n = n_i^2$  intrinsic or not

$V$  carrier drift velocity  $\text{cm/s}$   $V_{sat} = 10^7 \text{ cm/s}$  (in  $VV > 3 \times 10^4 \text{ V/cm}$ )

$M$  mobility  $M_p$  hole  $500 \text{ cm}^2/\text{Vs}$   $M_n$  electron  $1350 \text{ cm}^2/\text{Vs}$  } intrinsic Si

$M = \frac{\sigma}{qn} = \frac{qen}{qn} = \frac{en}{q}$

$N_D$  donor impurity concentration atoms/ $\text{cm}^3$

$N_A$  acceptor impurity concentration atoms/ $\text{cm}^3$

n-Type material  $N_D > N_A$   $p = \frac{n_i^2}{n}$   $n = \frac{(N_D - N_A) + \sqrt{(N_D - N_A)^2 + 4n_i^2}}{2}$

p-Type material  $N_A > N_D$   $n = \frac{n_i^2}{p}$   $p = \frac{(N_A - N_D) + \sqrt{(N_A - N_D)^2 + 4n_i^2}}{2}$

conductivity ( $N_D > N_A$ )  $\sigma \approx qn$  ( $N_D - N_A$ )  $= qM_n n$   $(\Omega \text{ cm})^{-1}$

( $N_A > N_D$ )  $\sigma \approx qp$  ( $N_A - N_D$ )  $= qM_p p$   $(\Omega \text{ cm})^{-1}$

diff  $J_{diff} = (+, -q) D_{pn} \left( \frac{\partial n}{\partial x} \right) = -q D_{pn} \frac{\partial p}{\partial x}$  A/ $\text{cm}^2$

$V_T = \frac{D_{n,p}}{M_{n,p}} = \frac{kT}{q}$  Einstein's Relationship eV/e = V

Thermal Voltage

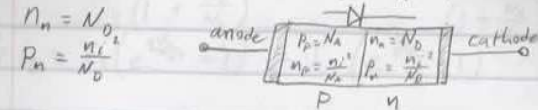
$J_{n,p}^T = q M_{n,p} n E + q D_{n,p} \frac{\partial n}{\partial x} = q M_{n,p} n (E + V_T \frac{\partial \ln n}{\partial x})$  Total A/ $\text{cm}^2$

$\nabla \cdot (\epsilon E) = \rho$  Gauss' Law  $\epsilon = \text{permittivity (F/cm)}$   $E = \text{electric field (V/cm)}$   $\rho = \text{charge density (C/cm}^3\text{)}$

## Device Electronics Ch3

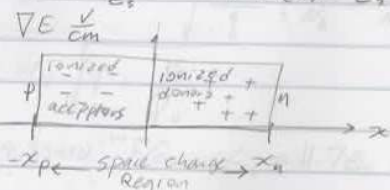
$P_p = N_A$  Positive carriers  $p_{side} = \text{acceptor concentration}$

$n_p = \frac{n_i^2}{N_A}$  Negative carriers  $p_{side} = \frac{(\text{intrinsic carrier density})^2}{\text{acceptor concentration}}$



Gauss' Law - Region of space charge  $\rho_c \text{ cm}^{-3}$

$$\nabla \cdot E = \frac{\rho_c}{\epsilon_s} \rightarrow E(x) = \frac{1}{\epsilon_s} \int \rho(x) dx$$



$E(x) \leftarrow$   $\leftarrow$  hole diffusion  $\leftarrow$  electron diffusion  $\leftarrow$  hole drift  $\rightarrow$  electron drift

unconnected diode  
Diffusion = -Drift  
for  $e^-$  and holes

Charge neutrality  $q N_A x_p = q N_D x_n$

$q$  - charge of  $1e^-$  C  
 $N_{A,D}$  - Acceptor, Donor Concentrations  $\text{cm}^{-3}$

$x_{p,n}$  - distance from metallurgical junction

Junction Potential - difference in internal chemical potentials from n to p side

$$\Phi_j = - \int E(x) dx \text{ Volts}$$

$$\text{also } \Phi_j = V_T \ln \left( \frac{N_A N_D}{n_i^2} \right)$$

Depletion Region

$$\text{Forward bias } W_{do} = (x_n + x_p) = \sqrt{\frac{2\epsilon_s}{q} \left( \frac{1}{N_A} + \frac{1}{N_D} \right) \Phi_j} \text{ meters}$$

$$E_{max} = \frac{2\phi_j}{w_{do}} = \frac{qN_A x_p}{\epsilon_s}$$

Partial widths of depletion zone

$$x_n = \frac{w_{do}}{\left(1 + \frac{N_A}{N_D}\right)} \quad x_p = \frac{w_{do}}{\left(1 + \frac{N_D}{N_A}\right)}$$

Diode

$$I_D = I_s \left( e^{\frac{qV_{AHT}}{kT}} - 1 \right) = I_s \left( e^{\frac{V_D}{V_T}} - 1 \right)$$

Reverse Bias

$$V_j = \phi_j + V_R$$

$$w_d = (x_n + x_p) = \sqrt{\frac{2\epsilon_s}{q} \left( \frac{1}{N_A} + \frac{1}{N_D} \right) (\phi_j + V_R)}$$

where  $w_d = w_{do} \sqrt{1 + \frac{V_R}{\phi_j}}$

Permittivity  $\epsilon_0 = 8.85 \times 10^{-14} \text{ F/cm}$   $\epsilon_s = 11.7\epsilon_0$

Junction Capacitance  $C_j = \frac{dQ_n}{dV_R} = \frac{C_{j0}}{\sqrt{1 + \frac{V_R}{\phi_j}}}$ ,  $C_{j0} = \frac{\epsilon_s A}{w_{do}}$

reverse bias

Forward bias  $C_D = \frac{dQ_D}{dV_D} = \frac{(I_D - I_s) \tau_F}{V_T}$ ,  $Q_D = I_D \tau_F$

(diffusion cap)

## Device Electronics

### Rectifier Circuits

$$\frac{1}{2} \lambda \quad V_{dc} = V_p - V_{on} - R_L = \infty$$

Voltage DC max =  $V_{peak} - V_{diode}$  on

Discharge interval  $V_D(t) = (V_p - V_{on}) e^{-\frac{t}{RC}}$

Ripple  $V_r = (V_p - V_{on}) - (V_p - V_{on}) e^{-\frac{t - \Delta t}{RC}}$

$$V_r = (V_p - V_{on}) \left[ 1 - e^{-\frac{t - \Delta t}{RC}} \right]$$

$$V_r \approx (V_p - V_{on}) \left[ 1 - e^{-\frac{t}{RC}} \right] \approx \frac{(V_p - V_{on}) T}{RC}$$

$T = \text{period on } t \text{ axis}$

$$I_{dc} = \frac{V_p - V_{on}}{R_L}$$

$$\Delta V = \frac{I_{dc}}{C} T \quad \text{linear capacitive discharge}$$

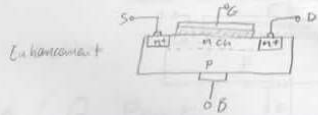
$$I_P = I_{dc} \frac{2T}{4T} \quad \text{repetitive peak current}$$

$$I_{sc} = \omega C V_P = 2\pi(60) \times \text{Capacitance} \times \text{Peak input voltage}$$

60 Hz

$$\Delta T = \frac{1}{\omega} \sqrt{\frac{I (V_p - V_{on})}{V_P}} = \frac{1}{\omega} \sqrt{\frac{2 V_r}{V_P}}$$

## Device Electronics



### MOSFETs

5 All Regions  $K_n = \mu_n C_{ox} \frac{W}{L}$  Transconductance

$K_n$  - Transconductance parameter  $= k_n' \frac{W}{L}$

$\mu_n$  - mobility of carriers (electrons)

$C_{ox}$  - capacitance per unit area  $= \frac{\epsilon_{ox}}{t_{ox}}$

$\epsilon_{ox}$  - oxide permittivity,  $\epsilon_{ox} = 3.9\epsilon_0$ ,  $\epsilon_0 = 8.85 \times 10^{-14} \text{ F/cm}$

$t_{ox}$  - oxide thickness

$\frac{W}{L}$  - width/length of channel

$Q' = -W C_{ox} (V_{ox} - V_{TN})$  Channel Charge

$Q'$  - charge per unit length

$V_{ox}$  - Voltage at Gate

$V_{TN}$  - Threshold N-ch

$i(x) = i = Q' v_x = [-W C_{ox} (V_{ox} - V_{TN})] [-\mu_n E_x]$  Channel Current

$i$  - current in channel

$v_x$  - Velocity of carriers

$i(x) = -\mu_n C_{ox} W (V_{gs} - V(x) - V_{TN}) \frac{dV(x)}{dx}$  Current

$V(x)$  - Voltage at position

$i_{DS} = \mu_n C_{ox} \frac{W}{L} (V_{gs} - V_{TN} - \frac{V_{DS}}{2}) V_{DS}$  Current

$i_{DS} = K_n (V_{gs} - V_{TN} - \frac{V_{DS}}{2}) V_{DS}$  For  $V_{gs} - V_{TN} \geq V_{DS} \geq 0$

$i_{DS} = \frac{K_n}{2} (V_{gs} - V_{TN})^2 (1 + \lambda V_{DS})$  For  $V_{DS} \geq (V_{gs} - V_{TN}) \geq 0$

$\lambda$  - Channel length modulation parameter

$V_{TN} = V_{T0} + \gamma (\sqrt{V_{gs} + 2\phi_F} - \sqrt{2\phi_F})$  No Body Effect Body Effect

$V_{T0}$  - zero substrate bias value for  $V_{TN}$  (V)

$\gamma$  - body effect parameter ( $\sqrt{V}$ )

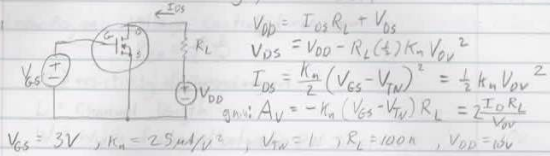
$2\phi_F$  - Surface potential parameter (V)  $0.3 \leq 2\phi_F \leq 1$

$V_{SB}$  - Voltage Source - Body

$K' = \frac{q^2 \mu_n N_A}{4\pi \epsilon_0 \epsilon_s} = 200 \mu A/V^2$ ,  $V_{T0} = 0.6V$ ,  $V_{TN} = 0.75V$ ,  $K_n = K' \frac{W}{L}$ ,  $\gamma = 0.5 \text{ V}^{1/2}$

## Device Electronics

### MOSFET Biasing / Q Point



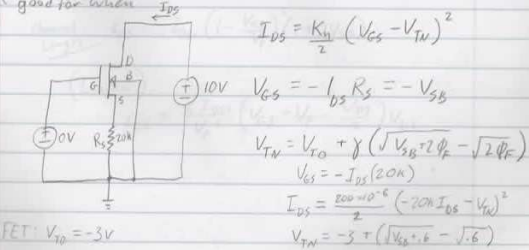
$$I_{DS} = \frac{K_n}{2} (V_{GS} - V_{TN})^2 = \frac{2.5 \times 10^{-6}}{2} (3 - 1)^2 = 50 \mu A$$

$$V_{DD} = 50 \mu A (100k) + V_{DS}$$

$$10V = V_{DS} + 5 \quad \text{saturation / small signal}$$

$$V_{DS} = 5V > V_{OV} = 2V \quad \text{condition for saturation}$$

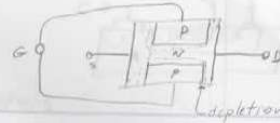
(assuming you want high gain and low currents)



FET:  $V_{T0} = -3V$   
 $\gamma = 1V$   
 $2\phi_F = .6V$   
 $K_n = 200 \mu A/V^2$

## Device Electronics

### JFET



Linear Region: Voltage controlled resistor

$$R_{CH} = \frac{\rho}{L} \frac{L}{W}$$

$\rho$  = resistivity of channel region

$L$  = channel length

$W$  = width of channel between PN junctions

NCH: S- D+

Gate: - To pinch off

+ To allow more current

Constant Current device:  $I_{DS}$  depends linearly on  $V_{GS}$

I.V.: (saturation)

$$I_{DS} = \frac{K_n}{2} (V_{GS} - V_P)^2 = \frac{K_n}{2} (-V_P)^2 \left( 1 - \frac{V_{GS}}{V_P} \right)^2$$

$$I_{DSS} = \frac{K_n}{2} V_P^2$$

$$V_P \text{ typical } -2.5 \pm 0.2V$$

(Sat)

$$I_{DS} = I_{DSS} \left( 1 - \frac{V_{GS}}{V_P} \right)^2 (1 + 2V_{DS})$$

(Linear)

$$I_{DS} = \frac{2 I_{DSS}}{V_P^2} (V_{GS} - V_P - \frac{V_{DS}}{2}) V_{DS}$$

## Device Electronics

### Bipolar Junction Transistors



Forward Characteristics

$$\text{Collector: } I_C = I_S \left( e^{\frac{V_{BE}}{V_T}} - 1 \right) = I_E$$

$I_S$  = Saturation current

$$e = 2.71828$$

$V_{BE}$  = Volt Base-Emitter

$$V_T = .025V \text{ for Si @ } 300K$$

$$\text{Base: } I_B = \frac{I_E}{\beta_F} = \frac{I_S}{\beta_F} \left( e^{\frac{V_{BE}}{V_T}} - 1 \right)$$

$\beta_F$  = Forward Gain

$$\text{Emitter: } I_E = I_C + I_B = \left( I_S + \frac{I_S}{\beta_F} \right) \left[ e^{\frac{V_{BE}}{V_T}} - 1 \right]$$

$$= I_S \left( \frac{\beta_F + 1}{\beta_F} \right) \left[ e^{\frac{V_{BE}}{V_T}} - 1 \right]$$

$$= \frac{I_S}{\alpha_F} \left[ e^{\frac{V_{BE}}{V_T}} - 1 \right]$$

$$\text{where } \alpha_F = \frac{\beta_F}{\beta_F + 1}, \beta_F = \frac{\alpha_F}{1 - \alpha_F}$$

$$\alpha_F = \frac{I_C}{I_E} \text{ Ratio of Collector-to-Emitter currents}$$

Full Transport Model - forward and reverse

$$\text{NPN } \left\{ \begin{aligned} I_C &= I_S \left[ e^{\frac{V_{BE}}{V_T}} - e^{\frac{V_{BC}}{V_T}} \right] - \frac{I_S}{\beta_R} \left[ e^{\frac{V_{BC}}{V_T}} - 1 \right] \\ I_B &= \frac{I_S}{\beta_F} \left[ e^{\frac{V_{BE}}{V_T}} - 1 \right] + \frac{I_S}{\beta_R} \left[ e^{\frac{V_{BC}}{V_T}} - 1 \right] \\ I_E &= I_S \left[ e^{\frac{V_{BE}}{V_T}} - e^{\frac{V_{BC}}{V_T}} \right] + \frac{I_S}{\beta_F} \left[ e^{\frac{V_{BE}}{V_T}} - 1 \right] \end{aligned} \right.$$

PNP: Reverse all subscripts i.e.  $I_C = I_S \left[ e^{\frac{V_{BC}}{V_T}} - e^{\frac{V_{BE}}{V_T}} \right]$

$$\text{General: } I_S = 10^{-15} A, V_A = 50V, \beta_F = 100, \beta_R = 1, V_{BE} = .7V$$



Early Effect: $V_A$		
		$i_c = I_s \left[ e^{\frac{V_{BE}}{V_T}} \right] \left[ 1 + \frac{V_{CE}}{V_A} \right]$ $\beta_F = \beta_{FO} \left[ 1 + \frac{V_{CE}}{V_A} \right]$ $i_B = \frac{I_s}{\beta_F} \left[ e^{\frac{V_{BE}}{V_T}} \right]$
Base-Emitter	Base-Collector	
Forward Bias	Forward Bias	Reverse Bias
Forward Bias	Saturated (closed switch)	Forward-Active (Linear Amplifier)
	$\frac{V_{BE}}{V_T}$	$\frac{V_{BC}}{V_T}$
Reverse Bias	Reverse-Active (poor amplifier)	Cutoff (open switch)
	$\frac{V_{BE}}{V_T}$	$\frac{V_{BC}}{V_T}$
Reverse Characteristics		
		$I_C = -\frac{I_s}{\alpha_R} e^{\frac{V_{CE}}{V_T}}$ $I_E = -I_s e^{\frac{V_{CE}}{V_T}}$ $I_B = \frac{I_s}{\beta_R} e^{\frac{V_{CE}}{V_T}}$
Cutoff Characteristics		
		$I_C = \frac{I_s}{\beta_R}$ $I_E = -\frac{I_s}{\beta_F}$ $I_B = -\frac{I_s}{\beta_F} \cdot \frac{I_s}{\beta_R}$

### Amplifiers

**Output Resistance**

$\frac{V_A}{I_C} = \text{output resistance } r_o$

$V_A = \text{Early Voltage}$

"Output resistance is inversely to  $I_C$ "

"Performance degrades as current increases"

$i_c = I_s e^{\frac{V_{BE}}{V_T}} \left[ 1 + \frac{V_{CE}}{V_A} \right]$

Find  $V_{BE}$  for

- in active mode,  $V_{CE} = 5V$
- at edge of saturation
- deep in sat,  $\beta = 10$  (assume normal  $\beta = 100$ ) (assume  $V_{CE} = 0.3 \text{ sat}$  or  $0.2 \text{ deep sat}$ )

$I_C = \frac{10^{-5}}{1} = 5 \mu A$

$I_B = \frac{I_C}{\beta} = 0.05 \text{ mA}$

$= \frac{V_{BB} - V_{BE}}{10}$

a)  $0.05 = \frac{V_{BE} - 0.7}{10}$  results in impossible  $V_{BE}$

b)  $V_{CE} = 0.3$

To determine Q point

Assume: Active  $\rightarrow$  solve then check:

if not, assume:

Saturation:  $V_{CE} = 0.2V$

or  $\beta$  forced

Determine node voltages,  $\beta = 100$

Assume  $V_{BE} = 0.7$

$-4 + V_{BE} + I_E(3.3k) = 0$

$I_E = \frac{(4 - 0.7)V}{3.3k} = 1 \text{ mA}$

$V_E = 3.3V$

$I_C = 0.99 I_E = 0.99 \text{ mA}$

On the collector side:

$(0.99 \text{ mA})(4.7k) = 4.65 \text{ V}$

$V_C = 10 - 4.65 = 5.34$

$V_C - V_E = 5.34 - 3.3 = 2.04$

Changing  $V_B$  to 6 results in impossible  $V_{CE}$  so assume it's  $0.2V$ , saturated

### Bipolar Simplified Transport Models

#### Forward Active

Basic

$I_C = I_s e^{\frac{V_{BE}}{V_T}}$

$I_E = \frac{I_s}{\alpha_F} e^{\frac{V_{BE}}{V_T}}$

$I_B = \frac{I_s}{\beta_F} e^{\frac{V_{BE}}{V_T}}$

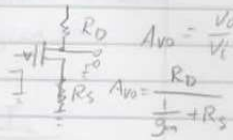
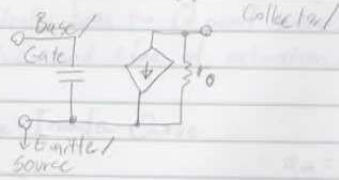
# MOSFETs



$$I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_t)^2$$

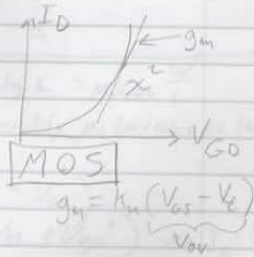
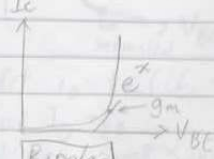
Process specific parameters simplify to  $K_n$

$$I_D = \frac{1}{2} K_n V_{GS}^2$$



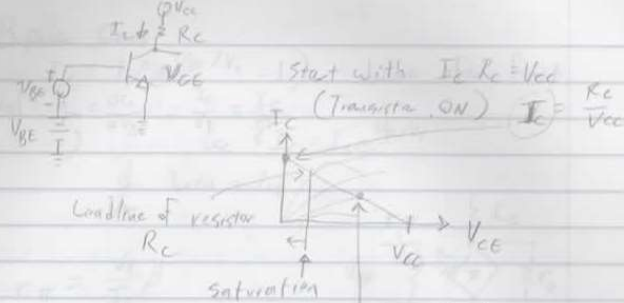
Product of  $g_m r_o = \text{constant}$

"Open Output Resistance"



$$\frac{dI_C}{dV_{BE}} = \frac{I_C}{\frac{kT}{q}} = g_m$$

"Gain is proportional to base current"



Ideally bias to Q point near  $\frac{1}{2}$  middle between  $V_{CC}$  and edge of saturation region

Bipolar Voltage Transfer Curve



$$g_m = \frac{I_C}{\frac{kT}{q}}$$

$$kT/q = V_t = 25mV$$

Concept  
Origin

Mosfet  $I_D = \frac{1}{2} K_n (V_{GS} - V_t)^2$

where  $K_n = \mu_n C_{ox} \frac{W}{L}$   
mobility  $\mu_n$  decreases as Temp increases

Find gain - Small Signal

$$I_D + i_D = \frac{1}{2} K_n ((V_{GS} - V_t) + v_{gs})^2$$

$$I_D + i_D = \frac{1}{2} K_n (V_{GS} + V_t)^2 + 2(V_{GS} - V_t)v_{gs} + v_{gs}^2$$

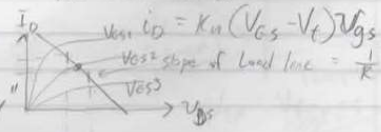
assume  $(V_{GS} - V_t) \gg v_{gs}$

if  $|v_{gs}| \sim 5$  to  $10mV$  "small signal"  $i_D = K_n (V_{GS} - V_t)v_{gs} + \frac{1}{2} K_n v_{gs}^2$

Linearized

$$g_m = \frac{\Delta i_D}{\Delta v_{gs}}$$

"small excursions only"



Small signal dbe@sonic.net

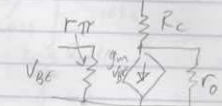
Bipolar Gain

$$I_C = I_S (e^{\frac{V_{BE}}{V_t}} - 1)$$

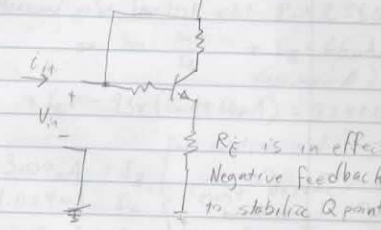
base xfa funct  
def:  $g_m = \frac{dI_C}{dV_{BE}} = \frac{I_C}{V_t} = \frac{I_C}{\frac{kT}{q}}$   
 $I_C = I_{SE} \frac{V_{BE}}{V_t}$

$\frac{d}{dV_{BE}}$  both sides

$$r_{\pi} = \frac{V_t}{I_C}$$



$$r_{in} = \frac{V_{in}}{I_{in}}$$



$$A_v = -g_m R_C$$

$$V_{CE} = V_{CC} - I_C R_C \rightarrow V_{CE} > V_{BE} - 0.4V$$

For AC analysis

$$V_{BE} = V_{BE} + v_{be}$$

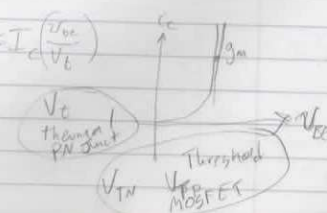
$$I_C = I_{sat} (e^{\frac{V_{BE} + v_{be}}{V_t}} - 1)$$

$$I_C = I_{sat} e^{\frac{V_{BE}}{V_t}} \cdot e^{\frac{v_{be}}{V_t}}$$

$$I_C = I_{CE} + \frac{I_C}{V_t} v_{be}$$

$$i_c = \frac{I_C}{V_t} v_{be}$$

$$g_m = \frac{I_C}{V_t}$$



## Experiment 2 - Transcribed to turn-in

Design the following circuit to have  $A_v \geq -100$

Assume: Use  $A_v = -g_m R_c$ ,  $I_c = 1\text{mA}$ ,  $R_B = 10\text{k}$ ,  $R_C = 2.55\text{k}$

$$g_m = \frac{I_c}{V_T} = \frac{1\text{mA}}{0.026\text{V}} = 38.46\text{mA/V}$$

Since requirement is  $A_v \geq -100$

$$100 \leq -g_m R_c \text{ so solve } R_c \geq 9.21\text{k}$$

$$100 = -g_m R_c$$

$$100 = -\frac{1\text{mA}}{0.026\text{V}} R_c$$

$R_c = 2.55\text{k}\Omega$ , Measured value for test ckt  $R_c = 2.550\text{k}$

Assume:  $\beta = 150 = \frac{I_c}{I_B}$  so  $150 = \frac{1\text{mA}}{I_B} \Rightarrow I_B = 6.6\mu\text{A}$  (assumed  $\beta$ )

Find  $R_E = -V_E / I_E \Rightarrow R_E = -9.3\text{V} / (1\text{mA} + 6.6\mu\text{A}) = 9.24\text{k}\Omega$

Assume  $V_{BE} = 0.7\text{V}$

Measured: Note that  $\frac{0.033\text{V}}{10.45\text{k}\Omega} = 3.04\mu\text{A} = I_B$   
 $\frac{10.07 - 0.7\text{V}}{2.55\text{k}\Omega} = 1.024\text{mA} = I_C$   
 $\frac{10 - 0.656}{2.55\text{k}\Omega} = 1.015\text{mA} = I_E$   
 Must rotate because  $I_C \approx I_E$

But, Using measured  $I_C \approx I_E$ ,  $\frac{1}{\beta} \approx \frac{I_B}{I_E} \Rightarrow \beta = 333$

With  $R_c = 2.6\text{k}$  and  $I_c = 1\text{mA}$ ,  $V_{RC} = 2.6\text{V}$   
 abs Max peak  $V_{out}$

$$A_v = -g_m R_c, g_m = \frac{I_c}{V_T}$$

From: Whites, sdsmt.edu EC330

Recall  $\frac{1}{\beta} I_D \rightarrow \text{Small Signal} \rightarrow \frac{1}{\beta} I_D = \frac{nV_T}{I_D}$

Small Signal

Determine  $r_{\pi}$

Assume active mode

$$i_B = \frac{I_c}{\beta} V_{be} = \frac{g_m}{\beta} V_{be} = \frac{1}{\beta} \left[ I_c + \frac{I_c}{V_T} V_{be} \right]$$

$$i_B = \frac{I_c}{\beta} V_{be} = \frac{g_m}{\beta} V_{be}$$

$$r_{\pi} = \frac{V_{be}}{i_B} = \frac{\beta}{g_m}$$

Determine  $r_e$

$$r_e = \frac{V_{be}}{i_e} = \frac{V_{be}}{I_c} = \frac{V_T}{I_c}$$

the AC component of:  $i_e = \frac{I_c}{\alpha} = \frac{I_c}{\beta} V_{be}$

$$r_e = \frac{V_{be}}{i_e} = \frac{V_{be}}{I_c} = \frac{V_T}{I_c}$$

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$I_B$  & Input Resistance

$$i_B = \frac{I_c}{\beta} = \frac{1\text{mA}}{150} = 6.6\mu\text{A}$$

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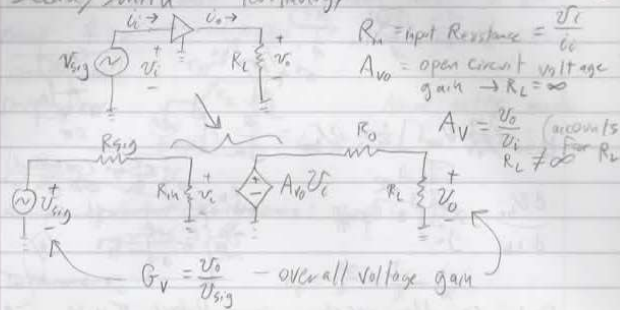
$$i_B = \frac{I_c}{\beta} = \frac{1\text{mA}}{150} = 6.6\mu\text{A}$$

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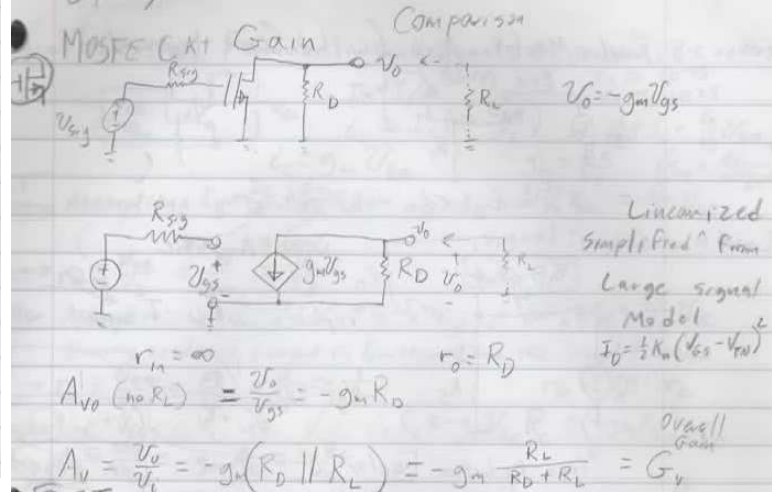


## Sedra/Smith Terminology

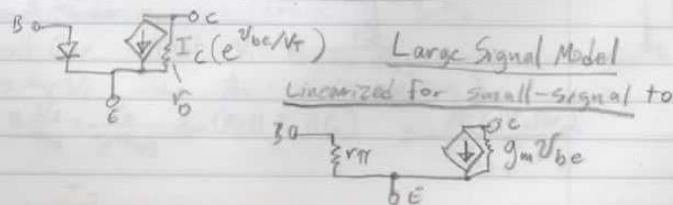
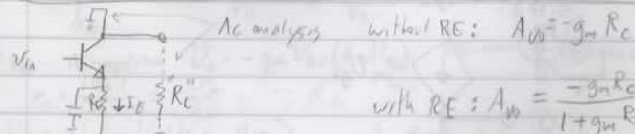
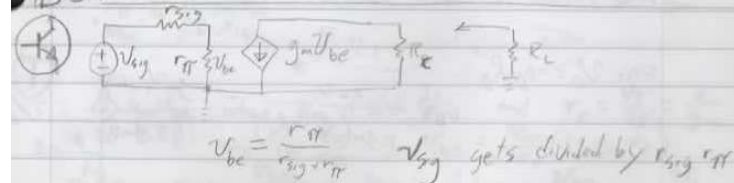


Common Base & Collector amps have high  $r_i$  and low  $r_o$

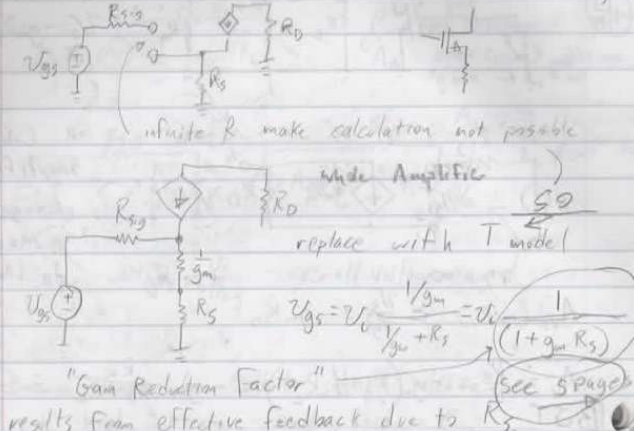
## Bipolar/



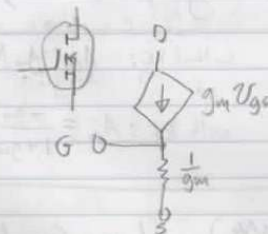
## BJT



## Real Mosfet Amp (with Source Degeneration $R_{sig}$ )



since  $v_o = -g_m R_D v_{gs}$   
 $v_o = \frac{g_m v_i}{g_m (1/g_m + R_S)} = \frac{-g_m v_i R_D}{1 + g_m R_S} = v_o$





**Bipolar / MOSFET Gain Comparison**

**MOSFET**

$V_{GS} \rightarrow V_{GS}$

$V_o = -g_m V_{gs}$

**Linearized Simplified Model**

Large signal Model

$r_{in} = \infty$

$r_o = R_D$

$A_{vo} (no R_L) = \frac{v_o}{v_{gs}} = -g_m R_D$

$A_v = \frac{v_o}{v_i} = -g_m (R_D || R_L) = -g_m \frac{R_D R_L}{R_D + R_L} = G_v$

**BJT**

$V_{GS} \rightarrow V_{GS}$

$V_o = -g_m V_{gs}$

$V_{be} = \frac{r_{\pi}}{r_{sig} + r_{\pi}} v_{sig}$  gets divided by  $r_{sig}, r_{\pi}$

**AC analysis**

without RE:  $A_{vo} = -g_m R_c$

with RE:  $A_{vo} = \frac{-g_m R_c}{1 + g_m R_E}$

**Large Signal Model**

Linearized for small-signal to

**Real Mosfet Amp (with Source Degeneration  $R_{sig}$ )**

infinite R make calculation not possible

**Wide Amplifier**

replace with T model

$V_{GS} \rightarrow V_{GS}$

$V_{GS} = V_o$

$\frac{1}{g_m} = \frac{V_o}{I_D}$

$I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{th})^2$

"Gain Reduction Factor"

results from effective feedback due to  $R_S$

since  $V_o = -g_m R_D$

$V_o R_D = \frac{g_m V_i}{g_m (g_m R_S)} = \frac{-g_m V_i R_D}{(1 + g_m R_S)} = V_o$

**Bipolar Transistor Formulae**

Small-Signal: B-E forward, B-C reverse

$I_C = I_S e^{V_{BE}/V_T}$

$I_S \approx 10^{-15}$

$V_T \approx 0.025$

$i_c \approx I_C (1 + \frac{v_{be}}{V_T})$  or  $i_c = I_C + \frac{I_C}{V_T} v_{be}$

$i_c = g_m v_{be}$

$g_m = \frac{I_C}{V_T}$  ( $g_m = \frac{I_C}{V_T}$ )

Signal: amplified base I

Input resistance Assumption:  $I_B = \frac{I_C}{\beta} + \frac{I_C}{\beta} \frac{v_{be}}{V_T}$   $\Rightarrow i_b = \frac{I_C}{\beta} \frac{v_{be}}{V_T} = \frac{g_m}{\beta} v_{be}$

Base  $\rightarrow r_{\pi} = \frac{v_{be}}{i_b} = \frac{\beta}{g_m} = \frac{V_T}{I_B}$

Emitter Assumption:  $i_e = \frac{I_C}{\beta} + \frac{I_C}{\beta} \frac{v_{be}}{V_T} \rightarrow i_e = I_C \frac{v_{be}}{V_T}$   $i_e = \frac{I_C}{V_T} v_{be} = \frac{I_C}{V_T} \frac{V_T}{I_C} i_e = i_e$

Showing resultant current in emitter from the amplification

$r_e = \frac{v_{be}}{i_e} = \frac{V_T}{I_C} = \frac{1}{g_m}$

$r_{\pi} = (\beta + 1) r_e$

assuming  $V_{CC} - V_{CE} - i_c R_c = V_{CC} - i_c R_c = -g_m v_{be} R_c = (-g_m R_c) v_{be}$

Gain  $A_v = \frac{v_o}{v_{be}} = -g_m R_c = \frac{-I_C R_c}{V_T}$

**BO**

$r_{\pi} = \frac{V_T}{I_B}$  and  $r_{\pi} = \frac{\beta}{g_m}$

**T**

$g_m v_{be}$  where  $g_m$  is still  $\frac{I_C}{V_T}$  and  $r_e = \frac{V_T}{I_C} = \frac{1}{g_m}$

$i_b = \frac{I_C}{\beta} \frac{v_{be}}{V_T}$  and  $i_e = \frac{V_{be}}{r_e} - g_m v_{be} = \frac{V_{be}}{r_e} (1 - \beta r_e)$

$v_{be} = \frac{V_{be}}{r_e} (1 - \beta r_e) = \frac{V_{be}}{r_e} (1 - \frac{\beta}{\beta + 1}) = \frac{V_{be}}{r_e} \frac{1}{\beta + 1}$

$v_{be} = \frac{V_{be}}{r_e} \frac{1}{\beta + 1}$

Output  $r_o$  Assumption  $V_o = -g_m v_{be} (R_c || r_o)$

$r_o = \frac{V_o}{i_c} = \frac{V_o}{-g_m v_{be}} = \frac{V_o}{-g_m \frac{V_{be}}{r_e} \frac{1}{\beta + 1}} = \frac{V_o}{-g_m \frac{V_{be}}{r_e} \frac{1}{\beta + 1}}$

$V_o = -(g_m V_{be}) (R_c || r_o)$ ,  $A_{vo} = -g_m (R_c || r_o)$

higher  $\beta \rightarrow$  higher  $r_{\pi}$

Gain:  $\approx -g_m R_c$

Overall Gain First  $V_i = V_{sig} \frac{r_{\pi}}{r_{sig} + r_{\pi}}$

$V_o = A_v V_i$  where  $A_v = -g_m (R_c || R_L || r_o)$

$G_v = \frac{V_o}{V_{sig}} = \frac{r_{\pi}}{r_{sig} + r_{\pi}} (-g_m (R_c || R_L || r_o)) = -\frac{\beta (R_c || R_L || r_o)}{R_{sig} + r_{\pi}}$

Small-Signal AC Analysis  
Common Emitter

$R_{in} = \frac{V_i}{i_b}$  where  $i_b = (1-\alpha)I_c = \frac{I_c}{\beta+1}$   
and  $i_e = \frac{V_i}{r_e + R_e}$   
 $R_{in} = (\beta+1)(r_e + R_e)$  recall  $r_e = \frac{V_T}{I_E}$   $R_e = \text{const}$

Adding Emitter Degenerative Resistance

$\Delta R_{in} = \frac{R_e \text{ with } R_o}{R_{in} \text{ without } R_e} = \frac{(\beta+1)(r_e + R_e)}{(\beta+1)r_e} = 1 + \frac{R_e}{r_e} \approx 1 + \beta \frac{R_e}{r_e}$

Gain  $V_o = -i_c R_c = -\alpha i_e R_c$

$A_v = -\alpha \frac{R_c}{r_e + R_e}$  Total emitter resistance  $r_e + R_e$   
So  $R_e$  adds to input resistance  
 $= -\frac{g_m R_c}{1 + R_e/r_e} \approx -\frac{g_m R_c}{1 + \beta R_e/r_e}$

So the factor  $1 + \beta R_e/r_e$  is how much  $R_e$  reduces gain

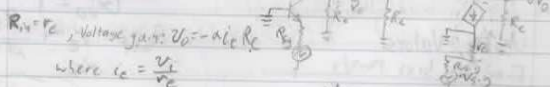
Adding Load Resistor  $A_v = A_{vo} \frac{R_L}{R_L + R_c}$

$R_o = R_c$  because in small-signal transistor  $\alpha \approx 1$  off (??)

Overall gain  $G_v = \frac{V_o}{V_{sig}} = \frac{R_{in}}{R_{in} + R_{sig}} \times (-\alpha) \frac{R_c R_L}{r_e + R_e}$  and  $\alpha \approx 1$

so  $G_v = -\beta \frac{R_c R_L}{R_{sig} + (\beta+1)(r_e + R_e)}$

Common Base



$R_{in} = r_e$ , Voltage gain:  $V_o = \alpha i_e R_c$   
where  $r_e = \frac{V_T}{I_E}$  where  $r_e = \frac{V_T}{I_E}$  (DC Bias point)

so  $A_v = \frac{V_o}{V_i} = \frac{I_c}{I_E} R_c = \frac{I_c}{I_E} R_c$  gain of amplifier proper

also, considering  $R_L$ ,  $A_v = g_m (R_c || R_L)$

then, considering  $R_{sig}$ ,  $G_v = \frac{R_{in}}{R_{in} + R_{sig}} g_m (R_c || R_L) = \frac{R_c R_L}{R_{sig} + r_e}$

since  $\alpha \approx 1$ , gain  $\approx$  ratio of output to input resistances

BJT

	$R_{in}$	$A_{vo}$	$R_o$	$A_v$	$G_v$
Common Emitter	$(\beta+1)r_e$	$-g_m R_c$	$R_c$	$-g_m (R_c    R_L)$ $-\alpha \frac{R_c    R_L}{r_e + R_e}$	$-\beta \frac{R_c    R_L}{R_{sig} + (\beta+1)r_e}$
(with $R_e$ )	$(\beta+1)(r_e + R_e)$	$-\frac{g_m R_c}{1 + g_m R_e}$	$\frac{R_c}{r_e + (R_c    R_L) + g_m R_e (R_c    R_L)}$	$-\frac{g_m R_c R_L}{r_e + R_e}$ $\approx -\frac{g_m R_c R_L}{r_e + R_e}$	$-\beta \frac{R_c    R_L}{R_{sig} + (\beta+1)(r_e + R_e)}$

Common Base	$r_e$ if $r_o = \infty$ or $\frac{r_e + R_e}{1 + \frac{R_e}{r_e} + (\beta+1)r_e}$	$g_m R_c$	$R_c$	$g_m (R_c    R_L)$ $\alpha \frac{R_c    R_L}{r_e}$	$\alpha \frac{R_c    R_L}{R_{sig} + r_e}$
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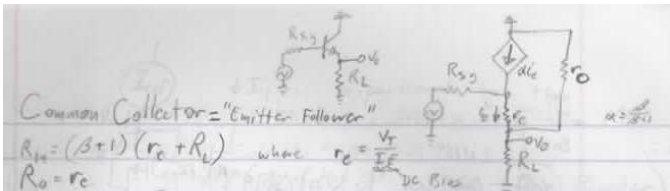
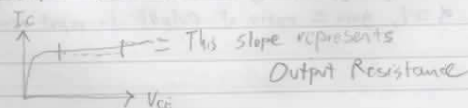
Common Collector Emitter Follower	$(\beta+1)(r_e + R_e)$ emitter is combined "load" and "emitter" resistances in parallel	1	$r_e$	$\frac{R_L}{R_L + r_e}$	$\frac{R_L}{R_L + r_e + \frac{R_{sig}}{\beta+1}}$
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Using Values

From DC bias point

$r_e = \frac{V_T}{I_E}$   $g_m = \frac{I_C}{V_T}$  since  $I_C \approx I_E$   $r_e \approx \frac{1}{g_m}$

Output Resistance: Related to Early Voltage



Common Collector = "Emitter Follower"

$R_{in} = (\beta+1)(r_e + R_e)$  where  $r_e = \frac{V_T}{I_E}$  DC Bias

$R_o = r_e$

$A_v = \frac{V_o}{V_i} = \frac{R_L}{R_L + r_e}$  so, if  $R_L = \infty$ ,  $A_{vo} = 1$

With  $V_i = \frac{R_{in}}{R_{in} + R_{sig}} (V_{sig})$  or  $V_i = V_{sig} \frac{(\beta+1)(r_e + R_e)}{(\beta+1)(r_e + R_e) + R_{sig}}$

and  $G_v = \frac{V_o}{V_{sig}} = \frac{V_i}{V_{sig}} A_v$  so

$G_v = \frac{(\beta+1)R_L}{(\beta+1)(r_e + R_e) + R_{sig}}$  neglecting  $r_e$

(with  $r_o$ :  $A_{vo} = \frac{r_o}{r_o + r_e}$ )

$r_{in} = (\beta+1)R_L$

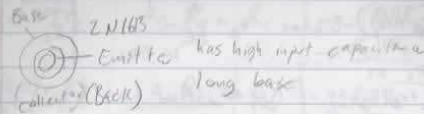
$G_v = \frac{r_e}{R_L + r_e + \frac{R_{sig}}{\beta+1}}$

$R_{out} = r_e + \frac{R_{sig}}{\beta+1}$

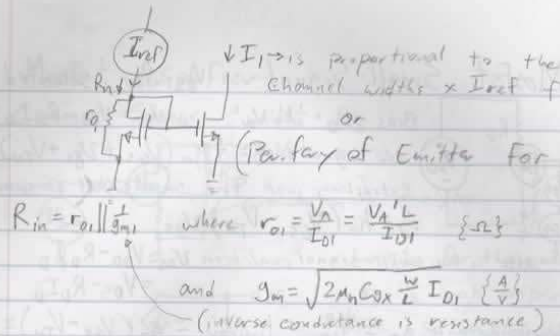
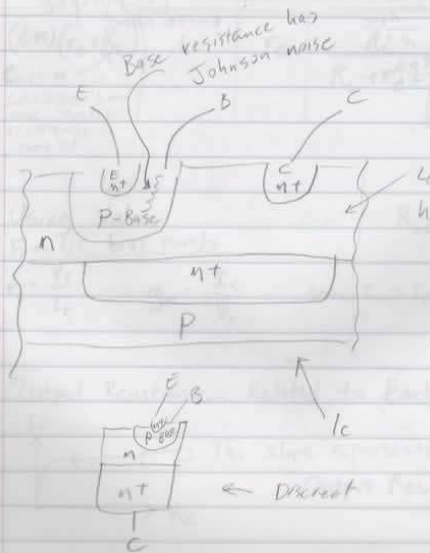
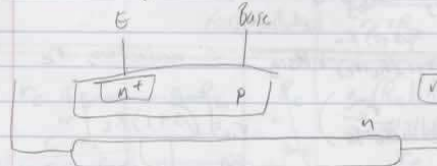
$R_{out} = r_e + \frac{R_{sig}}{\beta+1}$

$R_{out} = r_e + \frac{R_{sig}}{\beta+1}$

$R_{out} = r_e + \frac{R_{sig}}{\beta+1}$



Mobility of  $e^-$  is greater than  $p^+$



$$R_{in} = r_{o1} \parallel \frac{1}{g_m} \quad \text{where } r_{o1} = \frac{V_A}{I_{O1}} = \frac{V_A' L}{I_{O1}} \quad \{ \Omega \}$$

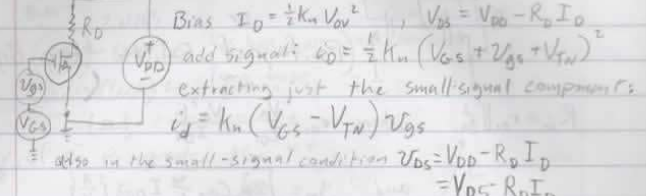
$$\text{and } g_m = \sqrt{2 \mu_n C_{ox} \frac{W}{L} I_{O1}} \quad \left\{ \frac{A}{V} \right\}$$

(inverse conductance is resistance)

$$\text{Since } r_{o1} \gg \frac{1}{g_m}, \quad R_{in} \approx \frac{1}{g_m}$$

section  
p. 290

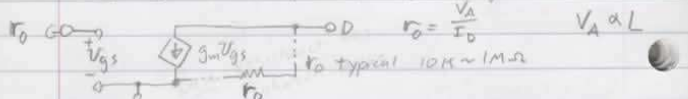
Mosfet Small-Signal  $V_{DS} > V_{OV}$ , Saturated



$$g_m \text{ transconductance } g_m \equiv \frac{d i_D}{d v_{gs}} = K_n V_{OV} = K_n (V_{GS} - V_{TN}) = \frac{2 I_D}{V_{OV}}$$

$$(in \text{ small-signal } i_D) \quad v_{gs} \quad \text{since } V_{OV} = \sqrt{\frac{2 I_D}{K_n}}$$

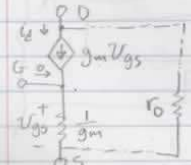
$$\text{Small-Signal Models } g_m = \sqrt{2 K_n \frac{W}{L} I_D}$$



$$A_{V_0} \equiv \frac{v_o}{v_{gs}} = -g_m R_D \quad \text{neglects } r_o$$

$$A_{V_0} = -g_m (R_D \parallel r_o) \quad A_V = A_{V_0} \frac{R_L}{R_L + r_o} \quad (R_L \text{ not shown})$$

alternate model  $G_V$  total gain, also considers input resistance



$$G_V = \frac{R_{in}}{R_{in} + r_{sig}} A_{V_0} \frac{R_L}{R_L + r_o}$$

$$v_o = -(g_m v_{gs}) (R_D \parallel r_o)$$

remember to  $\parallel r_o$  with  $R_D$  and any  $R_L$

$$A_{V_0} \text{ Intrinsic Gain } A_{V_0} = -g_m r_o = \frac{V_A}{V_{OV}} = \frac{V_A \sqrt{2 \mu_n C_{ox} \frac{W}{L}}}{\sqrt{I_D}}$$

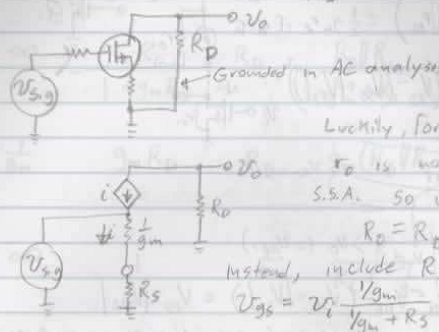
where  $g_m$  = see above  
 $r_o$  = see above



# MOS

## Mosfet Amplifier

### Common Source with $R_S$



Luckily, for

$R_D$  is not

S.S.A. So

$R_D = R_L$

instead, include  $R$

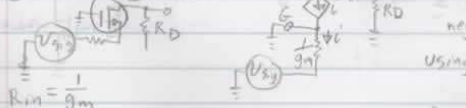
$$V_{GS} = V_i \frac{g_m}{g_m + R_S}$$

and since  $V_O = -i R_D$ ,  $i = \frac{V_i}{V_{GS} + R_S} = 2$

and  $A_{V_O} \equiv \frac{V_O}{V_i} = \frac{R_D}{V_{GS} + R_S}$

including an external  $R_L$   $A_V = \frac{R_D || R_L}{\frac{1}{g_m} + R_S}$

### Common Gate



$$R_{in} = \frac{1}{g_m}$$

Since  $V_O = -i R_D$ ,  $i = \frac{V_i}{V_{GS}}$

$$A_{V_O} \equiv \frac{V_O}{V_i} = g_m R_D$$

### Common Drain - "Source Follower"

$$R_{in} = \infty$$

$$A_{V_O} (R_L = \infty) = 1$$

$$A_V = \frac{R_L}{R_L + \frac{1}{g_m}} \quad (\text{voltage divider})$$

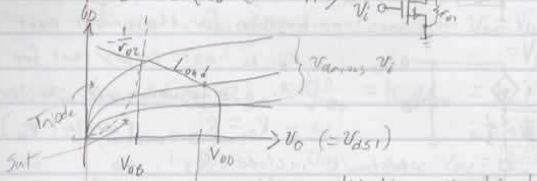
$$R_O = \frac{1}{g_m} \quad (R_L = \infty)$$

$$R_O = \frac{1}{g_m} || R_L$$

## MOS Amp, active load

$$A_V = -g_m (r_{D1} || r_{D2}) = \frac{1}{2} g_m r_D \text{ if } r_{D1} = r_{D2}$$

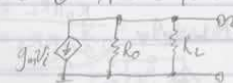
$$I = \frac{1}{2} \mu C_{ox} \frac{W}{L} (V_{DD} - (V_{GS} + |V_{TP}|))^2$$



$$V_{DS} = V_{DD} - (V_{GS} - |V_{TP}|) = V_{DD} - |V_{DS}|$$

OK to go near edge of sat, but stay in For smallsignal MOS

## Cascode & Active load



# MOS

	$R_{in}$	$A_{V_O}$	$R_O$	$A_V$	$G_V$
Common Source	$\infty$	$-g_m R_D$	$R_D$	$-g_m (R_D    R_L)$	$-g_m (R_D    R_L)$
Common Source with $R_S$	$\infty$	$\frac{g_m R_D}{1 + g_m R_S}$	$R_D$	$\frac{R_D    R_L}{\frac{1}{g_m} + R_S}$	same (because $R_{in} = \infty$ )
Common Gate	$\frac{1}{g_m}$	$g_m R_D$	$R_D$	$g_m (R_D    R_L)$	$\frac{R_D    R_L}{R_{in} + \frac{1}{g_m}}$
Source Follower	$\infty$	1	$\frac{1}{g_m}$	$\frac{R_L}{R_L + \frac{1}{g_m}}$	$\frac{R_L}{R_L + \frac{1}{g_m}}$ (same again, no-2 input)



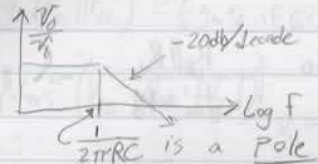
Zero  
+20 db/decade  
gain/freq

Pole  
-20 db/decade

3db is  
1/2 Power  
point

## Bode Plots

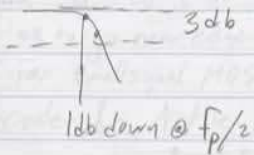
$$\frac{V_{out}}{V_{in}} = \frac{K}{s + RC}$$



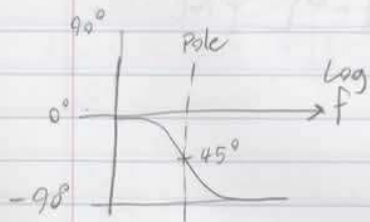
$$V_o = \frac{1}{j\omega C}$$

$$\tau = RC$$

If you take  $20 \log\left(\frac{a}{b}\right) \rightarrow \text{decibel}$   
V pole (@ 3db down) =  $\frac{1}{\sqrt{2}}$



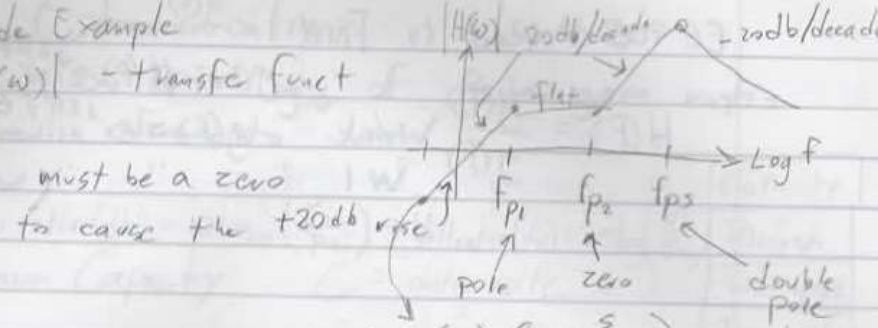
every 1db down is  
 $2f_p$



- Pole in denominator  
- Zeros in numerator

## Bode Example

$|H(\omega)|$  - transfer function



$$|H(\omega)| = \frac{K \left(1 + \frac{s}{\omega_z}\right) \left(1 + \frac{s}{\omega_{p2}}\right)}{\left(1 + \frac{s}{\omega_{p1}}\right) \left(1 + \frac{s}{\omega_{p3}}\right)^2}$$

