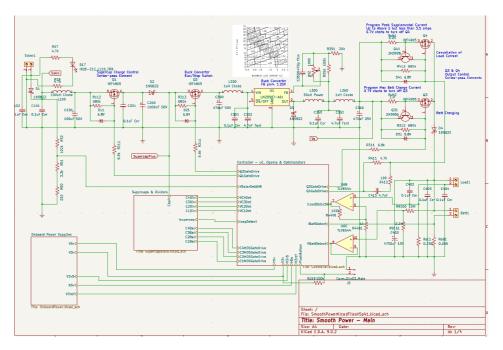
Smooth Power Plus

Leveraging a hybrid analog/digital power-control & monitoring system to solve or satisfy a real-world problem or need

Build the kit

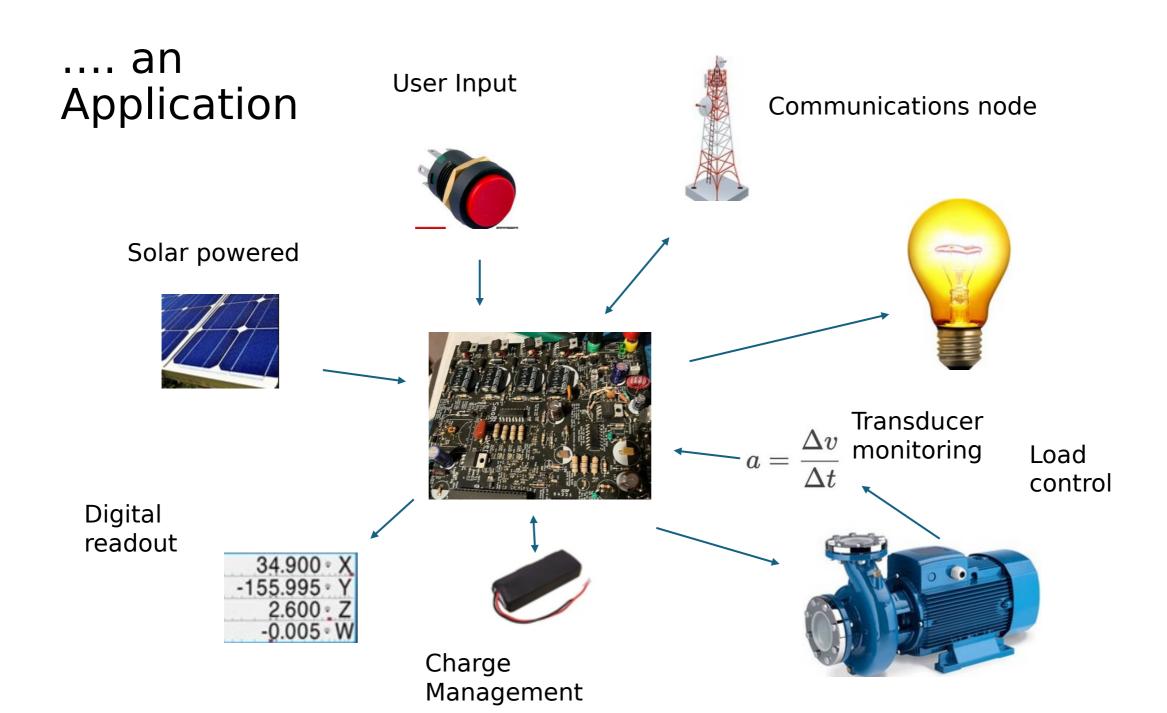


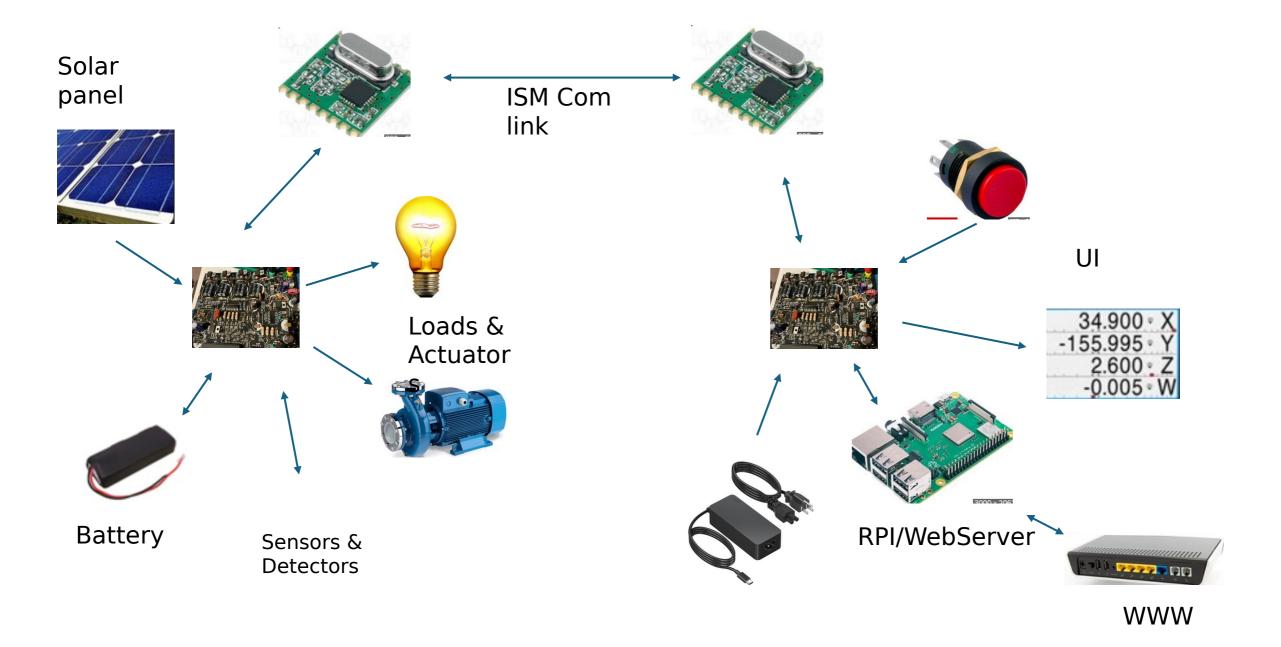


Tailor the code...

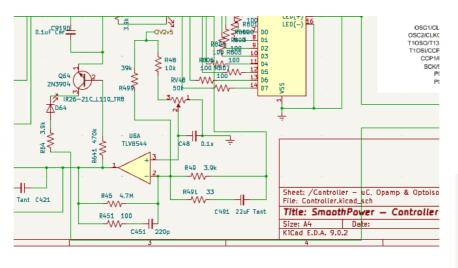
for....

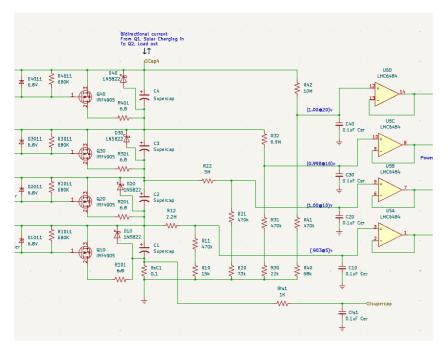
```
//MainProjectProgramV5p41d6.c - Batt & Sol Volts, timer on display
  //Bug recovery solution: had done a /**/ at end to comment out the EEPROM stiff and ha //Troubleshooter/MPLab output indicated "_MSDelay undefined symbol, but would not pop //Revamped Power Control, added logic to turn Q1 Q2 and Q3 on and off
 //EEProm subfunctions not implemented
  #pragma config FOSC = INTIO67 //internal oscillator block, port function on RA6 and R
  #pragma config BORV = 27, PWRT = ON //brown-out-Reset volt = 2.7V
  #pragma config WDTEN = ON //watchdog time abled //#pragma CONFIG3H<0> = 1
  #pragma config PBADEN = ON //don't disable. Set to on so pin 36 can be solar input vol
  #pragma config IESO = OFF //whatdoesitdo?
  #pragma config LVP = OFF //Low Voltage Programming. Need to turn OFF to use pin 38 (RBS)
  #pragma config DEBUG = OFF //whatdoesitdo?
  #pragma config HFOFST = OFF, MCLRE = OFF // fast start-up and MCLR pin disabled
v #include <stdio.h>
  #include <xc.h>
  #define RX RC7
  #define ldata PORTD //lcd data pins on PORTD
  #define rs PORTBbits.RB0 //rs of lcd on PORTB0
```





.... Do a DEEP DIVE





Battery & Supercapacitor stack management & balancing...

Native A/D converters connected to stack

Research possibilities

Ultra-low-power capabilities

