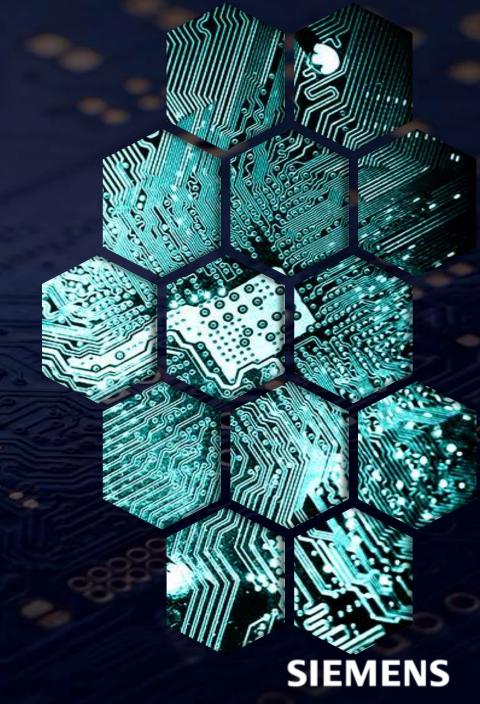
From Automation to Autonomy: Al's Evolving Role in Printed Circuit Board Design

Jerry Suiter

Senior Solution Architect Director Siemens Electronic Board Systems



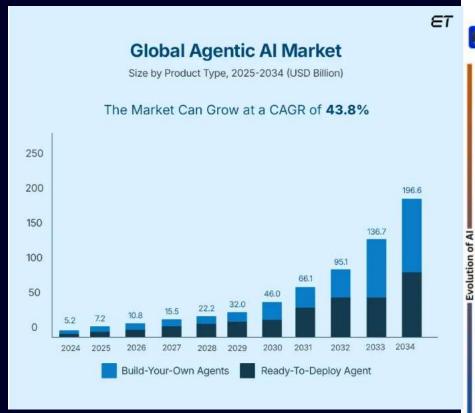
Jerry Suiter Bio

Jerry Suiter is a seasoned expert in Electronic Design Automation (EDA), with a BSE in Computer Engineering from the University of Central Florida. Since beginning his career at Intergraph in 1990, Jerry has spent 35 years at Siemens, where he leads the PCB design product portfolio, including Xpedition Designer, IOPT, Constraint Manager, Layout, Fablink, Drawing Editors, and Library Manager.

He also drives the User Experience for desktop tools and spearheads AI/ML innovation across the portfolio. His pioneering work in AI for PCB design has earned multiple patents and accolades, including the 2024 Siemens Digital Industries Invention of the Year and the 2025 Inventors Award in Digitalization and the Industrial Metaverse.



Accelerating Growth in Agentic Al Investment







Al Startups

Enabling emerging technologies in Al PCB Design



Placement and routing Plane generation Stack-up Verification



Requirements decomposition Component & block selection Schematic & BOM generation



Select components Layout schematic BOM generation



Design reuse Synthesize from code Auto-route



Auto-route Planes



Component research & creation Schematic wiring Layout Design reviews



State of Al in PCB design – Exploring the Al Hype

An example... (simple design)

Manual

Route time: ??? (assume 100%)

Al

Route time: Cancelled after 5hrs (59%)

Algorithmic

Route time: 11 sec (98%; opens easily fixed with plane mods)

Gartner's Al Hype Cycle shows technologies like GenAl and Al agents moving from the Peak of Inflated Expectations toward the Trough of Disillusionment as organizations realize limitations such as bias, hallucinations, and compliance challenges.

Forrester predicts that in 2026, enterprises will defer 25% of planned AI spending due to ROI concerns, signaling a correction from hype to value-driven adoption.

IEN: Engineering Reality Check: All excels at narrow tasks (e.g., predictive maintenance, optimization) but struggles with deep contextual reasoning and dynamic environments. Physical principles still matter for robust engineering decisions

Challenges applying Al within EDA

Model training

Digital twin model complexity (multi-domain, multi-discipline, multi-variable)

Wide variance of design types

Model scaling with complexity

Model usefulness increases with data

Synthetic models, ROM models, surrogate models, oh my...

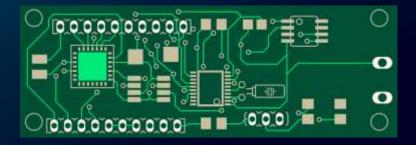
Learning on the fly during design vs. substantial training up-front

LLMs already trained by Amazon, Microsoft, NVIDIA, etc.

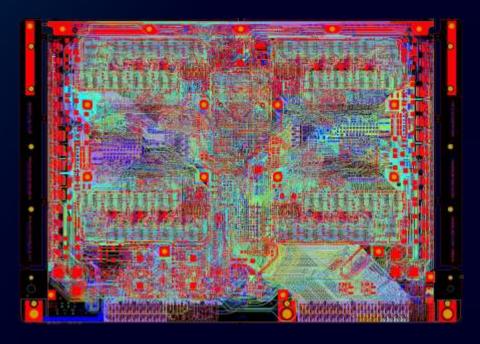
Need domain-specific fine-tuning

Vendor vs. user vs. open-source data

Need for training data will handicap startups



VS.



Challenges applying Al within EDA

IP security

What constitutes intellectual property (design, constraints, process data)?

Can decision-making parameters learned by Al be abstracted/anonymized from IP?

If not, is synthetic data accurate enough?

Permissions needed to extract models from IP

IP infringement – models with unknown origin

Design data in the cloud

Model management: user, design, project type, site, division, global, cloud





Challenges applying Al within EDA

Verifiable & accurate results

Garbage in...garbage out...

How will results be tested? When can testing stop?

How do we know if it's hallucinating, using old data, or plagiarizing?

Understanding of algorithms and models used

Understanding of the data sources used If the results are good, do you really care if Al was used?

Impact of export controls & geopolitics
Industrial grade AI: robust, reliable,
trustworthy, secure, accurate, verifiable,
usable, domain-specific

How accurate is and what are its limitations?

While our experiments have shown that suggestions are of the same or better quality than the average engineer, we can't give any assurance that it's error free. Like any engineer, may sometimes be wrong. We recommend taking the same precautions you take with work done by your engineers (Reviews, Audits, etc.)

- Example Tools:
 - Cadence Allegro and Mentor Graphics Xpedition are EDA tools that use Al to automate
 placement and routing, saving significant time while optimizing performance.
- Example Tool: Siemens Valor NPI uses Al to simulate manufacturing and predict yield,
 identifying possible failure points and helping designers optimize for higher reliability.

"The variability in AI results is often a feature, not a bug, designed to prevent repetitive outputs and foster creativity."

Gemini 2.5





Challenges applying AI within EDA

Adoption resistance

Many forms of automation incur resistance

Resistance to change; to AI; to replacement

Have to show real value to engineers & mgmt.

How is value defined?

Is it worth the compute time & cost?

Cost of usage (consumption, 3rd party engines)

The Future of Product Development Opinions are Split on the Destabilizing Effect of Al 35% of design and manufacturing Percentage of respondents who agree that AI is a threat companies report using AI to produce Strongly agree 16% informed design options. More than half Somewhat agree 25% of all companies report approaching or already achieving their goal of incorporating Neither agree nor disagree 19% Al into their companies, and 79% agree Somewhat disagree 24% Al will make their industry more creative. Strongly disagree 16% Source: Autodesk State of Design & Make Report



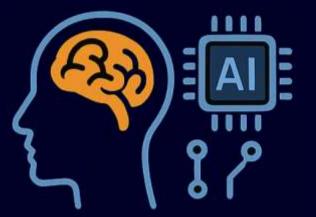
* Source: DALL-E image generator in ChatGPT 4



Challenges with AI Technology

First-of-its-kind innovations: Design paradigms that have no precedent

Problem: Al lacks the necessary experience to provide meaningful insights



The "Semantic Gap":

Problem: Translating high-level instructions (e.g., 'optimize impedance for this differential pair') into basic geometric operations

Combinatorial Explosion of Choices:

Problem: The number of possible geometric paths, layer changes, and via placements is astronomically large

Lack of High-Level Reasoning:

Problem: A human designer understands the why behind a geometric choice

Context Sensitivity:

Problem: The "best" geometric placement or routing for one design might be terrible for another

Possible Solutions



Reinforcement Learning (RL) and Generative Al

Knowledge Graph / Semantic Understanding

Reduced-Order Models & Neural Operators

Simulation-in-the-Loop

Human-in-the-Loop and Explainability



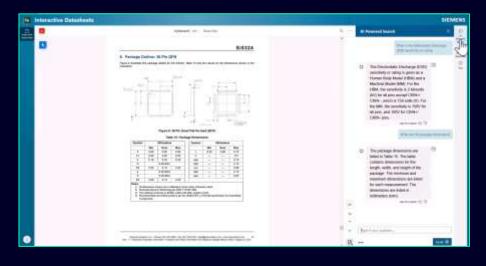
ENABLING INNOVATION

Al capabilities in place

Process Prediction in All Products

Fewer clicks required to access the right command at the right time





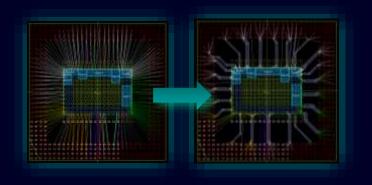
Smart Datasheets

- Natural language queries
- Guidance on effective prompting
- Delivered via PartQuest portal for first release

Product Support Copilot

Search multiple knowledge sources and receive humanlike responses for any query





Al-driven Auto Sketch Generation in i3D Integrator

Some example PCB design use cases

Optimized product

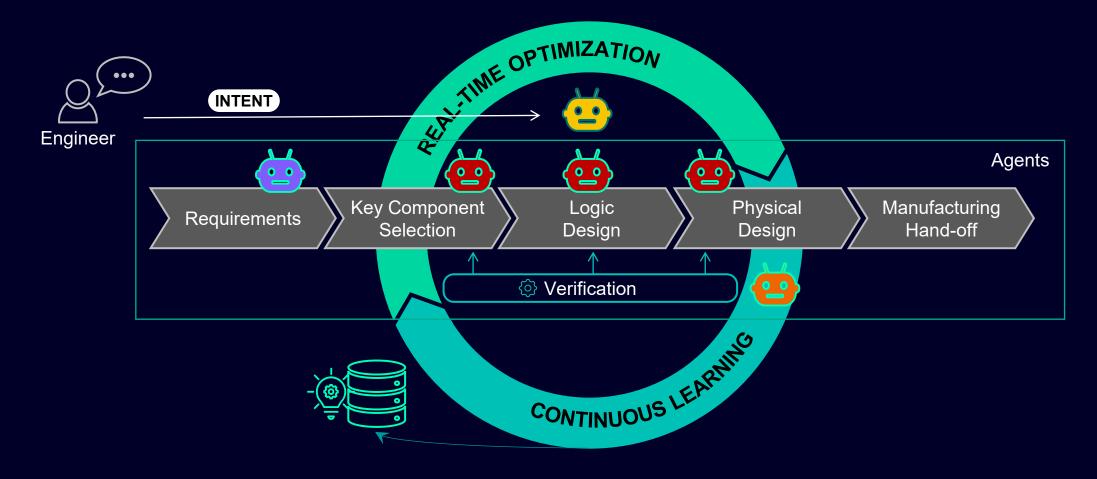
Mfg. prep. Verification **Architecture** Logic Layout Intent definition Component Laver Design space Assessment of Architecture & recommendation assignment exploration ideal Trade-off manufacturer requirements Supply chain Decoupling decomposition prediction optimization Predict management Constraint Design reuse Prioritization Component manufacturability Quote definition of results generation placement Library model Plane Root cause creation generation analysis Schematic Connection Dynamic connectivity unravelling verification Design reuse Failure Routing Stack-up/materials prediction selection

General

- Product support
- Script generation
- Report generation
- Process prediction
- Process execution
- Design agents



The Future of Al Assisted PCB Design













Agentic Simulation Validation

Based on the available simulation data. I've analyzed the eye diagram performance across multiple cases. Here's my detailed ranking

PERFORMANCE RANKING (Best to Worst)

3 1st Place: Case 1 (Full Swing Configuration)

Overall Score: 85/100

- Eye Height: 0.0119 V (79.1% of requirement) X
- Eye Width: 0.407 s (135.8% of requirement)
- · Swing Type: full
- Voltage Span: 0.0659 V
- Grid Resolution: 431 x 1,200 = 517,200 points
- . BER Range: -16 to 0 dil

Strengths:

- . Excellent timing performance with 35.8% margin
- High-resolution simulation grid
- · Full swing configuration provides maximum signal amplitude
- . Good statistical confidence with 517K data points

Weaknesses:

- . Eye height fails requirement by 20.9%
- . Needs amplitude improvement

3 2nd Place: Case_2 (Reduced Swing Configuration)

Overall Score: 70/100

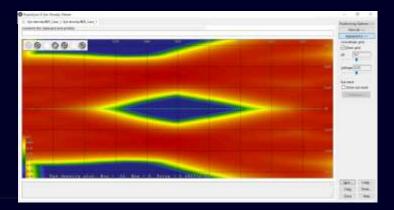
- Eye Width: 0.361 s (120.4% of requirement)
- Swing Type: Reduced
- Additional Metric: 13.643 (higher than Case: 1\ 8.769)

Strength:

- . Timing performance still passes with 20.4% margin
- · Reduced swing may offer better noise immunity

Westmesses

- · Significantly worse eye height (only 31.6% of requirement)
- · Reduced swing limits signal amplitude recovery potential



FRESH ANALYSIS: PF_EyeBER_Case_20p3in Results

SIMULATION PARAMETERS EXTRACTED

Based on the HyperLynx PowerScope simulation data, here are the key parameters for PF_EyeBER_Case_20p3in:

Core Simulation Metrics

Parameter	Value	Units
Simulation Area Size	1 × 0.0647286	time « voltage
Cell Size	0.00231481 = 5.38956e 05	time = voltage
Origin	0, -0.0323643	time, voltage
BER Range	16 to 0	de
Voltage Step	5.38956e 05	Vistep

Page Size: 10 *

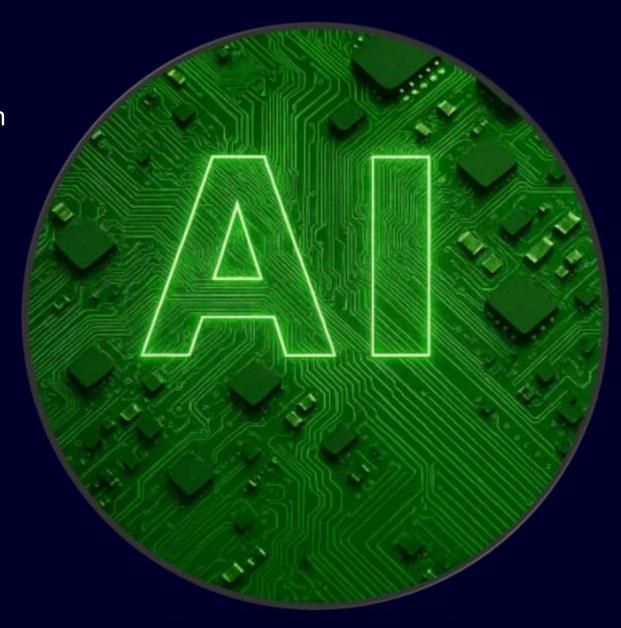
M DETAILED COMPARATIVE ANALYSIS

Key Performance Indicators:

Rank	Case	Eye Height	Eye Width	Voltage Span	Swing Type
8 1st	Case_1	0.0119V (79%)	0.4075 (136%)	0.0659V	Full
8 2nd	Case_2	0.00474V (32%)	0.361s (120%)	Unknown	Reduced
8 3rd	Case_51	Unknown	Unknown	0.0821V	Unknown

Summary

- Al still struggles to replace key parts of the design process.
- Al technology is evolving rapidly
- Excels at multi-step reasoning, Automated workflows, and information synthesis
- Al works best as a copilot, automating tasks and supporting designers, not replacing them.
- Understanding Al's capabilities and limitations is crucial for leveraging in EDA
- Al agents are an evolution that is rapidly showing potential in the automation of the design process





Thank you