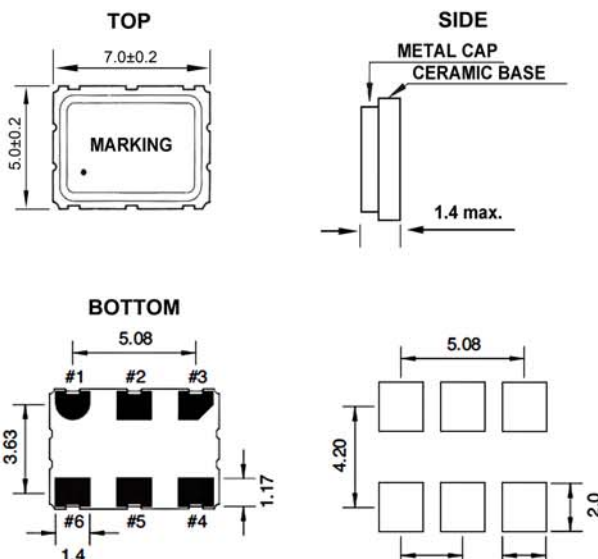


#### ● SPECIFICATION

Model	OSC-S7 LVPECL SERIES		
Power supply voltage	2.5V±5%	3.3V±5%	3.3V±5%
Output level	LVPECL		
Frequency range	15.000~160.000MHz	160.000~622.080MHz	
Storage temperature range	- 55°C ~ +125°C		
Operating temperature range	-10°C ~ +70°C , -40°C ~ +85°C		
Frequency stability	±25ppm ~ ±100ppm		
Current consumption	70mA(Max.)	135mA(Max.)	
Output Load	50Ω		
Output voltage level (V <sub>OH</sub> )	1.475V <sub>min.</sub>	2.275V <sub>min.</sub>	2.275V <sub>min.</sub>
Output voltage level (V <sub>OL</sub> )	1.095V <sub>max.</sub>	1.680V <sub>max.</sub>	1.680V <sub>max.</sub>
Duty cycle	45%~55%		
Enable input voltage(V <sub>IH</sub> )	1.750V <sub>min.</sub>	2.310V <sub>min.</sub>	2.310V <sub>min.</sub>
Enable input voltage(V <sub>IL</sub> )	0.750V <sub>max.</sub>	0.990V <sub>max.</sub>	0.990V <sub>max.</sub>
<b>RISE / FALL TIME</b>			
15.000~99.999MHz (ns)	1 Max.		
100.000~160.000MHz (ns)	0.5 Max.		
160.000~622.080MHz (ns)		0.7 Max.	
Phase Jitter RMS (12KHz~20MHz)	1ps Max.		
AGING	±3ppm/Year		

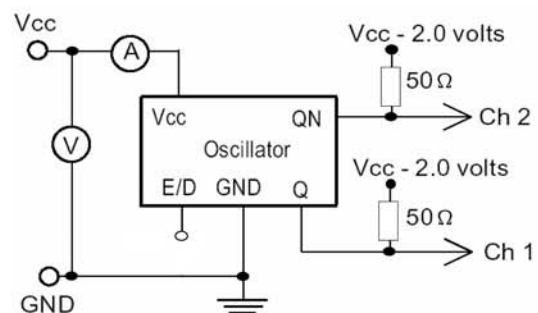
#### ● DIMENSION (mm)



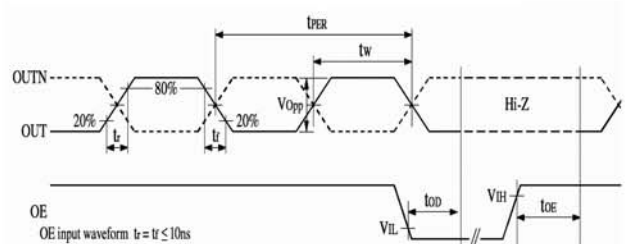
#1 E/D , #2 N.C. , #3 GND

#4 Q , #5 QN , #6 VDC

#### ● TEST CIRCUIT



#### ● OUTPUT WAVEFORM



DUTY1 = 100 \*  $t_w$  /  $t_{PER}$  (%) @ crossing point  
 DUTY2 = 100 \*  $t_w$  /  $t_{PER}$  (%) @ 50% waveform