

# Axiom Electronics PCBA Design for Manufacturability Guidelines

Section: 10.19

Revision: A

Revision Date: 2/14/13

DFM Subject: Copper Feature Edge Clearance

## DFM Requirement:

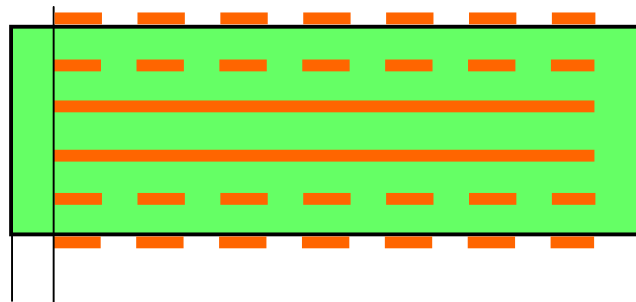
Copper feature to PCB edge clearance shall be 1.27mm (.050") minimum for V-scored and routed edges. Copper feature includes conductors (traces), plated-through-holes (vias), power and ground layers, test points, plating thieves, etc.

## DFM Impact:

Minor amounts of haloing, cracking and delamination may occur during V-score separation or edge routing, especially when Pb-free laminates are used. Copper features need to be far enough away from the edge to ensure that electrical shorts do not occur between copper layers. Electrical shorts can cause dielectric breakdown or CAF (conductive anodic filament) formation.

## DFM Details:

PCB Cross Section View



1.27mm for V-scored Edges and Routed Edges