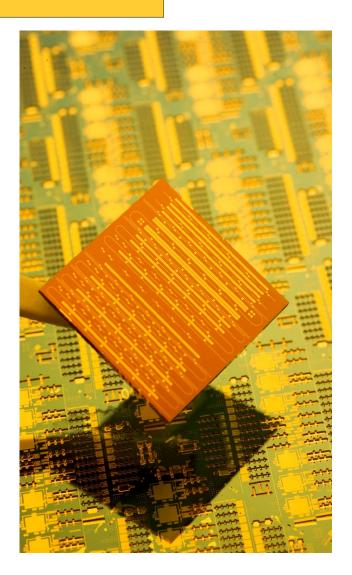
## AllianceATE Consulting Group, Inc.

AllianceATE is a semiconductor engineering and technology company that delivers services and software enabling SOC and RF chip design to High Volume Manufacturing (HVM).







Alliance ATE Consulting Group, Inc 3080 Olcott St. Suite 110C Santa Clara, CA 95054

Email: ken.meade@allianceATE.com

Phone: (408) 202 - 2582



**Conversion Tool** 



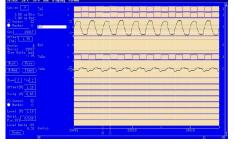
www.velocity CAE.com

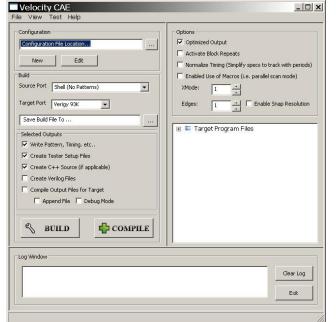
Brought to you by:



The Performance Leader in Test Engineering

VelocityCAE is a simple-to-use, low cost vector and timing generation tool developed by AllianceATE with efficient algorithms and an intuitive graphical user interface

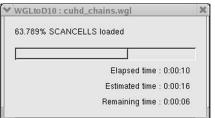




1
EDA Simulation
Files / Source
ATE Setup Files

.WGL, .STL .EVCD, etc.





2 Conversion & Program Generation

VelocityCAE Translation Engine



## Simulation-to-Test

**Linkage to Auto Program Generation Tools**Automatic **Rule Checks** 

Automatic Generation of Configurations from any simulation file

**Pin Masking** avaliable for all formats
Single or **Multiple Simulation File Processing** 

3 ATE Tester Format



Why VelocityCAE

Low Cost most cost effective vector translation on the market

Easy to Use 3 clicks away from a translation or test program

Fast & Efficient parallel scalable for fast vector computing

Live Technical Support

Fully Customizable fully customizable for specific applications

## **General Features**

Vector Generation Specific to the Diamond Series

Customizable Timing, Level, and Pattern block definitions

Autogenerate complete programs using reusable Test Libraries

General purpose pin masking and data override capabilities for all vector data

Reduce vector depth with automated repeat block recognition

uct, and DFT engineers, VelocityCAE is the simplest and most cost effective vector translation tool available today.

VelocityCAE takes EDA Simulation

files and translates them into com-

plete Diamond Series test programs.

Developed for everyone: Test, Prod-

