#### IAB2-7/8 setup guide

- 1- Cut off USB-Plus section.
- 2- Keep both Hub section together.
- 3- install 1/8 connectors in position J1, J2, J4, J5, J6, J7, J9 AND J10
- 4- Install 100uH inline coils or RF ferrit inline in position L1, L2, L4, L5, L6, L7, L9 and L10.

# Warning; Use the SQUARE hole on the PCB leaving the round hole available for the cable connection.

5- install 470ohm resistor in R1, folding the resistor toward center of the PCB to prevent it breaking off.

#### Warning; make certain the Resistor leads to touch anything.

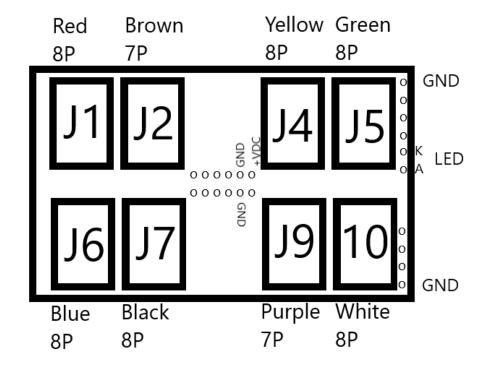
6- Install the LED in LED1, bending the leads ov the LED toward the edge of the enclosure.

#### Warning; make certain the LED leads to touch anything.

- 7- Install the 7 pins DIN cable Brown wire in L2 input round hole.
- 8- Install the 7 pins DIN cable Purple wire in L9 input round hole.
- 9- Install the 8 pins DIN cable Red wire in L1 input round hole. (alternate Mauve)
- 10- Install the 8 pins DIN cable Yellow wire in L4 input round hole. (alternate
- 11- Install the 8 pins DIN cable Green wire in L5 input round hole. (alternate
- 12- Install the 8 pins DIN cable Blue wire in L6 input round hole. (alternate
- 13- Install the 8 pins DIN cable Black wire in L7 input round hole. (alternate
- 14- Install the 8 pins DIN cable White wire in L10 input round hole. (alternate
- 15- Install both ground shieldcables to the corner located ground points.
- 16- Install the 8 pins DIN cable Brown wire to the VDC+ (Pin1) input in the center of the PCB on the same side as the LED1.
- 17- Install the 8 pins DIN cable Purple wire in ground (pin2) input in the center of the PCB on the same side as the LED1.
- 18- Install the 7 pins DIN cable Pink wire in ground (pin2) input in the center of the PCB on the opposite side as the LED1.
- 19- place the PCB in the lower enclosure, making certain the LED, connectors and cables are lined up properly.

- 20- install the tie wraps on the two cables, cutting off the exeeding portions.
- 21- After putting a small amount of glue on all for corners and two center side pieces insert the top cover on the lower cover.
- 22- after placing a very small amout of glue in each of the magnet holes, install each magnets.

# **PCB Location**



# Cable Colour Code

Needed!

# **Connector Allocation**



J2 = Band

J4 = PTT

J5 = ALC

J6 = AF

J7 = SQL

J9 = 8VDC

J10 = FSK

#### **Icom Pinout**

### ■ Accessory connector information

ACC (1)	PIN No.	NAME	DESCRIPTION	SPECIFICATIONS	
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	2	GND	Connects to ground.	Connected in parallel with ACC(2) pin 2.	
	3	SEND	Input/output pin. Goes to ground when transmitting. When grounded, transmits.	Ground level : -0.5 V to 0.8 V Output current : Less than 20 mA Input current (Tx) : Less than 200 mA Connected in parallel with ACC(2) pin 3.	
	4	MOD	Modulator input. Connects to a modulator.	Input impedance : 10 kΩ Input level : Approx. 100 mV rms	
	5	AF	AF detector output. Fixed, regardless of [AF] position in default settings. (see notes below)	Output impedance : 4.7 kΩ Output level : 100–300 mV rms	
	6	SQLS	Squelch output. Goes to ground when squelch opens.	SQL open : Less than 0.3 V/5 mA SQL closed : More than 6.0 V/100 μA	
	7	13.8 V	13.8 V output when power is ON.	Output current : Max. 1 A Connected in parallel with ACC(2) pin 7.	
	8	ALC	ALC voltage input.	Control voltage : –4 V to 0 V Input impedance : More than 10 kΩ Connected in parallel with ACC(2) pin 5.	

ACC (2)	PIN No.	NAME	DESCRIPTION	SPECIFICATIONS	
Rear panel view	1	8 V	Regulated 8 V output.	Output voltage Output current	: 8 V ±0.3 V : Less than 10 mA
	2	GND	Same as ACC(1) pin 2.		
	3	SEND	Same as ACC(1) pin 3.		
	4	BAND	Band voltage output. (Varies with amateur band)	Output voltage	: 0 to 8.0 V
	5	ALC	Same as ACC (1) pin 8.		
	6	TRV	Activates [XVERT] input/output when "HIGH" voltage is applied.	Input impedance Input voltage	: More than 10 kΩ : 2 to 13.8 V
	7	13.8 V	Same as ACC(1) pin 7.		

If the CW side tone level limit or beep level limit is in use, the CW side tone or beep tone decreases from the fixed level when the **[AF]** control is rotated above a specified level, respectively. (p. 96)