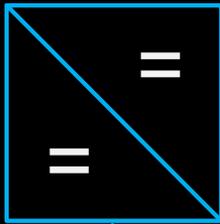


HV/MVDC Corridor  
→ 1500 VDC

**HIGH VOLTAGE ENTRY**  
SiC Isolation / DAB Stage  
1500 V HVDC  
Soft-start + fault containment



**LOCAL DC BACKBONE**  
Stable, low-loss  
High-density distribution  
BESS + DG integration

**QuietEdge Self-Organizing Layer**  
Emergent Control, No Sync Clock

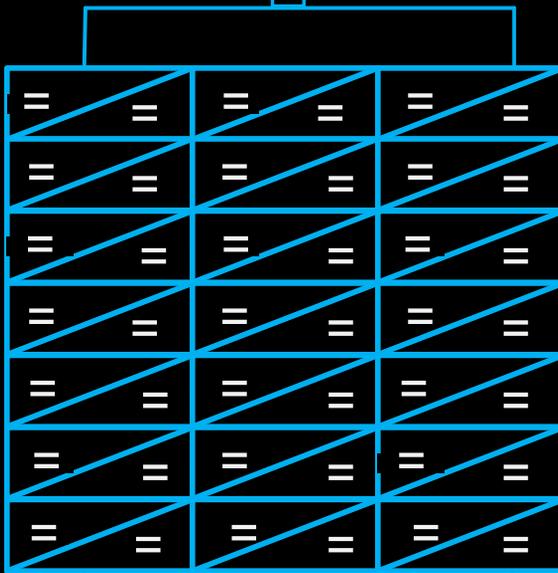


Generation Assets  
PV, H2, LG

**SELF-AWARE POWER NODES**  
QuietEdge Modular Units- 1MW Total  
Oscillator control (no sync lines)  
Symbolic override (trust + coherence)  
Hot-swap resync < 6 cycles  
µs-scale pulse adaptation

DC BUS -1500 VDC

SiC Resonant DAB



**EMS and Central Clock**  
No EMS Sync Required  
Accepts executive signals when coherent

**QuietEdge Self-Organizing Power Plane**  
Self-organizing bus  
Ripple cancellation  
Interleaving  
N+1 autonomous stability

**NO SYNC CLOCK REQUIRED**

DC BUS 48-800 VDC (n)



DC BUS 48-800 VDC (2)

DC BUS 48-800 VDC (1)

AI Server Rack (n)



AI Server Rack (2)

AI Server Rack (1)