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- A5. "Low Hydrogen Photovoltaic Cell'", Robert F. Zwaap, Troy Berens, **John R. Tuttle**, #20070116893, May 24, 2007.
- A4. "Vertical production of photovoltaic devices," **John R. Tuttle**, #20060219547, October 5, 2006.
- A3. "Process and photovoltaic device using an alkali-containing layer," **John R. Tuttle**, #20060219288, October 5, 2006.
- A2. "Pallet based system for forming thin-film solar cells," **John R. Tuttle**, #20060096635, May 11, 2006.
- A1. "Pressure control system in a photovoltaic substrate deposition apparatus," **John R. Tuttle**, #20060096536, May 11, 2006.
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 - 2) "Thermal process for creation of an in-situ junction layer in CIGS," **John R. Tuttle**, 7,319,190, January 15, 2008.
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SUMMARY OF PRESENTATIONS

Business / Technical / Investment (2003-2010)

Dr. Tuttle has conducted well over 250 presentations to Investor, business, and technical audiences on various subject matters including Energy, Photovoltaics (PV), Business Start-up, PV Industry, PV markets, Technology Competitiveness, Business and Technology Strategy, Innovation, Technology Transfer, and Manufacturing.

Technical (1990 - 2000)

- 1) "A 15.2% AM0 / 1433 W/kg Thin-Film Cu(In,Ga)Se₂ Solar Cell For Space Applications", John R. Tuttle, Aaron Szalaj and James Keane, *Proceedings of the 28th IEEE Photovoltaics Specialists Conference*, Anchorage, AK September 15-22, 2000 (oral).
- 2) "Progress Towards Commercialization of a 4.5-Sun, Flat-Plate Concentrating PV Module and System", John R. Tuttle, Aaron Szalaj, and Kelly Beninga, *Proceedings of the 28th IEEE Photovoltaics Specialists Conference*, Anchorage, AK September 15-22, 2000 (poster).
- 3) "A Crystalline and Thin-Film Cell PV Concentrator Package", **J.R. Tuttle**, E.D. Cole, T. Berens, *J. Alleman, and *J. Keane, *Proceedings of the 2nd World Conference and Exhibition on Photovoltaic Solar Energy Conversion*, Vienna, Austria, 6-10 July 1998, (poster).
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- 5) "A Cu(In,Ga)Se₂-Based Solar Cell, Module, and Manufacturing Project: The Fast Track to PV Power Commercialization", Tenth Sunshine Workshop on Thin-Film Solar Cells, Shinjuku, Tokyo, JAPAN, 8-9 November, 1996, (Invited).
- 6) "Investigations Into Alternative Substrate, Absorber, and Buffer Layer Processing for Cu(In,Ga)Se₂-Based Solar Cells", 25th IEEE Photovoltaic Specialists Conference, Washington, D.C., 13-17 May, 1996, (oral).
- 7) "The Performance of Cu(In,Ga)Se₂-Based Solar Cells in Conventional and Concentrator Applications", 1996 Spring MRS Meeting, Symposium J, San Fran., CA, 8-12 April, 1996, (Invited).
- 8) "Absorber Processing Issues in High-Efficiency Cu(In,Ga)Se₂-Based Solar Cells," 1995 Fall Meeting of the Materials Research Society, Boston, MA, Nov. 27 - Dec. 1, 1995 (oral).
- 9) "Prescriptions for the Fabrication of High-Efficiency Cu(In,Ga)Se₂-Based Thin Films and Devices," 13th European Photovoltaic Solar Energy Conference, Nice, FRANCE, 23-27 Oct., 1995 (oral).
- 10) "Thin-film Cu(In,Ga)Se₂ materials and devices: A versatile material for flat-plate and concentrator photovoltaic applications," The SPIE 40th Annual Meeting, 9-14 July, 1995 (oral).
- 11) "Absorber Processing Issues in High-Efficiency Thin-Film Cu(In,Ga)Se₂-Based Solar Cells", 13th NREL Photovoltaic Program Review, Lakewood, CO, May 17-19, 1995 (oral).
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- 13) "High-Efficiency Thin-Film Cu(In,Ga)Se₂-Based Photovoltaic Devices: Progress Towards A Universal Approach To Absorber Fabrication", 1994 OPA/OER Review, Nov. 16, 1994 (Oral).
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- 16) "High-Efficiency Thin-Film Cu(In,Ga)Se₂-Based Photovoltaic Devices: Progress Towards A Universal Approach To Absorber Fabrication", 23rd IEEE Photovoltaics Specialists Conference, Louisville, KY, May 10-14, 1993 (oral).
- 17) "Growth Mechanisms, Characterization, and Device Performance of Thin-Film Cu(In,Ga)Se₂ Solar Cells", 1993 Spring Meeting of the Materials Research Society, San Francisco, CA, April 1993 (oral).
- 18) "Novel Processing and Device Structures in Thin-Film CuInSe₂-Based Solar Cells," NREL Photovoltaic Advanced Research and Development 11th Review Meeting, Denver, CO May 13-15, 1992 (oral).
- 19) "Physical, Chemical, and Structural Modifications to Thin-Film CuInSe₂-Based Photovoltaic Devices", 22nd IEEE Photovoltaics Specialists Conference, Las Vegas, NV, October 1991 (poster, Polycrystalline Solar Cells Poster Award)
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- 25) "An Overview of Thin Film Photovoltaic Research at the Solar Energy Research Institute", 25th IECEC, Reno NV, August 1990 (Oral).
- 26) "Secondary and Polymorphic Phase Behavior of Thin Film CuInSe₂: Ramifications on the Device Performance", 21st IEEE PVSC, Kissimmee FL, May 1990 (Poster).
- 27) "Microstructural Characterization of Polycrystalline Thin Film CuInSe₂: A Promising Yet Complex Material for Photovoltaic Applications", University of Colorado, Boulder CO, February 1990 (Oral).



US005356839A

United States Patent [19]

Tuttle et al.

[11] Patent Number: 5,356,839

[45] Date of Patent: Oct. 18, 1994

[54] **ENHANCED QUALITY THIN FILM
CU(IN,GA)SE₂ FOR SEMICONDUCTOR
DEVICE APPLICATIONS BY VAPOR-PHASE
RECRYSTALLIZATION**

[75] Inventors: **John R. Tuttle**, Denver; **Miguel A. Contreras**; **Rommel Noufi**, both of Golden; **David S. Albin**, Denver, all of Colo.

[73] Assignee: **Midwest Research Institute**, Kansas City, Mo.

[21] Appl. No.: **45,860**

[22] Filed: **Apr. 12, 1993**

[51] Int. Cl.⁵ **H01L 21/302**

[52] U.S. Cl. **437/225; 437/5;**
437/234; 136/260; 136/265; 136/258

[58] Field of Search 437/225, 5, 103, 106,
437/234; 136/258 PC, 260, 265, 264

[56] **References Cited**

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| 5,078,804 | 1/1992 | Chen et al. | 437/5 |

Primary Examiner—Tom Thomas

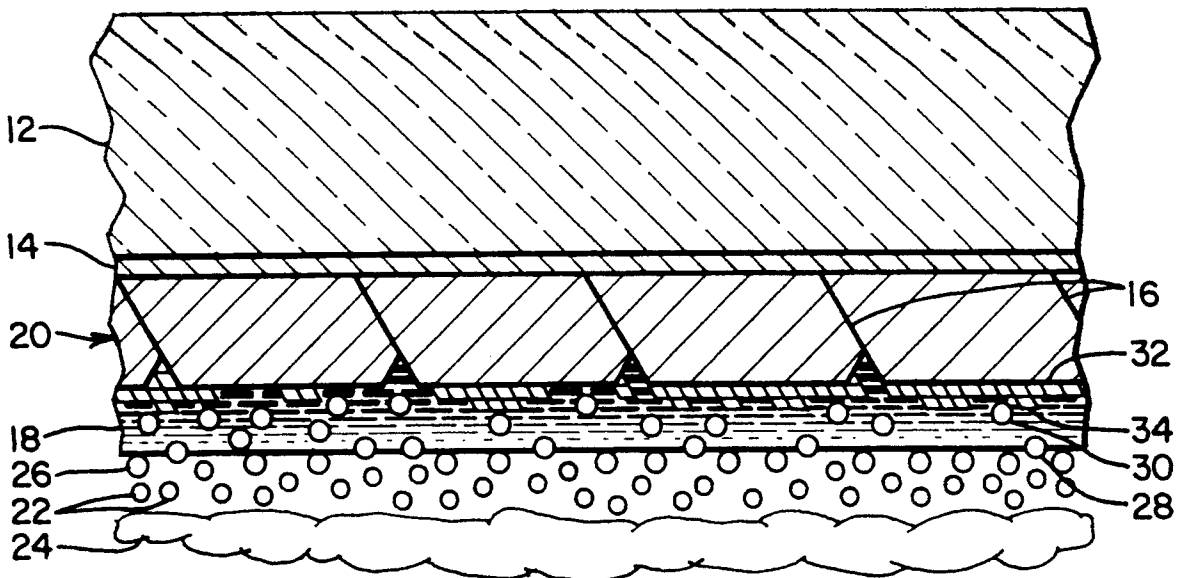
Assistant Examiner—Tuan Nguyen

Attorney, Agent, or Firm—Ken Richardson

[57]

ABSTRACT

Enhanced quality thin films of Cu_w(In,Ga)_ySe₂ for semiconductor device applications are fabricated by initially forming a Cu-rich, phase-separated compound mixture comprising Cu(In,Ga):Cu_xSe on a substrate to form a large-grain precursor and then converting the excess Cu_xSe to Cu(In,Ga)Se₂ by exposing it to an activity of In and/or Ga, either in vapor In and/or Ga form or in solid (In,Ga)_ySe₂. Alternatively, the conversion can be made by sequential deposition of In and/or Ga and Se onto the phase-separated precursor. The conversion process is preferably performed in the temperature range of about 300°–600° C., where the Cu(In,Ga)Se₂ remains solid, while the excess Cu_xSe is in a liquid flux. The characteristic of the resulting Cu_w(In,Ga)_ySe₂ can be controlled by the temperature. Higher temperatures, such as 500°–600° C., result in a nearly stoichiometric Cu(In,Ga)Se₂, whereas lower temperatures, such as 300°–400° C., result in a more Cu-poor compound, such as the Cu₂(In,Ga)₄Se₇ phase.

26 Claims, 3 Drawing Sheets



US007053294B2

(12) **United States Patent**
Tuttle et al.

(10) **Patent No.:** **US 7,053,294 B2**
(45) **Date of Patent:** **May 30, 2006**

(54) **THIN-FILM SOLAR CELL FABRICATED ON
A FLEXIBLE METALLIC SUBSTRATE**

(75) Inventors: **John R. Tuttle**, Frisco, CO (US);
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Falah S. Hasoon, Arvada, CO (US)

(73) Assignee: **Midwest Research Institute**, Kansas
City, MO (US)

(*) Notice: Subject to any disclaimer, the term of this
patent is extended or adjusted under 35
U.S.C. 154(b) by 0 days.

(21) Appl. No.: **10/480,880**

(22) PCT Filed: **Jul. 13, 2001**

(86) PCT No.: **PCT/US01/22192**

§ 371 (c)(1),
(2), (4) Date: **Dec. 12, 2003**

(87) PCT Pub. No.: **WO03/007386**

PCT Pub. Date: **Jan. 23, 2003**

(65) **Prior Publication Data**

US 2005/0074915 A1 Apr. 7, 2005

(51) **Int. Cl.**
H01L 31/336 (2006.01)
H01L 31/392 (2006.01)

(52) **U.S. Cl.** **136/265**; 136/256; 136/252;
136/264; 136/245; 136/244; 438/94; 438/95;
438/98; 257/431; 257/461; 257/464

(58) **Field of Classification Search** 136/256,
136/262, 265, 264, 245, 252, 244; 438/94,
438/95, 98; 257/431, 461, 464

See application file for complete search history.

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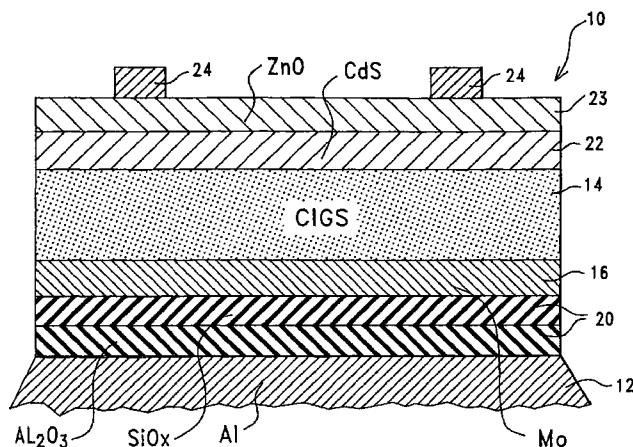
Primary Examiner—Alan Diamond

(74) *Attorney, Agent, or Firm*—Paul J. White

(57) **ABSTRACT**

A thin-film solar cell (10) is provided. The thin-film solar cell (10) comprises a flexible metallic substrate (12) having a first surface and a second surface. A back metal contact layer (16) is deposited on the first surface of the flexible metallic substrate (12). A semiconductor absorber layer (14) is deposited on the back metal contact. A photoactive film deposited on the semiconductor absorber layer (14) forms a heterojunction structure and a grid contact (24) deposited on the heterojunction structure. The flexible metal substrate (12) can be constructed of either aluminium or stainless steel. Furthermore, a method of constructing a solar cell is provided. The method comprises providing an aluminum substrate (12), depositing a semiconductor absorber layer (14) on the aluminum substrate (12), and insulating the aluminum substrate (12) from the semiconductor absorber layer (14) to inhibit reaction between the aluminum substrate (12) and the semiconductor absorber layer (14).

33 Claims, 3 Drawing Sheets





US007319190B2

(12) **United States Patent**
Tuttle

(10) **Patent No.:** **US 7,319,190 B2**
(45) **Date of Patent:** **Jan. 15, 2008**

(54) **THERMAL PROCESS FOR CREATION OF AN IN-SITU JUNCTION LAYER IN CIGS**

(75) Inventor: **John R. Tuttle**, Mechanicville, NY (US)

(73) Assignee: **Daystar Technologies, Inc.**, Halfmoon, NY (US)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 81 days.

(21) Appl. No.: **11/272,386**

(22) Filed: **Nov. 10, 2005**

(65) **Prior Publication Data**

US 2006/0102230 A1 May 18, 2006

Related U.S. Application Data

(60) Provisional application No. 60/626,843, filed on Nov. 10, 2004.

(51) **Int. Cl.**
H01L 31/00 (2006.01)

(52) **U.S. Cl.** **136/264**; 136/262; 136/265

(58) **Field of Classification Search** 136/262,
136/264, 265

See application file for complete search history.

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Primary Examiner—Nam Nguyen

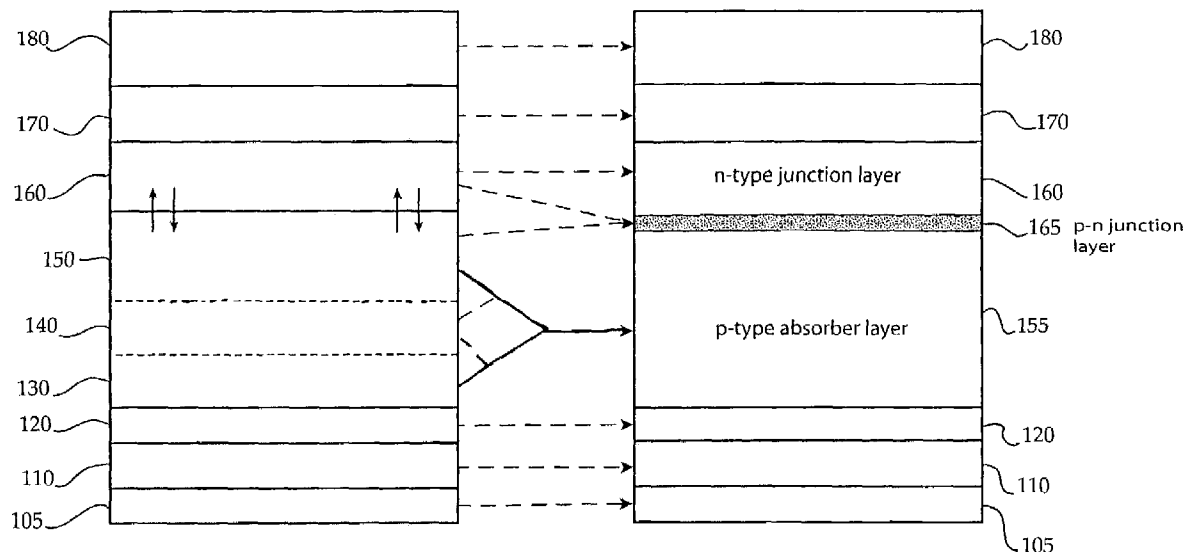
Assistant Examiner—Jeffrey Barton

(74) *Attorney, Agent, or Firm*—Hiscock & Barclay, LLP;
Thomas R. FitzGerald, Esq.

(57) **ABSTRACT**

The present invention relates generally to the field of photovoltaics and more specifically to manufacturing thin-film solar cells using a thermal process. Specifically, a method is disclosed to manufacture a CIGS solar cell by an in-situ junction formation process.

10 Claims, 1 Drawing Sheet





US007576017B2

(12) **United States Patent**
Tuttle

(10) **Patent No.:** **US 7,576,017 B2**
(45) **Date of Patent:** **Aug. 18, 2009**

(54) **METHOD AND APPARATUS FOR FORMING
A THIN-FILM SOLAR CELL USING A
CONTINUOUS PROCESS**

(75) Inventor: **John R. Tuttle**, Mechanicville, NY (US)

(73) Assignee: **DayStar Technologies, Inc.**, Halfmoon,
NY (US)

(*) Notice: Subject to any disclaimer, the term of this
patent is extended or adjusted under 35
U.S.C. 154(b) by 251 days.

(21) Appl. No.: **11/271,583**

(22) Filed: **Nov. 10, 2005**

(65) **Prior Publication Data**

US 2006/0096537 A1 May 11, 2006

Related U.S. Application Data

(60) Provisional application No. 60/626,843, filed on Nov.
10, 2004.

(51) **Int. Cl.**

H01L 21/00 (2006.01)

C23C 16/00 (2006.01)

(52) **U.S. Cl.** **438/800**; 118/719; 118/723 VE

(58) **Field of Classification Search** 118/723 VE;
438/800

See application file for complete search history.

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Primary Examiner—Lex Malsawma

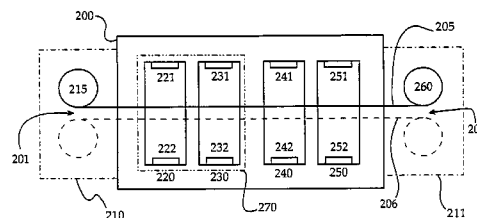
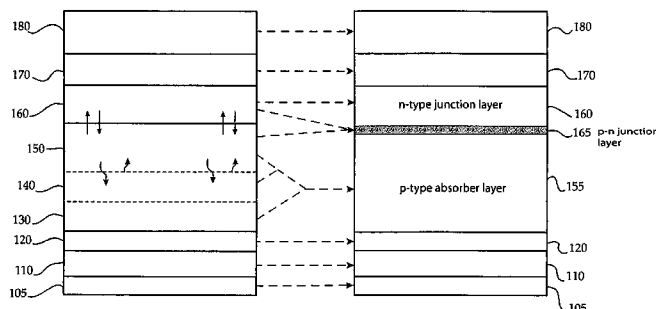
(74) *Attorney, Agent, or Firm*—Hiscock & Barclay, LLP

(57)

ABSTRACT

The present invention relates to new methods for manufacturing photovoltaic devices and an apparatus for practicing those methods of manufacture. The present invention employs a transfer-through system for advancing work piece substrates through an integrated apparatus of multiple treatment chambers that control each of the manufacturing processes.

44 Claims, 6 Drawing Sheets



Clean Energy Technologies: A Potential Way Forward to Finance Disruptive Innovation

John R. Tuttle ^{1*}, Michael E. deSa ^{1,#a}, Kristen Brown ^{1,#b}, and Don Chambers ²

ABSTRACT

Funding of early-stage clean-energy technologies exhibits exacting challenges resulting from capital intensity in a regulated industry. ARPA-E conducted a one-year research study with investors, innovators and intermediaries in which we categorize technologies as additive or disruptive and investors as real-money³ or conventional. Conventional investors such as angels, private and corporate venture capital invest in additive technologies due to lower risk and clearer exits into existing supply chains. Real-Money Investors, which include philanthropic, sovereign wealth and other institutional capital, currently invest into disruptive hard technologies for duration and a potential hedge against exposure to incumbent energy infrastructure obsolescence. We propose a potential way to close the investment risk ‘relay’ gap that prevails among real-money investors, with an innovative financial mechanism that needs empirical validation and application to prove its efficacy. (129 words).

INTRODUCTION

The U.S. Department of Energy’s Advanced Research Projects Agency – Energy (ARPA-E or the “Agency”) funds high-risk, high-reward early-stage energy technology research with a focus towards (i) reducing energy imports, (ii) reducing energy-related emissions and greenhouse gases, and (iii) improving energy efficiency in all sectors of the U.S. economy.

ARPA-E is charged with overcoming the long-term and high-risk technological barriers in the development of energy technologies. ARPA-E has two primary funding modalities: 1) Focused programs targeting a specific technology or problem; and 2) OPEN solicitations that seek innovative solutions that address the Agency’s broad mission. To date, efforts have resulted in the distribution of ~\$1.5B in research funding to both corporate (small and large companies) and non-corporate (federal labs and academic institutions) entities developing technologies requiring significant development of “hard” technology

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³ Capital from investors who have not borrowed. <http://www.financepractitioner.com/dictionary/real-money>

("hardtech"). In fact, approximately 90% of projects clearly require hardware, biological systems, or materials advancements, all of which are fields well known to require high capital investment and long development cycles. The remaining 10% of programs support predominately software-based innovations in electrical grid controls, energy efficient mobility, and vehicle automation. Realizing the impact of these programs relies on continued engagement with ARPA-E personnel after awards are made around aggressively set technical milestones.

In addition to the technical guidance given to selected projects, a critical component of the funding structure is providing guidance on a strategy for growth after ARPA-E funding subsides. The Agency contains dedicated resources with the specific mandate of providing such guidance on how to transition energy innovations into a market to realize energy or emissions impact. Understandably, optimizing the growth strategy for each unique project requires a detailed understanding of the commercialization ecosystem, which engages entrepreneurs, small and large businesses, and the private capital community to identify appropriate partners for financing, manufacturing, and customer acquisition.

One challenge often highlighted as being exceptionally difficult for early-stage energy technologies requiring long development cycles and comprising substantial hardware risk is the acquisition of private capital. In an effort to deepen the understanding of challenges associated with financing early-stage technologies, and to highlight opportunities for innovation in financing, ARPA-E engaged in a multi-part research study. The elements of this research effort include: 1) a thorough literature review to ascertain the state-of-the-art of understanding of early stage technology development financing; 2) draft and release of a request for information (RFI) seeking input from Innovators and capital providers; 3) analysis of both written responses and follow-up oral interviews; 4) the utilization of the RFI data to design a workshop to explore the issues; and 5) convening of the workshop attended by 60+ innovators, capital providers, and various types of intermediaries, to present the data and explore future opportunities. Our study synthesized all stages of the qualitative and focus study with cross validation.

Here, we provide background for the work, summarize the research methodology and findings and conclude with a discussion on how this work supports the existing knowledge base, offers new observations and insights on present investment trends and suggests future areas of research and practical experimentation that could lead to enhanced financing of clean energy 'disruptive' technology and its commercialization.

BACKGROUND

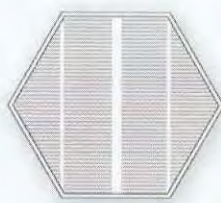
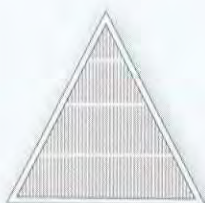
Funding Challenges

The process of moving a technology from invention through development to implementation has been of interest in a myriad of disciplines (Van de Ven 2013). The figure below, we provide two example illustrations of the sequential innovation process, beginning with ideation and ending with commercial success. The intermediate stages between initial conception and final commercialization range from three to five, and, in some cases, more (Branscomb & Auerswald 2002, Jenkins & Mansur 2011, Ford, Koutsky & Spiwak 2007, Zindler & Locklin 2010).

Solar Energy Cost Breakthrough Ahead?

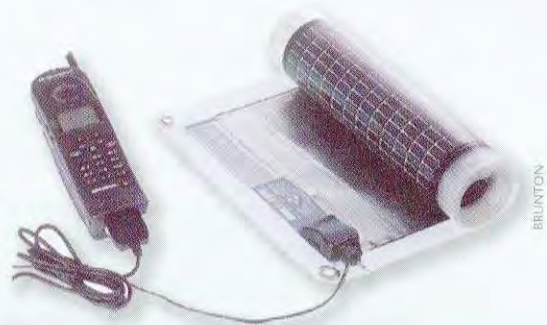
Photovoltaic Industry Pushes

One company looks to thin-film production efficiencies as the path to widespread adoption.



Thin-film cell technologies are a promising alternative to break through the barriers related to silicon cell cost, scaling and feedstock. Thin-film technology used in consumer products enables them to be flexible, portable and lightweight. Below, from left, the Juice Bag daypack from Reluminati LLC, Power Shade field shelter from PowerFilm Solar, and the SolarRoll from Brunton.

By John R. Tuttle, Ph.D.



BRUNTON



WWW.REVARESTORE.COM



POWERFILM SOLAR

Right, DayStar's solar cell manufacturing equipment is derived from commodity computer component industries.

GIGAWATT-SCALE MANUFACTURING OF DISCRETE FLEXIBLE CIGS SOLAR CELLS AS A NEAR-TERM CATALYST FOR THE TRANSITION TO A “SOLAR ECONOMY”

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ABSTRACT

Meeting the challenges of future worldwide electricity demand will eventually require a shift to the real-time use of today's most abundant source of energy – the Sun. Sustainability will likewise require the foundation of our economy to shift as well. A “solar economy” will only result from a technology approach that allows healthy near-term and sustainable return on investment (ROI) for the capital markets.

This contribution explores a unique approach to producing high-quality CIGS PV solar cells, with a low capital to capacity ratio that will enable high ROI gigawatt scale manufacturing. Through this pathway, the PV technology described herein could become the necessary catalyst towards the transition to a solar economy.

INTRODUCTION

Worldwide demand for energy is growing rapidly. One estimate is that this increase will be 75% by 2015, or 3 Terawatts (TW) of new generation capacity (1 TW = 10^{12} Watts). Other estimates say that to raise the standard of living of the developing world to that of the developed world (2-3 kW/person) will require 16-20 TW of new electricity generation. In either case, the world is heading toward an irreconcilable future where energy demand and the present form of energy supply irreversibly diverge.

Concerns about global environmental health and changing economies in the electricity market are encouraging a look

at new energy technology choices. But, significant and sustainable change will only occur when those with the “power” to change (i.e. access to capital) conclude there are near-term opportunities in the solar market that justify their attention and risk of investment.

Our Sun (our “Day Star”) is the “Sol” source from which all present forms of useable energy originates. Rather than the economic and environmental constraints posed by our present reliance on conventional fossil fuels – our “Ancient Sunlight” - we are obliged to develop an energy economy that exploits our most abundant source of real-time energy. Renewable solar energy generation technologies must transition from “alternative” status to that of a viable energy option. To achieve this transition requires a shift from a carbon-based economy (primarily Oil) to a Solar Economy. The timeframe, rate and magnitude of this shift will be fundamentally dependant on the perceived ROI that will result from making the change.

In order to achieve these goals, the PV industry must begin to focus efforts on material technologies, factory design, economics and financing methods that will enable a healthy ROI. When the investment community achieves healthy returns, further investment is stimulated, and an economic foundation is laid that will contribute to the GNP and resulting tax basis. This sets in motion a cycle of associated government subsidies that are supported by the investment community, benefit middle class job growth, and set in motion positive changes that affect global economic growth with minimal environmental impact.

DESIGN CONSIDERATIONS AND IMPLEMENTATION OF VERY-LARGE SCALE MANUFACTURING OF CIGS SOLAR CELLS AND RELATED PRODUCTS

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ABSTRACT: Discrete CIGS solar cells on flexible metal substrates offer an alternative to wafer-Si based cells. The manufacture of this cell technology additionally offers capital cost, throughput and yield advantages over the manufacture of either wafer-Si or traditional monolithically integrated thin-film modules. Economies of scale and volume can be achieved with the implementation of gigawatt-scale manufacturing. Likewise, vertical integration within the production line to include In refinement, steel substrate finishing, source material synthesis and formation, as well as module packaging materials (either glass and Al or plastic) provide for optimization of cost and supply-chain issues.

Keywords: CIGS, Photovoltaic, Solar City, Solar Cell, Solar Panel, Learning Curve

1. INTRODUCTION

Electricity demand is predicted to grow at an annual rate of 2.3 - 3.4% in the coming decade, depending on assumptions made [1]. In one scenario, where every planetary citizen is provided access to 2 kW of electricity supply by the middle of the century (the U.S.A. is at 3 kW per capita), the peak demand for electrical power increases from 4 terawatt (1TW = 10^{12} W) to 18.4 TW (Table I). It is both unlikely and undesirable that the increase in demand will be fulfilled by traditional hydrocarbon energy resources.

Table I: Electrical demand estimates

| | World Pop. | Per Capita Demand | Peak Demand ^a | Peak Demand ^b |
|-------------|------------|-------------------|--------------------------|--------------------------|
| 2004 | 6.38 B | 0.6 kW | 4.0 TW | 4.0 TW |
| 2050 | 9.22 B | 2.0 kW | 11.4 TW | 18.4 TW |

^a 2050 demand from pop. growth & per capita demand

^b 2050 demand from IEA growth rate of 2.3%

Clean energy resources, such as wind and solar, have the opportunity to backfill the demand. Of all non-carbon based resources, solar is believed to be the only energy resource that can ubiquitously meet the “Terawatt Challenge” [2]. With a current annual global production capacity of approximately one gigawatt (1GW = 10^9 W), the photovoltaics (PV) industry requires substantial growth in order to effectively meet the challenge. Additionally, the cost of components throughout the PV system product chain must be significantly reduced to achieve improved cost-competitiveness.

Keshner and Arya [3] recently examined this issue and came to three primary conclusions:

1. \$1.00 /watt at the system level is required to achieve cost-competitiveness with traditional power generation choices.
2. The \$1.00/watt benchmark cannot be achieved with crystalline Si solar cell technologies.
3. Multi-GW factories producing thin-film PV modules is the enabling factor in achieving the \$1.00/W cost benchmark.

This paper examines a specific roadmap for the achievement of 2.0 GW/yr production of Cu(In,Ga)Se₂ (“CIGS”) solar cells and modules. In contrast to the aforementioned study, we consider the following:

1. The production of CIGS solar cells by a continuous in-line process at higher unit volumes (200 MW / machine) offers capital investment advantages relative to the manufacture of

traditional monolithically integrated panels.

2. Flexible solar cells offer opportunities for non-rigid packaging that can lower implementation costs in the field. On-site Glass and aluminum sub-factories, considered a requirement to lower module costs, can be replaced with polymer, polymer composite, and steel finishing factories.
3. Control of the raw material supply chain, especially In, Ga and Se, and their synthesis and formation into source materials, such as sputtering targets, will be an important component in realizing full cost reductions.
4. Higher profit margins in the near-term resulting from system sales prices of \$1-\$3/W offer accelerated return-on-investment opportunities to justify the up-front capital investment required to achieve multi-GW scale manufacturing in multiple locations.

2 CIGS SOLAR CELLS AND MANUFACTURING

2.1 CIGS Solar Cell Technology

Solar cells based upon the CIGS p-type absorber have been under development for over 25 years. CIGS has proven to be both highly efficient, with laboratory performance approaching 20% [4], and highly reliable in the field, with lifetimes exceeding 18 years with little or no degradation. Unfortunately, there has not been substantial market penetration by this technology, in part

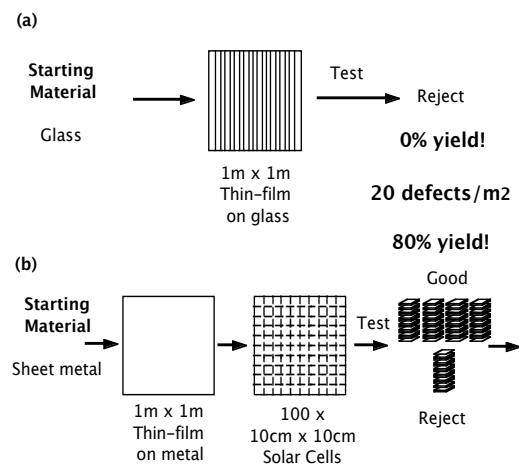


Figure 1: Illustration of (a) dis-economy of yield associated with monolithic integration of thin-film PV in contrast to (b) production of discrete solar cells.

A 15.2% AM0 / 1433 W/KG THIN-FILM CU(IN,GA)SE₂ SOLAR CELL FOR SPACE APPLICATIONS

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ABSTRACT

Thin-film CIGS solar cells have been investigated for use in the space environment. The study includes deposition on lightweight metal substrates, measurement under AM0 light, and irradiation by electrons and protons. A 15.2 % AMO cell performance has been achieved, resulting in a specific power of 1433 W/kg & 1235 W/kg for bare and SiOx “coverglass” cells, respectively. No degradation has been observed after e⁻ irradiation. A 24-cm² cell design has achieved 1100 W/kg specific power.

INTRODUCTION

Si and III-V photovoltaic (PV) technologies have dominated the satellite power market since its inception nearly 50 years ago. Multi-junction III-V cells have surpassed Si technologies in power density (W/m²), specific power (W/kg), and relative degradation (End-of-life (EOL) η / beginning-of-life (BOL) η), but have lagged in cost (\$/W) competitiveness. Generally, it is a variable combination of these factors that determine the cell material of choice for a particular satellite application.

As the number of government and commercial satellite constellations increase, and the satellite unit size decreases, there has emerged an increasing demand for PV cells and arrays with a lower cost-basis and lighter mass (also translating into lower cost). The only technologies with a promise to reduce costs by 50-90% are cells and arrays based upon thin-film PV technologies. At present, the selection includes amorphous Si and Cu(In,Ga)Se₂ (CIGS)-based PV cells and integrated panels. In this contribution, we present results from recent studies involving CIGS thin-film cells fabricated on ultra-lightweight metal substrates. Fabrication, cell testing, and manufacturing issues will be presented.

CELL DESIGN & FABRICATION

Cell Design

Thin-film PV was first developed as the next generation successor to wafer-Si technologies with the intention of

breaking the \$1/Wp cost barrier for terrestrial markets. Cost reduction is realized through material reduction and economies of scale and volume (EOSV). 1-10 μ m thin-film solar cells represent a greater than 95% reduction in semiconductor material costs. EOSV for terrestrial products was to be realized through in-line cell fabrication processes (in contrast to batch processes for wafer-Si) and monolithic integration (a process of concurrent cell fabrication and series interconnection). These goals have been difficult to achieve and have slowed the progress of commercialization of thin-film PV technologies.

The solar cell under investigation in this work is illustrated in Fig. 1. The primary difference between this cell design and a standard CIGS cell lies in the use of thin, flexible metal substrates (in contrast to rigid glass) and a proprietary buffer layer that contains Na. Present state-of-the-art performance for cells fabricated on soda-lime glass and 150- μ m stainless steel (SS) is 18.8%¹ and 17.5%², respectively, both measured under AM1.5 simulated illumination.

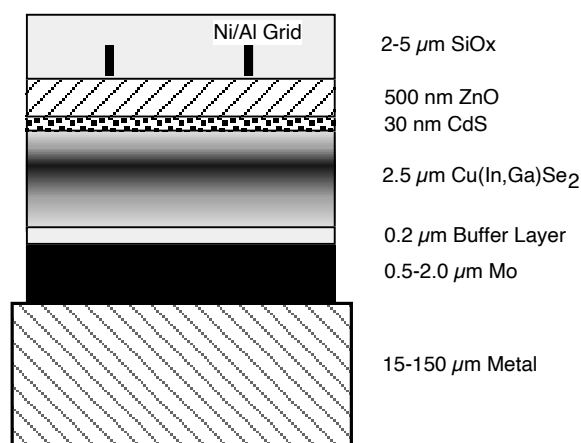


Fig. 1 Schematic of thin-film CIGS space solar cell

The use of a metallic substrate also deviates from the norm in that it does not support a monolithic integration scheme. Monolithic integration, on paper, provides for streamlined interconnection during the cell fabrication process. Unfortunately, this has not been realized in

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Perspective on High-efficiency Cu(In, Ga)Se₂-based Thin-film Solar Cells Fabricated by Simple, Scalable Processes

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*We have fabricated high-efficiency Cu(In, Ga)Se₂(CIGS)-based photovoltaic (PV) devices by four different processes. Each process may be characterized as either sequential or concurrent deposition of the metals with or without an activity of selenium. **A world-record, total-area efficiency of 17.1%** has been achieved by the concurrent delivery of the metals in the presence of selenium. Gallium has been introduced into the device in such a manner as to produce homogeneous, normal profiling and double-profiling graded bandgap structures. This has resulted in an open-circuit voltage (V_{oc}) parameter of 680 mV and a fill factor of over 78%. A growth model has been developed allowing for simple translation of these processes to a manufacturing environment for the large-scale production of modules.*

INTRODUCTION

The National Renewable Energy Laboratory (NREL), under contract to the United States Department of Energy, has been involved in the research and development of thin-film photovoltaics since 1982. The primary charter of the photovoltaics program is to develop new and better PV technologies and to support industry in doing the same. The goal is to introduce photovoltaics as a cost-effective alternative to conventional utility power generation. This goal is accomplished by an approach that first considers basic materials research, followed by solar cell development, and concludes with technology transfer to industrial organizations and market development.

To establish cost effectiveness in PV technology, both performance and cost are considered. Solar cells and modules fabricated from polycrystalline CIGS-based thin films are strong candidates for high performance and low cost.¹ Laboratory-scale device efficiencies in excess of 15% have been reported by several groups.²⁻⁴ The low-cost criterion is satisfied for most thin-film technologies through low material usage, monolithic integration and low manufacturing costs, to name a few. Several industrial groups have produced large-area (sub) modules with a performance in excess of 7%.^{5,6} One company has successfully produced a 10% module with an aperture area near 4000 cm².⁴ In this work, laboratory-scale device absorbers are fabricated by physical vapor deposition (PVD) processes that may be conducive to industrial scale-up.^{7,8} An additional advantage for the thin-film CIS technology developed at the NREL is the potential for high yield through greater-than-average process tolerances and self-limiting

Structure, chemistry, and growth mechanisms of photovoltaic quality thin-film Cu(In,Ga)Se_2 grown from a mixed-phase precursor

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The formation chemistry and growth dynamics of thin-film CuInSe_2 grown by physical vapor deposition have been considered along the reaction path leading from the $\text{Cu}_x\text{Se}:\text{CuInSe}_2$ two-phase region to single-phase CuInSe_2 . The $(\text{Cu}_2\text{Se})_\beta(\text{CuInSe}_2)_{1-\beta}$ ($0 < \beta \leq 1$) mixed-phase precursor is created in a manner consistent with a liquid-phase assisted growth process. At substrate temperatures above 500 °C and in the presence of excess Se, the film structure is columnar through the film thickness with column diameters in the range of 2.0–5.0 μm . Films deposited on glass are described as highly oriented with nearly exclusive (112) crystalline orientation. $\text{CuInSe}_2:\text{Cu}_x\text{Se}$ phase separation is identified and occurs primarily normal to the substrate plane at free surfaces. Single-phase CuInSe_2 is created by the conversion of the Cu_xSe into CuInSe_2 upon exposure to In and Se activity. Noninterrupted columnar growth continues at substrate temperatures above 500 °C. The addition of In in excess of that required for conversion produces an In-rich near-surface region with a CuIn_3Se_5 surface chemistry. A model is developed that describes the growth process. The model provides a vision for the production of thin-film CuInSe_2 in industrial scale systems. Photovoltaic devices incorporating Ga with total-area efficiencies of 14.4%–16.4% have been produced by this process and variations on this process. © 1995 American Institute of Physics.

I. INTRODUCTION

Photovoltaic (PV) devices fabricated with a thin-film Cu(In,Ga)(S,Se)_2 -based chalcopyrite absorber continue to advance towards economic viability with active-area conversion efficiencies exceeding 17%.^{1,2} Critical developments include processing temperatures greater than 500 °C and the utilization of $\text{CuIn}_{1-x}\text{Ga}_x\text{Se}_2$ ($1.0 \leq E_g \leq 1.7$ eV) and $\text{CuIn(S}_{1-y}\text{Se}_y)_2$ ($1.0 \leq E_g \leq 1.5$ eV) alloys as absorbers to better match the absorber band gap to the AM1.5 solar spectrum. In this report we focus on the common basis of these quaternary alloy systems, thin-film CuInSe_2 . It is the formation chemistry within the Cu:In:Se system and the resulting properties of the binary and ternary products^{3–5} that govern the performance of the alloys within the compositional range of interest. In most cases, the results presented here for the ternary CuInSe_2 system apply equally well to the quaternary Cu(In,Ga)Se_2 system. Fundamental studies of the ternary CuGaSe_2 and the quaternary Cu(In,Ga)Se_2 material systems can be found elsewhere in the literature.^{6–9}

To date, the absorbers for the high-efficiency devices are fabricated by either physical vapor deposition (PVD) of the constituent elements,^{10–13} or by the selenization of the metal precursors. PVD is a viable technology for the fabrication of CuInSe_2 -based absorbers and modules and is utilized as a prototyping tool for the development of novel fabrication processes.¹ Several industrial participants^{14,15} have developed the selenization process for large-area PV module production. Successful introduction of a profitable product, however, has been hindered by a compound formation chemistry that encourages spatial nonuniformities and poor adhesion to the underlying Mo back contact.

Previously, we briefly reported on a novel two-stage PVD process for the fabrication of high-quality ternary and

quaternary thin films.⁵ The process relies on the superior film morphology of a very Cu-rich Cu:In:Se mixture, or Cu_xSe binary, with a simple reaction chemistry for the subsequent formation of the single-phase ternary product. The resulting polycrystalline aggregate exhibits larger grains and higher preferred orientation relative to other processes. This structure is defined by non-interrupted columnar grains with lateral dimensions of 2–10 μm . This structure allows for continuous carrier transport between the interface and the back contact, and minimizes deleterious grain-boundary effects. A growth model incorporating the formation chemistry was described, and we argued for its universal application to other PVD and non-PVD absorber fabrication processes. The process has been successfully applied to both the CuInSe_2 and CuGaSe_2 ternary and combined quaternary alloy systems. Cu(In,Ga)Se_2 thin-film devices fabricated by this process have total-area efficiencies in excess of 14%. In this paper, we focus on presenting the supporting evidence and expanding on the details of the growth model and formation chemistry. Elsewhere in the literature,^{1,10} we report on two other PVD processes that, in part or whole, invoke this formation chemistry and have produced total-area PV device efficiencies of 16.4% and 15.1%, respectively.

Supporting data include structural characterization by x-ray diffraction (XRD) and high-resolution transmission electron microscopy (HRTEM) and diffraction (TED) that identify $\text{CuInSe}_2:\text{Cu}_2\text{Se}$ phase segregation and anomalous CuPt ordering¹⁶ of the CuInSe_2 lattice. XRD indicates a qualitative relationship between grain size, preferred orientation, and Cu content. A liquid-phase-assisted growth mechanism involving the Cu_xSe binary is suggested as the primary factor influencing the observed phenomena. Bulk and surface film compositions ranging from CuInSe_2 to In_2Se_3 are observed by XRD and x-ray photoelectron spectroscopy

Thin-film Cu(In,Ga)Se₂ materials and devices: A versatile material for flat-plate and concentrator photovoltaic applications

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ABSTRACT

Thin-film Cu(In,Ga)Se₂(CIGS) is used as the absorber in an all-thin-film solar cell for both conventional 1-sun and concentrator applications. The absorber fabrication process is represented by time-dependent profiles of elemental Cu, In, Ga, and Se fluxes. The Cu/(In+Ga) ratio determines the phase chemistry during growth whereas the Ga/(Ga+In) ratio determines the CIGS band gap profile. All film-growth processes enter the CIGS:Cu_xSe two-phase field where the Cu_xSe facilitates large grain growth. Characterization of cells under 1-sun illumination reveals world-record total-area performance of 17.1%. Improvements relative to previous cells are linked to decreased inter-diffusion of In and Ga within the absorber. Cell parameters include an open-circuit voltage (V_{oc}) of 654 mV and short-circuit current (J_{sc}) of 33.9 mA/cm². A second cell was fabricated for operation under concentration. The 1-sun, direct-spectrum measurement yielded a 15.1%-efficient cell. Under 5- and 22-sun concentration, the cell improved to 16.5% and 17.2%, respectively. This achievement is significant in that it proves a compatibility of polycrystalline thin-film and concentrator technologies. Further optimization could yield 1-sun performance in excess of 18% and concentrator performance in excess of 20%. A path to this goal is outlined.

Keywords: Thin films, Photovoltaics, Copper Indium Gallium Selenide, Concentrator Cell, Process Modeling

1. INTRODUCTION

Photovoltaics (PV) is a critical power generation technology for applications ranging from third-world and domestic rural electrification (near-term) to peak-power generation (mid-term). The key element to the implementation of PV lies in developing devices and modules with crystalline-level performance at thin-film costs. This is rapidly becoming the case for thin-film CIGS-based PV technology. Thin-film CIGS solar cells are presently being investigated worldwide by industrial, academic, and institutional facilities. Laboratory-scale device efficiencies in excess of 15% have been reported by several groups.¹⁻³ In this contribution, we are pleased to report on significant progress that includes world-record device performance of 17.1% total-area (18.0% active-area) conversion efficiency for conventional flat-plate 1-sun applications and 17.2% conversion efficiency for device operation under 22-sun concentration.

The National Renewable Energy Laboratory's (NREL) PV Program is chartered to develop state-of-the-art laboratory-scale processes and transfer the technology to industry for scale-up. For manufacturing, the configuration of choice is that of a flat-plate module with monolithic interconnection of series-connected cells. This module integration approach allows for large-area deposition on inexpensive glass substrates, with an interconnect scheme that minimizes area loss and is not labor intensive. Several industrial groups have produced large-area (sub)modules with performance in excess of 7%.^{4,5} One company has successfully produced a 10% module with an aperture area near 4000 cm².³ In this work, laboratory-scale device absorbers are fabricated by physical vapor deposition (PVD) processes that may be conducive to industrial scale-up.^{6,7} The absorber processing techniques are described in terms of source material delivery, compound formation, recrystallization, and surface termination.^{6,8} The cells are characterized under standard AM1.5 illumination.

Alternatively, concentrator module geometries offer advantages due to savings in cell manufacturing costs per unit of power produced. Historically, concentrator studies have focused on high-efficiency crystalline III-V and Si-based^{9,10} devices as the cell component. As the performance of thin-film CIGS solar cells approaches the 20% level, however, the differentiation arguments begin to break down. Previous modeling work⁹ predicted that 1.14 eV is the optimum direct band gap for operation under a wide range of concentration levels and temperatures. This, conveniently, is the band gap of the CIGS absorbers that have produced record device efficiencies. In this work, we describe the fabrication of thin-film CIGS solar cells for operation under concentration. In general, the device design and fabrication are identical to that used for 1-sun operation, except for the front-contact grid pattern. Finished cells are characterized under the direct spectrum at various levels of concentration. The results shed important light on the robust nature of this particular thin-film solar cell system.