

University of Technology Sydney (UTS)

Quantum Circuit Design & Verification Project

Two PhD Positions are open! Please send your CV and academic transcripts to Profs Yuan Feng and Sanjiang Li

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Research Directions (see VeriQC.com for more info.)

quantum circuit design and verification, quantum software, and quantum artificial intelligence

The project aims to develop comprehensive theory and effective techniques for formal modelling, equivalence checking, and model checking of quantum combinational and sequential circuits.

This PhD offer includes:

3-4 Years Full PhD Scholarship (>AU\$28,000 each year) + tuition

Interested applicants who are self-motivated and with strong backgrounds in computer science, physics, mathematics, or electronics are welcome!

University of Technology Sydney

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| QS World University Rankings 2023 global ranking | 137 |
| Times Higher Education World University Rankings | 143 |
| Computer Science at UTS in ARWU global ranking | 11 |

The UTS Centre for Quantum Software & Information

UTS:Q|SI

is international leading in Quantum Computing

Most recent publications appear in AAAI, DAC, ICCAD, ACM TODAES, IEEE TC, IEEE TCAD, etc.

Some representative works:

[Book] [*Model Checking Quantum Systems: Principles and Algorithms*](#), Cambridge University Press, 2021

[*A Tensor Network based Decision Diagram for Representation of Quantum Circuits*](#), *ACM Transactions on Design Automation of Electronic Systems*, 2022

[*Qubit mapping based on subgraph isomorphism and filtered depth-limited search*](#), *IEEE Transactions on Computers*, 2021