

## Synchronism check relay KSR 30



### Technical Data

**Voltage:**

100/400 V ~ +10%/-20%

**Frequency:**

50 ... 60 Hz

(other voltages and frequencies on request)

**Consumption**

System 1, Terminal 1 and 2 or 1 and 3: 5 VA  
System 2, Terminal 4 and 5 or 4 and 6: 1 VA

**Setting range**

3 ... 30° (el.) of beat voltage  
(Linear setting by means of fixable potentiometer, scaled 0 .. 10)

**Ambient temperature**

-20°C ... +55°C

**Output relay / Contact capacity**

max. 250 V ~ und 7.5 A

max. 30 V = 180 Watt

max. 250 V = 100 Watt

**Degree of protection**

IP 20/P20

**Weight**

0,8 kg

Description	Ordering information
<p>The synchronism check relay KSR should be applied to avoid antiphase paralleling of 3phase AC-system, when synchronizing is operated manually or automatically.</p> <p>The unit is assembled in a plastic enclosure, fitted with facilities for snap-in fixing on a mounting rail. The connection terminals are assembled at the front side of the housing.</p> <p>The two AC voltage systems, which are to be paralleled, have to be connected to the unit. The measuring circuit compares the phase position, by detecting the beat voltage. When beat voltage approaches zero point, falling below the set value, the output relay will be energized and remains in this position as long as the set value will not be exceeded. The LED signal indicates switching of output relay contact and is showing, that paralleling now is enabled.</p> <p>The synchronism check relay is suitable for 100 % duty cycle, but is should be switched off, when synchronizing procedure is finished. The synchronizing enable range should be selected as small as possible. The correct setting angle <math>\alpha</math> has to be calculated, taking into account the individual operating time <math>t_v</math> of circuit-breaker and the permissible frequency difference <math>\Delta f</math>, in conformity with the following formula: <math>\alpha = 0,4 t_v [\text{ms}] \times \Delta f [\text{Hz}]</math></p> <p><i>Example:</i> The individual closing time <math>t_v</math> of a circuit-breaker (including the time lag of all auxiliary contactors connected in this control circuit) was found to be 75 ms. The permissible frequency difference <math>\Delta f</math> in this case shall be 1.0 Hz.</p> <p>Then the setting point will be: <math>\alpha = 0,4 \times 75 \times 1,0 = 30^\circ</math></p>	<p>Art.-Nr. 2172000.05</p>