

Technical Overview

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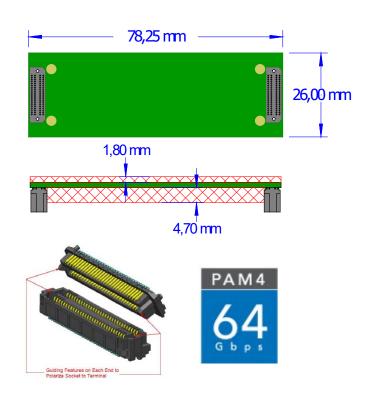


Technical Overview



Physical

- Base Size: 26 mm x 78.25 mm
- 4,7 mm Component height
- PCIe Gen6 Capable, up to x16
- Seamless integration into managed systems (IPMI)
- 40 I/Os











Technical Overview



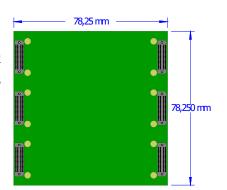
Scalability

- Single QMC
 - PCle x4
 - 40 I/Os

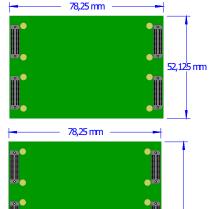


- Double QMC
 - PCIe x8
 - 80 I/Os

- Triple QMC
 - PCle x12
 - 120 I/Os



- Quad QMC
 - PCle x16
 - 160 I/Os











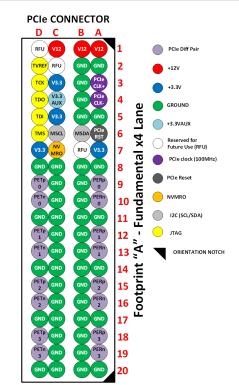
104,375 mm





Host Interface

- Power Supply
 - 3.3 Volt
 - 12 Volt
- PCI-Express x4
 - Rx/Tx
 - Reference-Clock
- JTAG-Interface
 - Incl. Present Detection
- Management Interface
 - 3.3VAUX
 - I2C









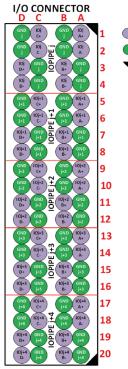


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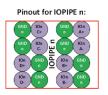


I/O Capabilities

- 40 I/Os per single QMC (or 20 pairs)
- Organized as 5 IOPIPEs each offering
 - 8 single-ended / 4 differential I/Os
 - Individual Ground
- Support of isolated interfaces
- 80 I/Os per double QMC
- 120 I/Os per triple QMC
- 160 I/Os per quad QMC













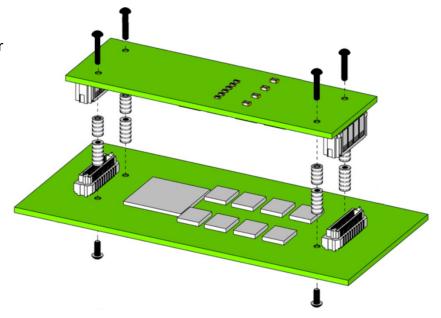


Technical Overview



Air Cooled

- Complete Mounting kit provided by the Carrier manufacturer
 - Standoff mounted on Carrier
 - Spacer covering the space for potential heatsink of QMC
 - QMC can use a heatsink
 - » Cannot extend beyond defined size envelope









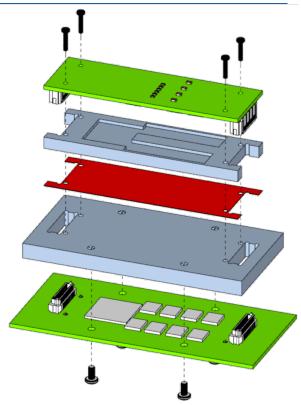


Technical Overview



Conduction Cooled

- Skyline Heatsink is part of QMC
 - Already mounted at delivery
- Complete Mounting kit including red TIM (Thermal Interface Material)
 - All provided by carrier manufacturer
- Design of the carrier cooling structure is 100% up to the carrier designer
 - Cannot violate mechanical envelopes











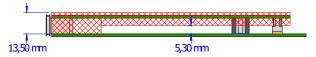




Stacking Heights

- Traditional Mezzanines have fixed Stacking height
 - Limited component height for the carrier

Fixed 10 mm XMC Stacking Height



- Variable QMC Stacking Height
- Carrier defines Stacking Height
 - Maximizes component envelope on carrier

Variable QMC Stacking Height

