

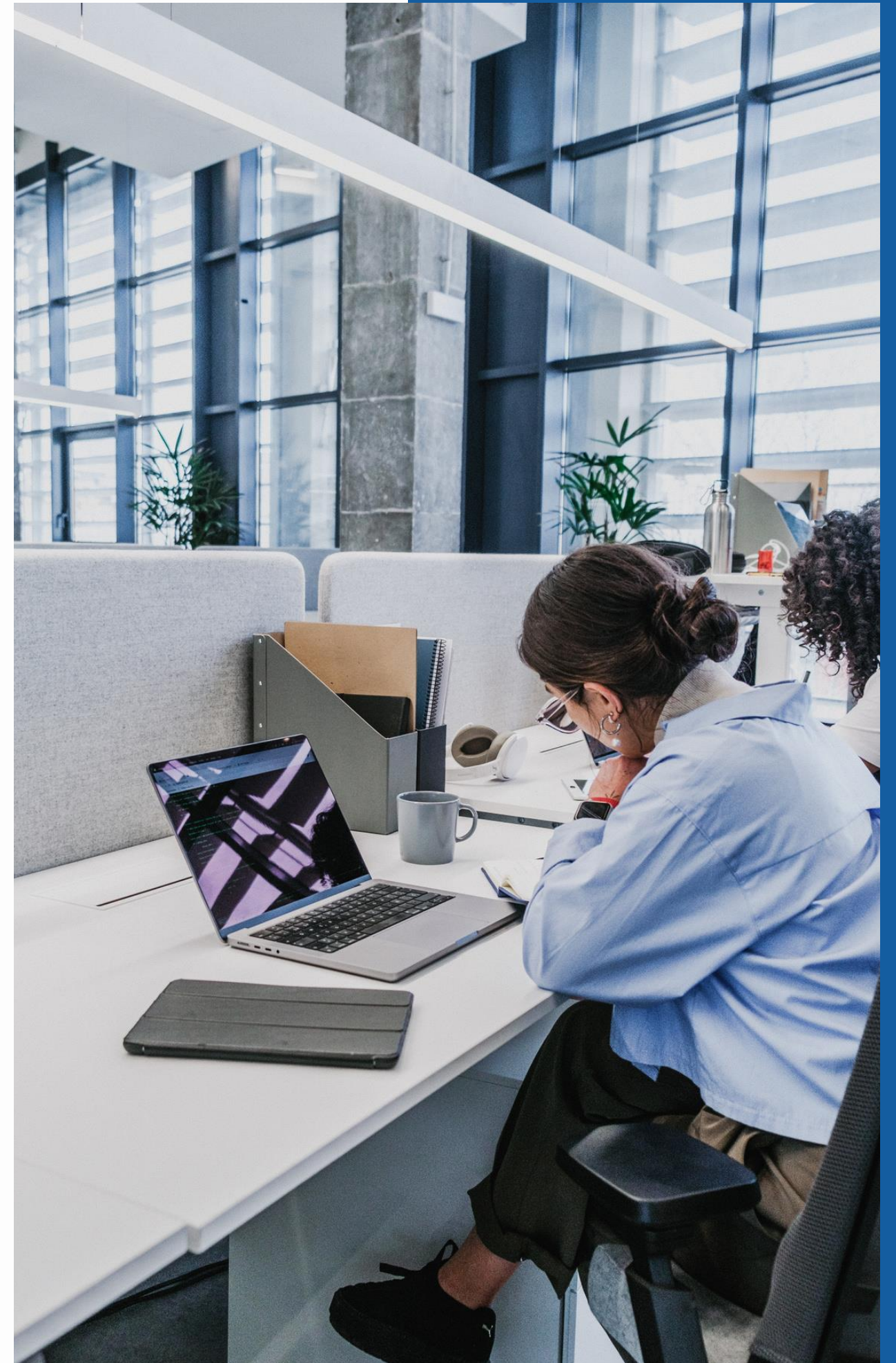


TRISPACE
TECHNOLOGIES

Services Pitch Deck

Overview

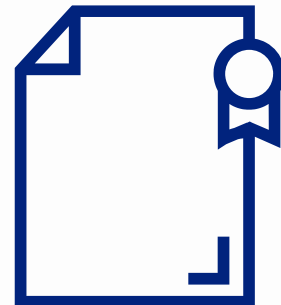
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About Company

- Established in December 2015, TriSpace Technologies was founded by N Vijay Anand, who serves as the Director.
- TriSpace has achieved breakthrough in battery optimization through innovative solutions.
- The company boasts of a dynamic team of over 20 fresh talents driving innovation.
- With an impressive track record, TriSpace Technologies has filed numerous patents and is recognized as one of the top 10 ICT startups.
- TriSpace is a registered partner by NXP, validating our commitment to excellence and innovation in engineering solutions.
- TriSpace Technologies proudly aligns itself as a member of the Voice of Indian Communication Technologies Enterprise (<https://voiceofindiancomm.com>)

Our Credentials



Patents

TriSpace Technologies secured 24 patents across India, the US, and China, with 8 of them successfully granted, showcasing our commitment to groundbreaking innovation on a global scale.



Awards

TriSpace Technologies, a top 10 ICT startup, earned the prestigious CII Best Patent award for three consecutive years—2021, 2022, and 2023 and also the National Intellectual Property Awards 2023 acknowledging our ongoing excellence in innovation.




Endorsement

NXP has officially recognized and endorsed TriSpace Technologies as a registered partner, solidifying a valued collaboration and affirming TriSpace's credibility in the industry.

Patents List

Patent Title	IN Patent Office	US Patent Office	CN Patent Office
A system and method for optimizaing power consumption in Mobile devices	Granted	Granted	Filed
A SYSTEM AND METHOD FOR OPTIMIZING POWER CONSUMPTION IN VOICE COMMUNICATION IN MOBILE DEVICES	Granted	Granted	Not Filed
A SYSTEM AND METHOD FOR OPTIMIZING POWER CONSUMPTION IN VIDEO COMMUNICATION IN MOBILE DEVICES	Granted	Granted	Filed
A SYSTEM AND METHOD FOR OPTIMIZING POWER CONSUMPTION IN MULTIMEDIA SIGNAL PROCESSING IN MOBILE DEVICES	Granted	Filed	Filed
A system and method for optimizing Bill of Material cost and power performance of platform System-On-Chip in Mobile device	Granted	Filed	Filed
A system and method for optimizing Bill of Material cost and power performance of platform System-On-Chip for Battery Management System	Filed	Filed	Not yet Filed
A system and method for optimizing Bill of Material cost and power performance of Graphics Processing Unit GPU core	Filed	Filed	Not yet Filed
A system and method for extracting tropological features from an image	Filed	Filed	Not yet Filed
A system and method for optimizing Bill of Material cost and power performance for Drones	Filed	Filed	Not yet Filed
A system and method for detection and identification of feature sub-image in a given image	Filed	Filed	Not yet Filed

PRODUCT DETAILS

- 1) Hardware platform used Open-Q 820 board, Snapdragon APQ8096/MSM8996 Hexagon SDK version 3.4.3**
 - 2) Codecs used for performance evaluation: WAMR voice codec**
 - 3) Solution developed:**
 - WAMRCodec library targeting and running on QDSP (10-11 mA TriSpace Technologies)**
 - WAMRCodec library targeting and running on ARM CPU core (6-7 mA TriSpace Technologies)**
 - 4) Firmware implementation and optimization targeting CPU are patent (CPU vendor agnostic) protected.**
 - 5) Our solution are cpu-dsp combo, cpu-cpu combo implementations based.**
- 

Services We Offer

01

Software development and firmware engineering expertise

02

Specialization in ARM and Intel architecture product development

03

Proficiency in DSP processor programming and assembly programming for power optimization

04

Concept engineering and C modeling

05

Prototyping capabilities and algorithm-architecture exploration

06

Domain expertise spanning DSP, audio, speech, video, and AI/ML applications

Implementation

1

Requirement and problem definition:
Thoroughly define the project needs and challenges.

2

Solution hypothesis:
Formulate a strategic solution approach based on the defined requirements.

3

Performance evaluation (memory and MCPS): Conduct an in-depth assessment of previous Performance metrics, focusing on memory usage and MCPS

4

Solution development:
Engage in comprehensive development based on the formulated solution hypothesis.

5

Testing and validation:
Rigorously test and validate the developed solution for functionality and efficiency.

6

Deployment:
Implement the finalized solution into the intended environment or system.

Engagement Models

FIXED PRICE

solution as a package

TIME AND MATERIAL

Manpower

EXTENDED DATA TEAM

Product Engineering Services



**TRISPACE
TECHNOLOGIES**

THANK YOU!

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