
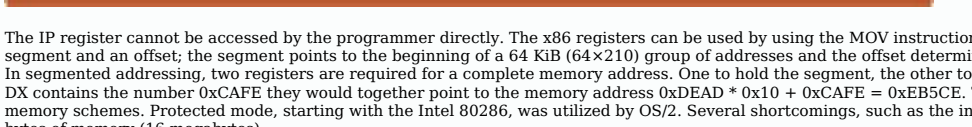


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I'm not robot


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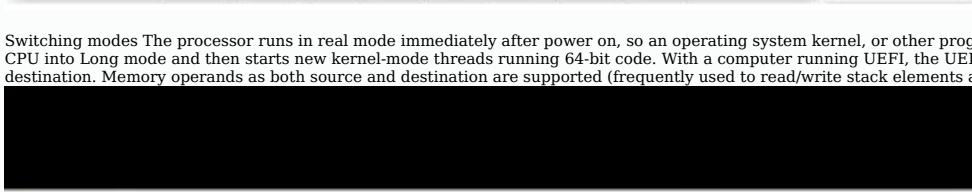
[illegible]

CMP instruction

- | | General Register | Memory Location | Constant |
|------------------|------------------|-----------------|----------|
| General Register | yes | yes | yes |
| Memory Location | yes | no | yes |

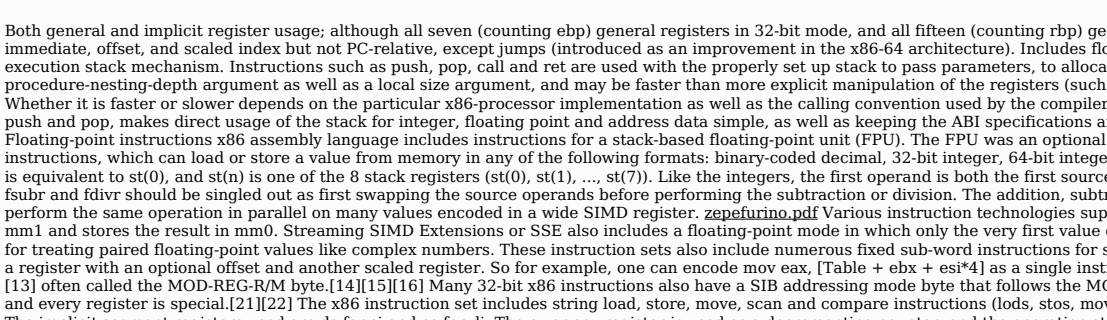
code, the segment selector can be broken down into two address fields: CS:IP (CS is Code Segment, IP is Instruction Pointer). The Intel 80386 featured three operating modes, and the protected mode may be the most important feature change. In this mode, in which some instructions are available and others are not, additional instructions provided in these modes to

The screenshot shows the BIOS setup utility with the 'Main' menu selected. The 'Virtualization' option is highlighted, and the 'Virtualization Technology' is set to 'Enabled'. The 'Virtualization' menu is expanded, showing options for 'Virtualization Technology', 'Virtualization Address Space', 'Virtualization Address Space Size', 'Virtualization Address Space Base', 'Virtualization Address Space Limit', 'Virtualization Address Space Offset', 'Virtualization Address Space Shift', 'Virtualization Address Space Mask', 'Virtualization Address Space Base Address', 'Virtualization Address Space Limit Address', 'Virtualization Address Space Offset Address', 'Virtualization Address Space Shift Address', 'Virtualization Address Space Mask Address'.



CMP / CPI

Easy to learn with Animation



CMP instruction (1 of 3)

- Syntax: (OSZCAP)
CMP destination, source
- Example: destination == source

[illegible]