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List of Publications

1. "Poly-Silicon Thin Film Transistors with Self-Aligned Cobalt Silicide and Nickel Silicide Source and Drain Contacts." **G. Sarcona**, M.J. Stewart, M.K. Hatalis. IEEE Electron Device Letters. **vol 20**, 332 (1999).
2. "Preparation and Stability of Low Temperature Cobalt and Nickel Silicides on Thin Polysilicon Films." R.H. Howell, **G. Sarcona**, M.K. Hatalis. J. Vacuum Science and Technology A. **vol 18(1)**, 87 (2000).
3. "Nickel Silicides Grown on Amorphous Silicon and Silicon-Germanium Thin Films." **G. Sarcona**, S. Saha, M.K. Hatalis. Electrochemical and Solid-State Letters, 1(5) 233 (1998).
4. "A SONY 32-bit / 64-bit Configurable RISC Processor Core." K.C. Lin, E. Wang, A. Yu, T. Kurata, A. Chang, D. Chen, T. Nguyen, M. Kawaguchi, H. Tung, **G. Sarcona**. 1996 Sony Research Forum Conference Proc.
5. "Polysilicon Thin Film Transistors with Cobalt and Nickel Silicide Source and Drain Contacts." **G. Sarcona**, M.K. Hatalis. Materials Research Society Symposium Proceedings, Spring 1996, Symposium H: Flat Panel Display Materials **vol 424**, 159 (1997).
6. "Low Temperature Flat Panel Display Driver Circuits in RTP Crystallized Polysilicon." M. Stewart, T. Afentakis, **G. Sarcona**, M.K. Hatalis. SID Symposium Digest of Technical Papers May 1999. **vol 30**, 460 (1999).
7. "Silicides for polysilicon TFT-LCD applications." R.S. Howell, M.J. Stewart, **G. Sarcona**, M.K. Hatalis. SPIE Proceedings, Feb. 1997, Symposium on Active Matrix Liquid Crystal Displays Technology and Applications. SPIE Proceedings **vol 3014**, 141 (1997).
8. "Low Temperature Silicides for Poly-Silicon Thin Film Transistor Applications." **G. Sarcona**, M.K. Hatalis. Materials Research Society Symposium Proceedings: Silicide Thin Films. **vol 402**, 191, (1996).
9. "Single Crystal Silicon Thin Film Transistors Fabricated at Low Process Temperatures on Glass Substrates." D.N. Kouvatsos, D. Tsoukalas, **G. Sarcona**, M.K. Hatalis, J. Stoemenos. Electronics Letters **32**, 775 (1996).
10. "Low Temperature Silicides for Thin Film Transistor Applications in Active Matrix Liquid Crystal Display Technology." **G. Sarcona**, M.K. Hatalis. IEEE Proceedings of the Second International Workshop on Active Matrix Liquid Crystal Displays, 1995 pp. 92. (1995).
11. "Sputtered Tungsten Gates for AMLCDs." M. Stewart, **G. Sarcona**, M.K. Hatalis. *ibid*, pp. 114.
12. "Thin Film Transistors Fabricated at Low Temperatures in Single-Crystal Silicon Films on Glass Substrates." D.N. Kouvatsos, **G. Sarcona**, D. Tsoukalas, M.K. Hatalis, D. Goustoridis, J. Stoemenos. *ibid*, pp. 119.
13. "Characterization of Cobalt Annealed on Silicon-Germanium Epilayers." F. Lin, **G. Sarcona**, M. K. Hatalis, A. F. Cserhati, E. Austin, D. W. Greve. J. Thin Solid Films **250**, 20, (1994).
14. "Amorphous Silicon and Silicon-Germanium Thin-Film Transistors Formed by Ion Implantation." **G. Sarcona**, M. K. Hatalis, A. Catalano. Materials Research Society Spring 1993 Symposium Proceedings, v. 297., 907.
15. "Saturation Mobility Study in Amorphous Silicon and Silicon-Germanium Thin-Film Transistors with Ion-Implanted Source and Drain Regions." **G. Sarcona**, M. K. Hatalis, A. Catalano. Proceedings of the Active Matrix Liquid Crystal Display Symposium 1993, Lehigh University, Ed.: M. K. Hatalis. pp. 85. Conference sponsored by ARPA, IEEE, and Soc. for Information Displays (SID).
16. "Electrical and Structural Properties of Cobalt Annealed on Silicon-Germanium Epilayers." **G. Sarcona**, F. Lin, M. K. Hatalis, A. F. Cserhati, E. Austin, D. W. Greve. Materials Research Society Fall 1992 Symposium Proceedings, v. 281. Ed.: C. W. Tu, D. C. Houghton, R. T. Tung. pp. 647.