

External Time Reference Interface

GPS 100 00

User's Guide

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1.0 EXTERNAL TIME REFERENCE OVERVIEW

The External Time Reference Interface (ETR) card provides a way to synchronize multiple Sequence of Events Recorder (SER) cards to an external time reference. The card also allows the time in the PLC's time of day clock to be synchronized with an external time reference. A time offset register is also provided to allow the ETR to adjust the time for the local time zone. When used with a Global Positioning Satellite (GPS) receiver, the ETR card provides both position and velocity information.

The ETR has two synchronizing inputs, a Global Position Satellite (GPS) receiver interface and a modulated IRIG-B interface. The GPS 300 00 GPS receiver that is supported by the ETR card is an integrated module containing a GPS receiver and antenna which are mounted in a weather proof plastic housing that can be located up to 250 feet from the ETR. The GPS receiver is powered by the Quantum PLC power supply through the ETR card. The GPS receiver communicates with the ETR through a five conductor twisted pair cable using RS 232 signal levels. The GPS 100 00 series ETR, which plugs into one of the I/O slots of a Quantum controller, comes with a Modsoft custom loadable function block and a Concept IEC function block which can be used to set the time in the PLC time of day clock.

When the ETR is first powered up it will try to synchronize with a GPS receiver and then the IRIG-B signal in that order. If neither signal is present the ETR will revert to its own crystal controlled clock. When the ETR has synchronized with a GPS receiver the current time and date will be correct. Since an IRIG-B time code signal does not contain year information, when the ETR has synchronized with an IRIG-B signal, the time, day and month will be updated from the IRIG-B signal, but the year will be obtained from the current PLC time of day clock setting.

Included in the time synchronization signal that is sent to the SER cards are a time quality indication. The time quality is calculated based on the synchronization mode of the ETR. When the ETR is first turned on, the time will be based on the time of day clock in the PLC. If the ETR is unable to read a valid time from the PLC, the time quality will be "BAD" indicating that no time reference is available. If a valid time is read, then the time quality will be set to "POOR" indicating an error of greater than 50 mS. If the ETR is synchronized with either a GPS receiver or an IRIG-B signal then the time quality will be "GOOD" indicating synchronization within 1 mS. If both of these signals are lost then the ETR will continue to maintain time based on the internal crystal oscillator. When a time period has elapsed sufficient for the crystal oscillator to have drifted more than 1 mS, the time quality will change to "FAIR" indicating an error of greater than 1 mS but less than 50 mS. When a time period has elapsed sufficient for the crystal oscillator to have drifted more than 50 mS, the time quality will change to "POOR" indicating an error of greater than 50 mS.

2.0 HARDWARE INSTALLATION

The GPS 100 00 ETR card plugs into an I/O slot of a Quantum controller. The card communicates with the controller through six 3X input registers and three 4X output registers.

The ETR card communicates with the SER cards through an RS-485 connection, which is made through a 4-pin connector on the front of the card.

The ETR card has two synchronization sources, which can provide a time reference. The interface for the Trimble Navigation Acutime II receiver is through a 9 pin D connector and the interface for a modulated IRIG-B signal is through a SMB connector, both of which are mounted on the front of the card.

2.1 MODSOFT

The GPS 100 00 card is supported under Modsoft version 2.6 and later, with no modifications. For earlier versions of Modsoft, a disk is provided with each card that contains the files:

QUANTUM.SYS GCNFTCOP.SYS

If this disk is not available, it can be downloaded from the Monaghan Engineering web site at www.monaghan-engineering.com.

The QUANTUM.SYS file contains the help screens for all of the Quantum modules. The version that is provided with the GPS card is the standard Modsoft version 2.51 QUANTUM.SYS, with support for the GPS 100 00 card added. This file should be copied to the C:\MODSOFT\RUNTIME subdirectory.

The GCNFTCOP.SYS file contains the I/O addressing information for all of the Modicon I/O modules. The version that is provided with the GPS card is the standard Modsoft version 2.51 GCNFTCOP.SYS, with support for the GPS 100 00 card added. This file should be copied to the C:\MODSOFT\RUNTIME subdirectory.

Under some circumstances it may be necessary to add support for the GPS 100 00 card to an existing GCNFTCOP.SYS file. This can be done by adding the following lines to the existing file, following the directions in the file header.

```
GPS 100 00,yyy,0,12,06,GPS/IRIG TIME SYNC,1,L0133,1,0000,0
```

where yyy is the module order number.

2.2 CONCEPT

The GPS 100 00 card is supported under Concept version 2.1 and later. The GPS card configuration data must be added to the Concept database. This is done by running the MODCONN.EXE utility provided with Concept. Select the FILE command, then the Open Installation File command and then load the MNENG.MDC file that is provided on the disk supplied with the GPS card. Then exit the program, start Concept and the GPS card will be available in the module selection list.

2.3 CARD OPERATION

After the card has been installed and the Traffic Cop information has been entered, the Active light should on. The ETR has three other indicator lights, which are defined as follows:

GPS This light will flash when a GPS receiver is sending 1 pulse per second pulses to the ETR card and will be on steady when the GPS receiver has locked on to at least three satellites which are providing accurate time and position, and the ETR has selected the GPS signal as a time source.

IRIG-B This light will be on when the ETR has decoded a valid IRIG-B signal and has selected the IRIG-B signal as a time source.

Internal This light will be on when the ETR is using its internal crystal as a time source.

2.4 HOT STANDBY

The GPS 100 00 card can be used in a hot standby application. If redundant GPS 100 cards are used, the synchronization outputs of each card should be wired in parallel. When the GPS 100 card is powered up, it enters into the slave synchronization mode. The GPS 100 will monitor the synchronization bus for time synchronization messages from other time sync masters. If another GPS 100 is currently sending

synchronization messages the card will continue to monitor the bus. When time sync messages are not received for 10 seconds the GPS 100 card will enter the master mode and start sending time sync messages.

2.5 SER CARD CONNECTION

In order to synchronize the time of multiple SER modules with an External Time Reference module, the RS-485 time synchronization bus must be connected between all of the modules. Wiring within a single I/O rack may be done with unshielded wire, but for long runs between I/O racks a twisted pair shielded cable must be used. The RS-485 bus connects to pins 38 - 40 of the front wiring connector on the 800 Series SER card, pin 2 - 4 of the Quantum SER card, and to pins 2 - 4 of the plug connector on the ETR card. On the GPS 100 00 card, pin 1 is located toward the bottom of the card. The wiring is as follows:

ETR	800 Series SER #1	800 Series SER #2	Quantum SER #3	Quantum SER #N	Signal Name
2	38	38	2	2	A
3	39	39	3	3	B
4	40	40	4	4	Ground

2.6 REGISTER ASSIGNMENTS

The GPS 100 00 contains six input registers and three output registers. The first 4 input registers and the first output register contain information that can be used directly by PLC logical functions. The last two input and output registers are used by the GPS loadable function to access internal information on the GPS 100 00 card.

2.6.1 SER INPUT REGISTER 1

The first input register contains the Signal Source information from the GPS 100 00 card.

- Bit 1 – GPS receiver selected as source
- Bit 2 – IRIG-B selected as source
- Bit 3 – Reserved for future use
- Bit 4 – GPS receiver 1PPS signal present
- Bit 5 – GPS receiver time sync message present
- Bit 6 – Reserved for future use

2.6.2 SER INPUT REGISTER 2

The second input register contains the current millisecond value.

2.6.3 SER INPUT REGISTERS 3 & 4

The third and fourth input registers contain a 32 bit long integer, which represents the time of day in seconds since January 1, 1984.

2.6.4 SER INPUT REGISTERS 5 & 6

The fifth and sixth input registers are used by the GPS loadable to access the internal registers on the GPS 100 00 card.

2.6.5 SER OUTPUT REGISTER 1

The first output register is used to adjust the time to the local time zone. The output from the GPS receiver and the output from some IRIG-B time sources is set to Universal Coordinated Time (UCT). The value in

this register is added to the time from either the GPS receiver or the IRIG-B signal to obtain the local time. The acceptable range of values is from -23 to +23 hours.

2.6.6 SER OUTPUT REGISTERS 2 & 3

The second and third output registers are used by the GPS loadable to access the internal registers on the GPS 100 00 card.

3.0 SOFTWARE

Software support is provided for the GPS 100 00 card for 984 Ladder Logic programming in Modsoft, ProWORX NxT and Concept, and IEC Function Block programming in Concept. The following manuals provide detailed information on programming for the GPS 100 00:

984 Ladder Logic Function Block User's Guide	Publication 10011
IEC Function Block User's Guide	Publication 10012