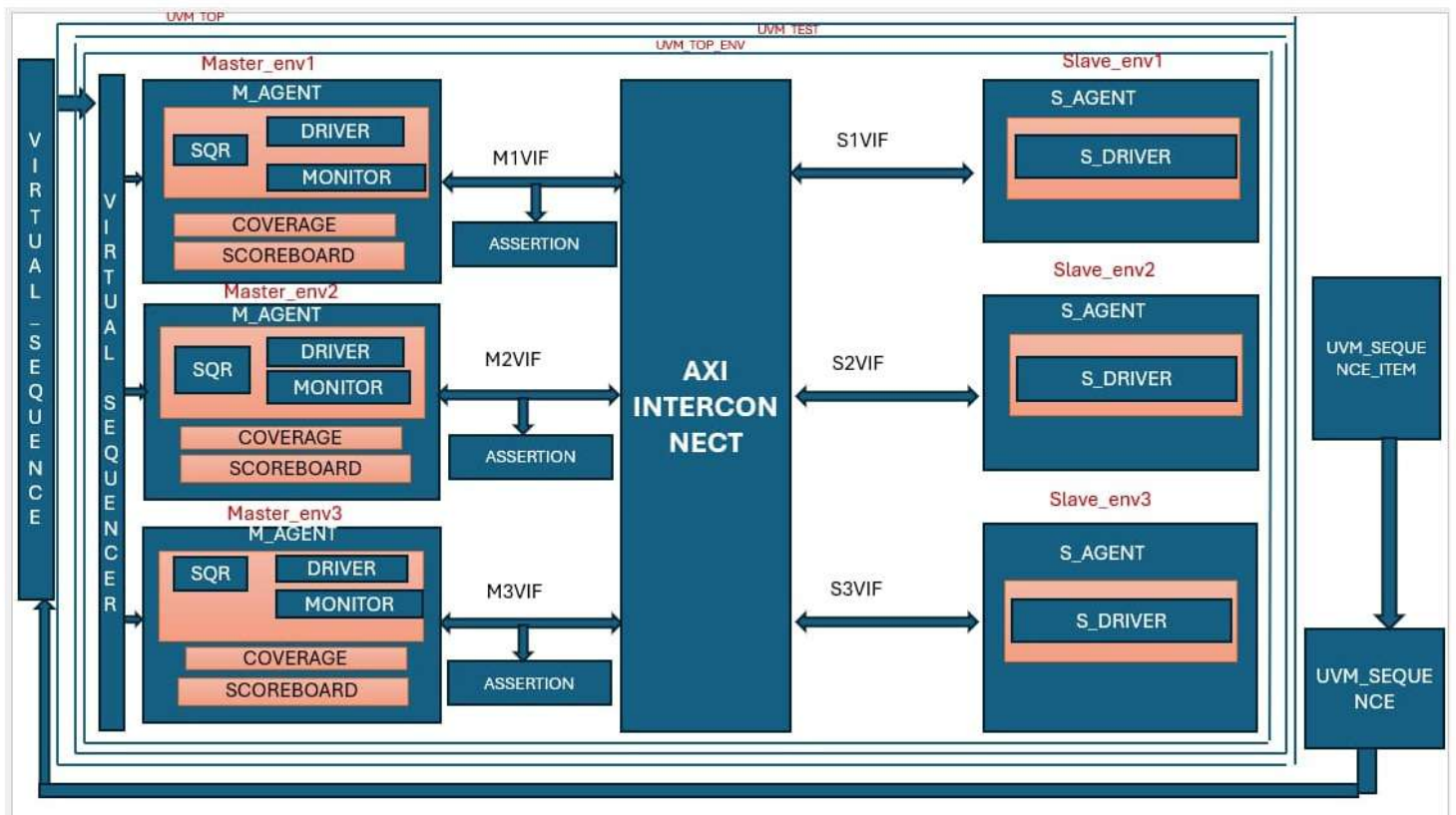




On-chip AMBA AXI4 multiple masters and slaves VIP implementation using UVM (pre silicon)