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1. introduce

1.1 Introduction to the PA-800 LITE UFS programmer

The PA-800LITE UFS programmer is designed to meet the engineering needs of data replication and verification of 1 to 8 UFS chips simultaneously. PA-800LITE UFS programmer speed can be set to Gear 1B, Gear2A. It can help you complete chip data copying and verification faster.

Our PA-800 LITE UFS programmer can be used with USB 3 without the need for a master as a source 0 The online mode reads data from the host PC, and you can also use the host computer to manage the project files.

PA-800LITE UFS Programmer also provides a convenient interface, just follow the steps, it will make your job easier than you think!

1.2 features

- Easy to operate, no master is required for operation. Save the parameters and data of the master card by creating a project, and engineer the production in the way of executing the project, so that the time of programming and calibration is shorter and easier to manage.
- 8 stations including a master station.
- Supports UFS 2.0, 2.1, 2.2, 3.0, 3.1.
- Controllable project setting, setting BootLun and Lun partitions based on the master card and writing data, controllable project setting, flexible project arrangement.
- Use Donghui's host computer software to control the programmer, perform engineering and project management.
- The PA-800Lite UFS programmer has a USB 3.0 port to connect to a standard Windows computer.
- Windows programs offer better user interface features and powerful features that make work easy.
- The copy speed can be set up to Gear2A with a throughput of approximately 180MB/s.

1.3 specification

Copy capacity	8 seats	
power supply	75W 115V/240V	
Computer interfaces	USB3.0,180MB/s	
size	260x169.5x50 mm	
weight	2. 5 kg	

2. Package contents

☑PA-800Lite UFS programmer 1/set

☑USB3.0 cable (1.5 m)...... 1/pcs

Power cord (1 5m) 1/pcs

☑UFS test mount (BGA153) / set

☑Handbook.. 1/pcs

3. Accessory selection

4. System settings

4.1 Hardware installation

The device needs to be linked to 220V power supplies, and the computer is connected to USB after startup

The device can be found in Serial Devices in Device Manager.

4.2 Software installation

Directly execute the installation package of the PA800lite.

4.3 Installation environment and system condition

Requires .NET4 7. 2 environment

5 Operation process

- A. The programmer uses USB3.0 to link the PC and power boot.
- B. If there are more than 2 programmers, use the host computer software to specify the programmer sequence, and do not specify the sequence for one programmer.
- C. First use the master station setting of the main sequence to set the working environment.
- D. A new project generated using the master of the master station on the programmer from the main sequence.
- E. Confirm the project settings.
- F. Confirm that the test stand is installed.
- G. Start the project.
- H. Put in the UFS chip.
- I. Perform engineering
- J. Confirm the quantity of work completed and remove the corresponding completed UFS chips.
- K. Reinsert the UFS chip that needs to be engineered to continue the project.
- L. The cycle repeats until the project is completed, and the chip is removed to finish the work.

6 Operational considerations

Put the chip into the Master station, use the Master parameter setting to adjust the working environment of the device, it is recommended to use the settings in the IC list, if the chip model is not included in the list, first use the basic settings into the Partition wizard to test that there is no problem in reading and writing and then use the New Project to create a project to run. If there is a problem with the read and write test, use the master parameter setting to adjust the working environment and continue to enter the partition wizard to do the read and write test. Once everything is good, create a project with New Project to run.

7 Interface operation

Create a new project



点击 Make New project

👽 Save to project					×
← → ✓ ↑ 🤤 → 此电脑 → 本地磁盘	(E:) → ufs8lite →	~	ひ 在 ufs8lite 中	搜索	Q
组织 ▼ 新建文件夹				·	2
桌面	^ 名称 [^]	修改日期	类型	大小	
🏪 办公 (C:)	- Image	2022/10/15 12:02	文件夹		
🚘 本地磁盘 (D:)	Log	2022/10/15 11:26	文件夹		
本地磁盘 (E:)	PA800Lite	2022/10/4 15:27	文件夹		
🕳 娱乐 (F:)	🗋 HBG4aA283.prj	2022/10/4 15:50	PRJ 文件	1 KB	
🕳 文档 (G:)	KLUDG4UHDB-B2E1fb19.prj	2022/10/15 12:17	PRJ 文件	2 KB	
🕳 数据 (H:)					
🕳 本地磁盘 (l:)					
— 软件 (J:)					
FPGA (K:)					
→ 新加卷 (L:)					
Samsung_T5 (M:)					
C TE (NA.)	×				
文件名(N): KLUDG4UHDB-B2E1b8	31e.prj				~
保存类型(T): Project files (*.prj)					~
▲ 隐藏文件夹			保存(S)	取消	

It will read the chip on the master station of the specified host, and then create a .prj file with the chip name to save the chip settings and the data in the chip.

The parameter setting of the New Project is based on the parameters of the Master station.

If the chip's bConfigDescrLock is started, the project is also set according to the chip settings, so it is recommended to use the Open Project to verify the settings once after the new project is completed. **bConfigDescrLock is a register that locks UFS settings at one time.**

If it is a blank master, only save the settings as an item, and a notification will pop up without data

Notify	×
Used area is 0, contir	nue anyway ?
确定	取消

Press OK to finish saving the project.

Make Null project



Save the settings of the chip into the project without saving the data

👒 Save to project					\times
	北电脑 > 桌面 > PA800Lite1.0.1.5 >	~ む 在	PA800Lite1.0.1.5	中搜索	Q
组织 ▼ 新建文件夹					?
OneDrive - Persi	名称	修改日期	类型	大小	^
山中時	HBG4a01C2.prj	2023/2/21 16:38	PRJ 文件		
	HBG4a01C2_null.prj	2023/2/21 16:27	PRJ 文件		
→ 3D 対象	KLUDG4UHDB-B2E1fb19_null.prj	2023/2/20 16:05	PRJ 文件		
🔡 视频	🔳 HBG4aF9A5.prj	2023/2/20 14:44	PRJ 文件		
▶ 图片	KM8F9001JM-B8132b35_null.prj	2022/12/8 23:07	PRJ 文件		
🔮 文档	KM8F9001JM-B8138525_null.prj	2022/12/8 22:36	PRJ 文件		
↓ 下载	KM8F9001JM-B8135d25_null.prj	2022/12/8 22:21	PRJ 文件		
1 音乐	KM8F9001JM-B8131933_null.prj	2022/12/8 21:53	PRJ 文件		
「「」「「」」「」」	🧾 H9HQ16AECMMDAR8A2F母片.prj	2022/12/7 16:20	PRJ 文件		
<u>ж</u> щ	************************************	0000 //0 /7 / 0 07	001 - //		>
- OS (C)					_
文件名(N): HBG	i4a6201_null.prj				\sim
保存类型(I): Proje	ect files (*.prj)				~
▲ 隐藏文件夹			保存(<u>S</u>)	取消	

Open Project Settings



Click Open Project

🗢 Open project					×
	› 本地磁盘 (E:) › ufs8lite ›		ٽ ~	在 ufs8lite 中搜索	م
组织 ▼ 新建文件夹					?
➡ 下载	^ 名称 [^]	修改日期	类型	大小	
♪ 音乐	- Image	2022/10/15 12:02	文件夹		
	Log	2022/10/15 11:26	文件夹		
🏪 办公 (C:)	PA800Lite	2022/10/4 15:27	文件夹		
🔜 本地磁盘 (D:)	🗋 HBG4aA283.prj	2022/10/4 15:50	PRJ 文件	1 KB	
🔜 本地磁盘 (E:)	KLUDG4UHDB-B2E1fb19.prj	2022/10/15 12:17	PRJ 文件	2 KB	
🕳 娱乐 (F:)					
🕳 文档 (G:)					
🕳 数据 (H:)					
🕳 本地磁盘 (I:)					
🕳 软件 (J:)					
EPGA (K:)					
🕳 新加卷 (L:)					
🕳 Samsung_T5 (M:)					
	~				_
文件名(N):	KLUDG4UHDB-B2E1fb19.prj		~	Project files (*.prj)	~
	L			tT开(O) 取迷	
				11/1(0) 取用	

Selecting the specified .prj file will bring up the settings

KLUDG4UHDB-B2E1fb19_null.prj
HS Gear O HS-Gear 1 Rate B
Tx HS Sync
🗌 FUA bit OutBuffer 8000 🗸
🗹 CheckProductName 🗌 CheckProductRevision
bBootLunEn O Boot disabled Boot LU A O Boot LU B
bRefClkFreq O 19. 2MHz O 26MHz O 38. 4MHz
CheckDeviceLifeEst DeviceLifeTimeEst 10% 20% 30% 40% 50%
CheckRPMBCounter
🗹 Program 🛛 Verify
OK Cancel

bConfigDescrLock is a register that locks UFS settings at one time.

If the chip has problems writing, it is recommended to adjust the TX HS Sync (data checksum) option from 4 A to 4 F, with 4 A being the fastest 4F For the slowest, the write cache OutBuffer ranges from 8 000 to 1 000, 8000 is the fastest and 1 000 is the slowest and some UFS chips need to be slower to be stable.

Press OK to complete the setup.

FUA bit is checked for some chips that cannot be written normally.

FUA bit: Write directly to memory without caching.

Execute the engineering plan

Click Run Project

PA800Lite 1.0.22
 File Project Master Configure Report Language
 Image Image

Select Specify schedule

🤏 Open project					×
← → ∽ ↑ 🔒 → 此电脑 →	本地磁盘 (E:) > ufs8lite >		5 V	在 ufs8lite 中搜索	Q
组织 ▼ 新建文件夹					0
➡ 下载	^ 名称 [^]	修改日期	类型	大小	
🎝 音乐	Image	2022/10/15 12:02	文件夹		
三 桌面	Log	2022/10/15 11:26	文件夹		
🏪 办公 (C:)	PA800Lite	2022/10/4 15:27	文件夹		
🕳 本地磁盘 (D:)	🗋 HBG4aA283.prj	2022/10/4 15:50	PRJ 文件	1 KB	
👝 本地磁盘 (E:)	KLUDG4UHDB-B2E1fb19.prj	2022/10/15 13:07	PRJ 文件	2 KB	
🔜 娱乐 (F:)					
🕳 文档 (G:)					
🕳 数据 (H:)					
🕳 本地磁盘 (l:)					
🕳 软件 (J:)					
EPGA (K:)					
🔜 新加卷 (L:)					
🕳 Samsung_T5 (M:)					
	~				_
文件名(N): k	KLUDG4UHDB-B2E1fb19.prj		~	Project files (*.prj)	~
				打开(O) 取消	
				337T(O) 40/F	

The Online Project interface then pops up

© 04000111- 4.0.22				
S PABUULITE 1.0.22			-	ЦX
File Project Master Configure Report Langu	age			
🚰 🛲 🖉 🌮 🐑 🕦 🐳 🗶 🖉				
C0000051		C0000047	1	
Check SUM:0	x3CE62C16		Check SUM: 0x3CE62C16	
2 4 6 8 Put IC		2 4 6 8	Put IC	
1 3 5 7		1 3 5 7		
	OK Cancel		OK Cance	1
C0000051	TON CONJUNCT OF THE OWNER	NY2 14 14 14 14 14	170 00 1802	

Click the OK button of the corresponding programmer interface to start the project.

The shortcut keys for OK are 1, 2, 3, and 4 to start the project.

☞ PA800Lite 1.0.22			- 🗆 X
File Project Master Configure Report Langua	ge		
🚼 🛲 😹 🂣 🧈 🔁 🕦 🐳 🗶 🜌			
C0000051		C0000047	
2 4 6 0		2 4 6 0	
1 3 5 7		1 3 5 7	
26% Completed 151.00 MB/S		19% Completed 148.00 MB/S	
Program 23C00000	Cancel	Program 1C400000	Cancel
C0000051			

Once the work is done

Se PA800Lite 1.0.22	- 0	×
File Project Master Configure Report Language		
🚼 🚍 🜉 💣 🤌 두 🌐 ∓ 💥 🛩		
C0000051 C0000047		
Success : 3, Failed : 0 P+Y end Success : 1, Failed : 0 P+Y	/ end	
2 4 6 8 Take Out IC 2 4 6 8 Take Out IC		
1 3 5 7 1 3 5 7		
100% Completed 123.00 MB/S 100% Completed 123.00 MB/S		
Flaned: 00:00:36 OK Cancel Flaned: 00:00:36 OK C	ancel	
алаузан. 00.00.00		
2000051		

Remove the IC and click OK to go back

September 2017 Septem	- 🗆 X
File Project Master Configure Report Language	
🕌 🛲 🚂 💣 🧇 두 🕤 🗣 💥 🖉	
C0000051	C0000047
Check SUM: 0x3CE62C16 Put IC	Check SUM:0x3CE62C16 Put IC
2 4 6 8	2 4 6 8
1 3 5 7	1 3 5 7
100% Completed 123.00 MB/S	100% Completed 123.00 MB/S
Elapsed: 00:00:36 OK	Elapsed: 00:00:36 OK Cancel
0000051	

You can insert the chip and click OK to continue the next project.

Click Cancel

<complex-block><complex-block></complex-block></complex-block>	PA800Lite 1.0.22	- 🗆 X
<image/> <complex-block><complex-block><complex-block><complex-block><image/><image/><image/><image/><image/><image/><image/><image/></complex-block></complex-block></complex-block></complex-block>	ile Project Master Configure Report Language	
<complex-block></complex-block>	🖢 🛲 💣 🤌 🔄 🕤 🕤 🐳 💥 🛛 🖝	
COUCDD1 Image: Council of the same of the sa		
2 4 Finish Job? 1 3 Finish Job? 100% Completed add2 RCH 2 4 Image 2 4 Image 2 4 Image 100% Completed Image RCH 2 1 Image RCH 2 1 Image RCH 2 1 Image RCH 100% Completed Image RCH Image RCH RCH 1 Image	C0000051	C0000047
2 4 1 3 Finish Job? 100% Completed 100% Completed 100% Completed 100% Completed 2 4 1 3 Finish Job? 100% Completed 100% Completed 100% Completed 100% Completed 2 4 1 3 Finish Job? 100% Completed 100% Completed 100% Completed 100% Completed 2 4 1 3 Finish Job? 100% Completed 100% Completed 100% Completed 100% Completed 20000035 100% Completed 100% Completed 100% Completed 100% Completed 2000051 100% Completed 100% Completed 100% Completed 100% Completed 100% Completed	Message × 16	Message × 16
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100% Completed NOM Bagsed: 00:00:38 Cancel	1 3 Finish Job?	1 3 Finish Job?
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Zlapsed: 00:00:36 um Cancel Elapsed: 00:00:36	100% Completed 确定 取消	100% Completed 确定 取消
0000051	Elapsed: 00:00:36 Cancel	Elapsed: 00:00:36
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	200051	

A Finish Job appears

Select OK or Cancel to turn off the corresponding programmer

Separation Separat	- 0 X
U File Project Master Configure Report Language	
	C0000047
]	Message × ¹⁶
	1 3 Finish Job?
â	100% Completed 确定 取消
	Elanzed: 00:00:36 on Cancel
C0000051	

Only format not configure

🤏 PA	800Lite 1.	0.22			
File	Project	Master	Configure	Report	Language
1	- I	i)	1	\mathbf{N}	
	/	0	only Format n	ot Configu	re

After selecting the same items as the chip, you can erase all LUNs that have been turned on



The shortcut key Q opens Windows, ESC closes Windows.

After clicking, a window will open showing the chip information of the 8 stations

Chip View			×
2	4 ProductNume : HBGGa 4842473461 OEMII: 01AD ProductRevision : SpeoVersion : 2.00 Manufacture=Name : SKONMIX Manufacture=Name : SKONMIX	6	8 ProductName : HBG4a 4342473461 OEMID : 01AD ProductRevision : SpeeVersion : 2.00 ManufactureFName : SNGNIK ManufactureDate : 05/2016 PermanentWEn : 0 PermanentWEn : 0 PermanentWEn : 1 ConfigNeerLock : 0 RFMB Counter : Authentication Key not yet programmed TotalRewDericeCapacity : 30,524008 LUM 0 : 30,500008 LUM 0 : 30,500008 LUM 2 : Boot A 4005 LUM 3 : 6005
1 ProductName : HBG4a 404273461 OBMID : O1AD ProductHevision : SpeeVersion : 2.00 ManufactureName : SIGNINIX ManufactureName : SIGNINIX ManufactureDate : 05/2016 PermanentVFEn : 0 PermanentVDisableFuJdate : 0 BootLumEn : 1 ConfigUescrLock : 0 RTMD Counter : Authentication Key not yet programmed TotalRawDeviceCapacity : 30,524MEB LUN 1 : Boot A 40EB LUN 2 : Boot B 40EB LUN 3 : 60ME	3	5 FroductName : HBC4a 4942473401 OZMID : OIAD FroductRevizion : SpeeVersion : 2.00 ManufacturePame : SURTNIX ManufacturePame : SURTNIX ManufacturePame : 05/2016 PermamentPFEn : 0 PermamentPfEn : 0 PermamentPfEn : 0 BootLunEn : 1 ConfigDesorLock : 0 RYME Counter : Authentication Key not yet programmed TotalRewDeviceCapacity : 30,524MB LUN 0 : 30,500MB LUN 1 : Boot A 4MB LUN 2 : Boot B 4MB LUN 3 : 6MB	7

Click on UFS Descriptor



For chip information on the master station of the specified programmer, please refer to

🛃 UFS Descriptor		– 🗆 X
Device Descriptor Configurati	on Descriptor Geometry Descriptor	Unit Descriptor RFMBUnit Descriptor Device Health Descriptor
Device : O DeviceClass : O	SpecVersion : 3.10 ManufactureDate : 10/2020	DeviceVersion : O NumSecureWPArea : 20
DeviceSubClass : O Protocol : O NuclearIV : 2	ManufacturerName : SAMSUNG ProductName : KLUDG4UHDB-B2E1	PSAMaxDataSire : O PSAStateTimeout : O ProchoeDeniciesTerrol : A
NumberVLU : 3 NumberVLU : 4 BeetKroble : 1	DemID : 3	rroductkevisionLevel : 4 ExtendedUFSFeaturesSupport : 101 WwitzBoostevEngEferProcessedUcerScopeEn : 0
DescrAccessEn : O InitPowerMode : 1	WDOBaseOffset : 16 WDConfigPLength : 1A	WriteBoosterBufferType : O NumSharedWriteBoosterBufferAllogUnits : O
HighPriorityLUN : 7F SecureBemovalType : 0	DeviceRTTCap : 2 PeriodicBTCUndate : 0	UniproVersion : 1.80 WnbyVersion : 4.10
SecurityLU : 1 BeckgroundOnsTorpLet : 4	UFSFeaturesSupport : 1	RefClkFreq : 1
InitActiveICCLevel : 0	QueueDepth : 20	ConfigDesorLock : 0

the UFS datasheet for the specific effect of each register

🕕 UFS Descriptor					-	×
Device Descriptor C	onfiguration Descripto	r Geometry Descriptor	Unit Descriptor	RPMBUnit Descriptor	Attributes	
Medi aTechnology	: 0	MaxContexIDNumber : F	Enł	nanced3MaxNAllocU : O		
TotalRawDeviceCa	pacity : 31, 256, 576KB	SysDataTagUnitSize : 0	Enł	nanced3CapAdjFac : 0		
		SysDataTagResSize : 0	Enł	nanced4MaxNAllocU : 0		
SegmentSize : 20	00	SupportedSecRTypes : 9	Enł	nanced4CapAdjFac : 0		
AllocationUnitSi	ze : 1	SupportedMemoryTypes : 8	009			
MinAddrBlockSize	e : 8	SystemCodeMaxNAllocU : 0				
OptimalReadBlock	Size : 80	SystemCodeCapAdjFac : O				
OptimalWriteBloc	kSize : 80	NonPersistMaxNAllocU : 0				
MaxInBufferSize	: 40	NonPersistCapAdjFac : 0				
MaxOutBufferSize	e : 40	EnhancediMaxNAllocU : 1D	CF			
RPMB_ReadWriteSi	ze : 20	Enhanced1CapAdjFac : 200				
		Enhanced2MaxNAllocU : 0				
DataOrdering : 0)	Enhanced2CapAdjFac : 0				

Click Configuration Descriptor

 UFS Descriptor 				-		\times
Device Descriptor Configuration Descr	riptor Geometry Descriptor	Unit Descriptor	RPMBUnit Descriptor	Attributes		
Configuration Desor Index 0 Configuration Desor Index 0 Configuration Desor Index 0 Configuration InitPowerMode HighFriorityLUN SecureRemovalType InitActiveICCLevel PeriodicRTCUpdate UNN 0 Configurable UNN 0 Configurable UNN 0 Configurable UNN 0 LUWriteFrotect MemoryType NumAllocUnits DataReliability LogicalBlockSize ProvisioningType ContactConabilities Save Configure Config	pure From File Confi	gure Only LUNO	ConfigDezorLoo)	c Conf	îi gure	

UFS settings can be modified via Configuration Descriptor, saved settings via Save Configura, and loaded via Configura From.

bConfigDescrLock is a register that locks UFS settings at one time.

(i) UFS Descriptor				-		×
Device Descriptor Configuration Descriptor	Geometry Descriptor	Unit Descriptor	RPMBUnit Descripto	or Attributes		
 Configuration Desor Index 0 ConfDesoContinue BootEnable DesorAccessEn InitPowerMode HighPriorityLUN SecureRemovalType InitActiveICCLevel PeriodicKICUpdate LUN 0 Configurable DUTable BootLunID LUWriteProtect MumAllocUnits DataReliability LogicalBlockSize ProvisioningType 	~	Value De Ox01 The lu	scription m is enabled			
Save Configure Fr	om File Confi	gure Only LUNO	ConfigDescrLo	Coni	îi gure	

To set LUNs, toggle LUNs

When you resize Lun, if you exceed the maximum value that can be set, you will be prompted for the maximum value available.

ice Descriptor Configur	ation Descriptor	Geometry Descriptor	Unit Descriptor	RPMBUnit Descriptor	Attribute	s	
Configuration Desor ConfDesoContinue BootEnable DesorAccessEn InitFowerMode HighPriorityLUN	Index O	^	Value 0x00009999	umber of Allocation	Descripti Units. LUN	on Capacity =	31, 2
 SecureRemovalType InitActiveICCLeve PeriodicRTCUpdate LUN O Configurable LUEnable BootLunID LUWriteProtect MemoryUrme 	Error The total enable could use is 0x10	LUN size 0x2666400000 is	great than device	capacity 0x773C00000	×		
NumAllocUnits DataReliability LogicalBlockSize				确定			

Note: After all settings are adjusted, click the Configura button to write the settings into the UFS chip.

Clicking Configure Only Lun0 button, will turn on only Lun0 to the maximum capacity of the chip.

Before using this function, it is best to use Save Configure to save the original settings of the chip in use, so as not to be unable to restore.

🕕 UFS	Descriptor				-		\times
Device	Descriptor Configuration Descriptor	Geometry Descriptor	Unit Descriptor	RPMBUnit Descriptor	Attributes		
	Configuration Descr Index O						
÷	LUN O Configurable						
. ⊥	LUN 1 Configurable						
±	LUN 2 Configurable						
····	LUN 3 Configurable						
····	LIN 5 Configurable						
- -	LUN 6 Configurable						
÷	LUN 7 Configurable						
		× .					
			JL				
_		`					
	Save Configure Fro	om File Confi	gure Only LUNO	ConfigDescrLock	Config	gure	

For the specific effect of each register, please refer to UFS datasheet

Attributes page

You can adjust the chip operating frequency and other settings, enter the corresponding value and enter to complete the setting

 UFS Descriptor
 ×

 Configuration Descriptor Geometry Descriptor Vnit Descriptor RFMEVnit Descriptor Device Health Descriptor Attributes
 •

 bBootLumEn
 Value
 De...

 bResCLAFFeeg
 0x01 26ME/z

0x00=19.2Mhz , 0x01=26Mhz

Click partition



There will be Lun and partition within UFS

Partition			_	×
INN O	Partition Name	Start Address	Size	^
LUN 1	GPT Header O	0x0	0x6000	
LUN 2	aop	0x6000	0x80000	
LUN 3	tz	0x86000	0x200000	
	hyp	0x286000	0x800000	
	hypbak	0xA86000	0x800000	
	modem	0x1286000	0x9000000	
	bluetooth	0xA286000	0x100000	
	abl	0xA386000	0x100000	
	dsp	0xA486000	0x2000000	
	keymaster	0xC486000	0x80000	
36 items				.::
Whole LUN				
Start Address Ox 0		Length Ox	6000	
Write Verify		Save]	
	Close			

The partition partition in Lun is divided according to the GPT header, the GPT header indicates the size of the partition (the beginning and end of the data position), and if there is a compliant GPT header in Lun , Partition Wizard will parse the GPT header so that the corresponding partition can be read and written.

Check Read the entire LUN from Address 0 or Write a file from Address0 to Lun.

Writing and checking based on the bin file is based on the selected or consecutive address start and address length in the partition partition, otherwise the selected Partition Name will be used The starting address and length of are primary

Save a single bin file radio Partition Name and save it, if you want to save a bin starting to a certain address length file, use SHIFT+left-click to directly select Start Partition Name, and then select End with SHIFT+left-click Partition Name can be saved after consecutive selection, or use CTRL+left-click to check that the address must be consecutive

Partition			_	×
LUN O	Partition Name	Start Address	Size	
LUN 1	No Partition	0x0	0x800000	
LUN 2				
IN 3				
1 items				.::
Whole LUN				
Start Address 0x 0		Length 0x 800	000	
Write Verify		Save		
	,			
	Close			
				.:

In the absence of a GTP header, select the write address by selecting the start address through the Start Address, and fill in Length. The start address cannot exceed Lun's Size, if the data written exceeds the set Size, the software will automatically help you intercept the part that does not exceed the set Size (starting from the head of the written data) and ask whether to write.

Tip: Start Address and Length are filled in in 16 decimals open with 0x.

1MB=0x100000, 1GB=0x40000000

Click Parameter



Parameter Settings appear

Master Settings	×
Select Chip	
	Auto Select
	Update List
HS Gear	
○ HS-Gear 1 Rate B	● HS-Gear 2 Rate A
Tx HS Sync	
● 4A ○ 4B ○ 4C	○ 4D ○ 4E ○ 4F
🗌 FUA bit	OutBuffer 8000 🗸
OK	Cancel

Click Auto Select If the UFS chip on the Master station is a model that we have known to have tested, it will be automatically put into our recommended machine operating environment settings.

Press OK to complete the setup.

FUA bit: Write directly to memory without caching.

FUA bit is checked for some chips that cannot be written normally.

Clicking the upgrade list will update the settings of the chip model to the IC list, and if the same model is the same, the settings in the original IC list will be overwritten.

The selection parameters are different to determine the working mode of the master station.

The parameter setting of the New Project is based on the parameters of the Master station.

Click Assign Machine



The Specify Machine Sequence screen appears



At the same time, the green light of the programmer's station will flash to confirm the specific programmer, and click the window to specify the information window of the equipment work.



The programmer designated as the upper left window is the main machine, and the chip that wants to read and write the master station is the chip that reads and writes the master station of the main machine.

Delete the device assignment



Click to delete the specified sequence, and then execute the project, the device sequence is randomly assigned.

If you set an over-programmer sequence, then plug in an programmer that is not outside the sequence number

	-	×
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No device appears

Click



Can be connected to devices outside the sequence

Click Report



The Report will appear

Report		×
Start2023-02-12 🗸	End 2023-03-08 V	Get Date
Projec <mark>HBG4a01C2.prj</mark>	~	
Success 218	Failed 323	
Log Maintenance Auto delet 1 v Mon	ths	
Date before 2023-02-12	V Delete Log	

Auto Delete deletes logs from 1, 3, and 6 months ago the next time the software is started, according to the current system time.

Data before is to select a time of logs and then delete all logs before that time.

Choose carefully

8 Repeat the steps

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Click OK to start the project

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C0000051		C0000047	
		2 4 6 8	
1 3 5 7		1 3 5 7	
26% Completed 151.00 MB/S		19% Completed 148.00 MB/S	
Program 2300000	Cancel	Program 1C400000	Cancel
C0000051			
	(1		

After the project is completed, remove the chip and press OK

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Put in the chip and press OK to continue the project, and the cycle is repeated until the chip that needs to be worked is completed.

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2 4 6 8 Put IC	bx3CE62C16	2	4 6 8	Check SUM:0x3CE62C16 Put IC		
1 3 5 7		1	3 5 7			
100% Completed 123.00 MB/S Elspred: 00:00:36	OK Cancel	100% Compl Elapsed: 0	eted 123.00 MB/S 0:00:36	OK	Cancel	
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After completing the project, press Cancel to pop up the End Work window, press End Work to close the project interface.

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1 3 Finish Job?		1	3 Fin	nish Job?			
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After pressing Cancel, the End Work window pops up, and pressing End Work closes the project interface.

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	Finish Job?		
100% Completed	确定取消		
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Close the program, turn off the programmer power to finish.