

# **PA800Lite UFS gang**



By no means a flasher that uses the main control chip of the card reader with incomplete functions, this equipment is a fully functional programmer developed by IP with independent intellectual property rights, which can erase and write all ICs and support all UFS under the JEDEC specification Chip.

#### Easy to operate

Users do not need to have knowledge of EGGS settings and rules to use it, as long as the IC is used by using our New Project function, the IC can be used All settings and attribute data are stored in a project, and the chip IC can be made using the Run Project Load parameters and data from the project stored on it.



### High speed and stability

The burner is equipped with a high-speed

• FPGA with a read and write speed of 190MB/s per station

### **Supports UFS settings**

Support Descriptors, Attributes, Flagssettings, MON splitting and designated boot areas.

#### Supports the full range of UFS

UFS 2.0, 2.1, 2.2, **3.0**, **3.1** support all EGGS under the JEDEC specification Chip.

## Easy to operate, no master is required for operation

The UI is simple and easy to operate.

PA800Lite is a large-capacity memory UFS (Universal Flash Storage) massproduced burner, one-to-eight programming method And multiple devices can be jointly produced online.

Save the parameters and data of the master card by creating a project, and engineer the production in the way of executing the project, so that the time of programming and calibration is shorter and easier to manage.

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