

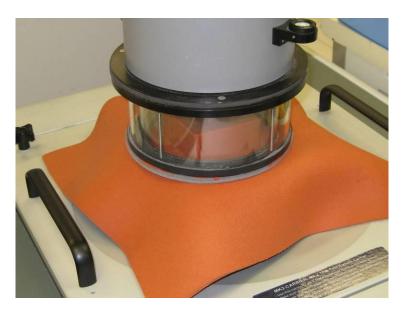
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Elevated Temperature During Latch-up Testing

Standards recommend performing latch-up testing under elevated temperature to ensure the junctions of the devices are at their upper operating levels, there by performing testing at the highest levels.

The design of the Thermo Scientifics ESD/Latch-up testers provides easy access to the device test area so that a thermostream or other temp forcing system can be used. Although the test area contains only the DUT socket, the DUT fixture and the system matrix below the fixture, are sensitive to the elevated temperature levels. Because of this, Thermo recommends that the DUT test area be insulator from the heat sources by an insulating foam, such as that shown below.

The use of this insulation, allows elevated temperature levels up to 150°C



Note, as shown below, multiple layers of insulation maybe required to ensure a good seal between the insulations and the thermostream head.

