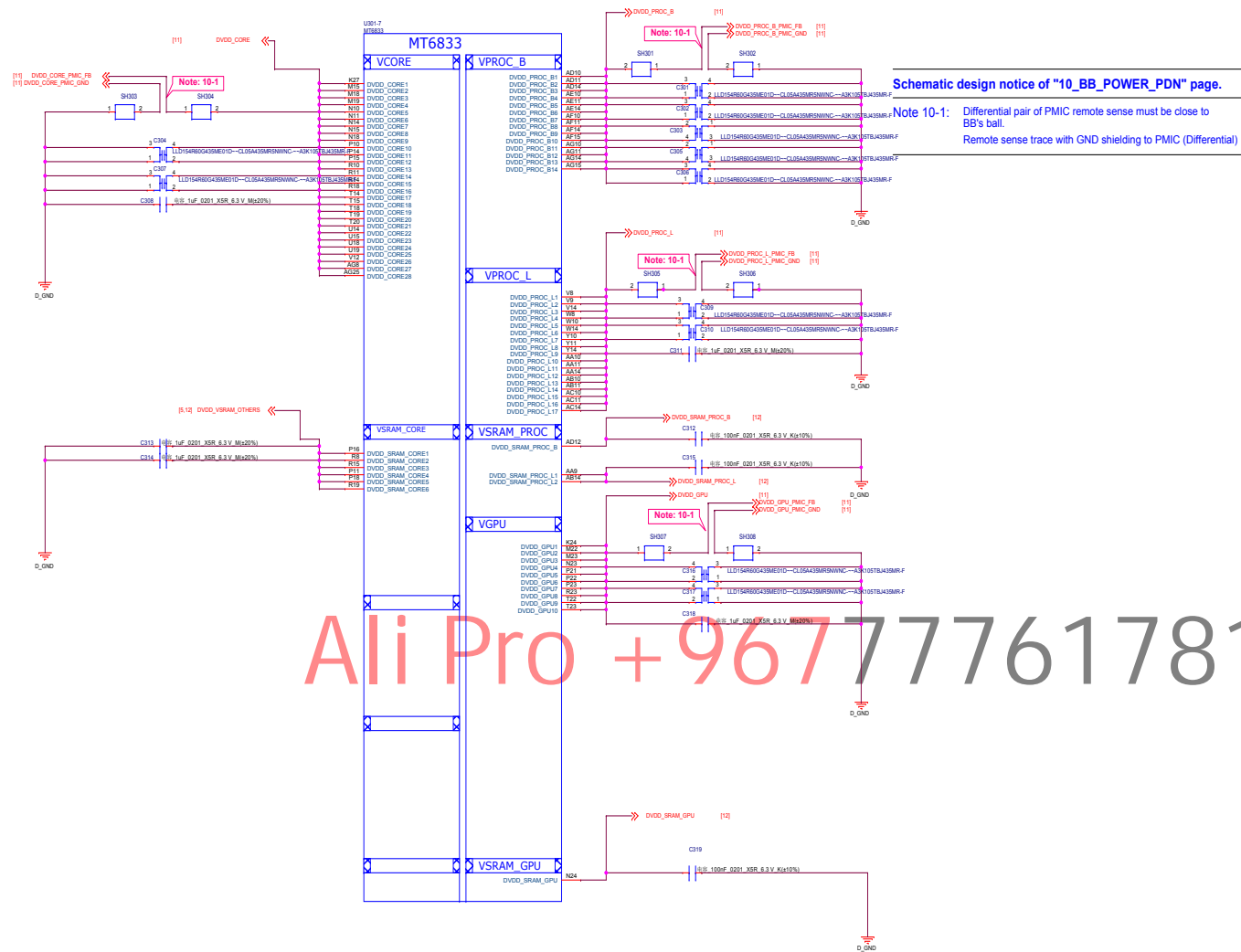


Date	Version	Page	Change notice
2020.06.xx	Pre-Released		
2020.07.xx	V01		
2020.08.14	V02	13_BB_1 29_POWER_EXT 20_POWER_MT6365_Buck 25_POWER_MT6315_VMD	Disconnect Dummy pins A1,A34,AP1,AP34 to GND Change C2923 cap from 6.3V to 10V [PDN] Change Caps of DVDD_GPU - C2039/C2038 to NC [PDN] Change Caps of DVDD_MODEM - C2502/C2534/C2523/C2522 to NC
2020.08.26	V03	File name	Modify the file name

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Title SM6350_Control			
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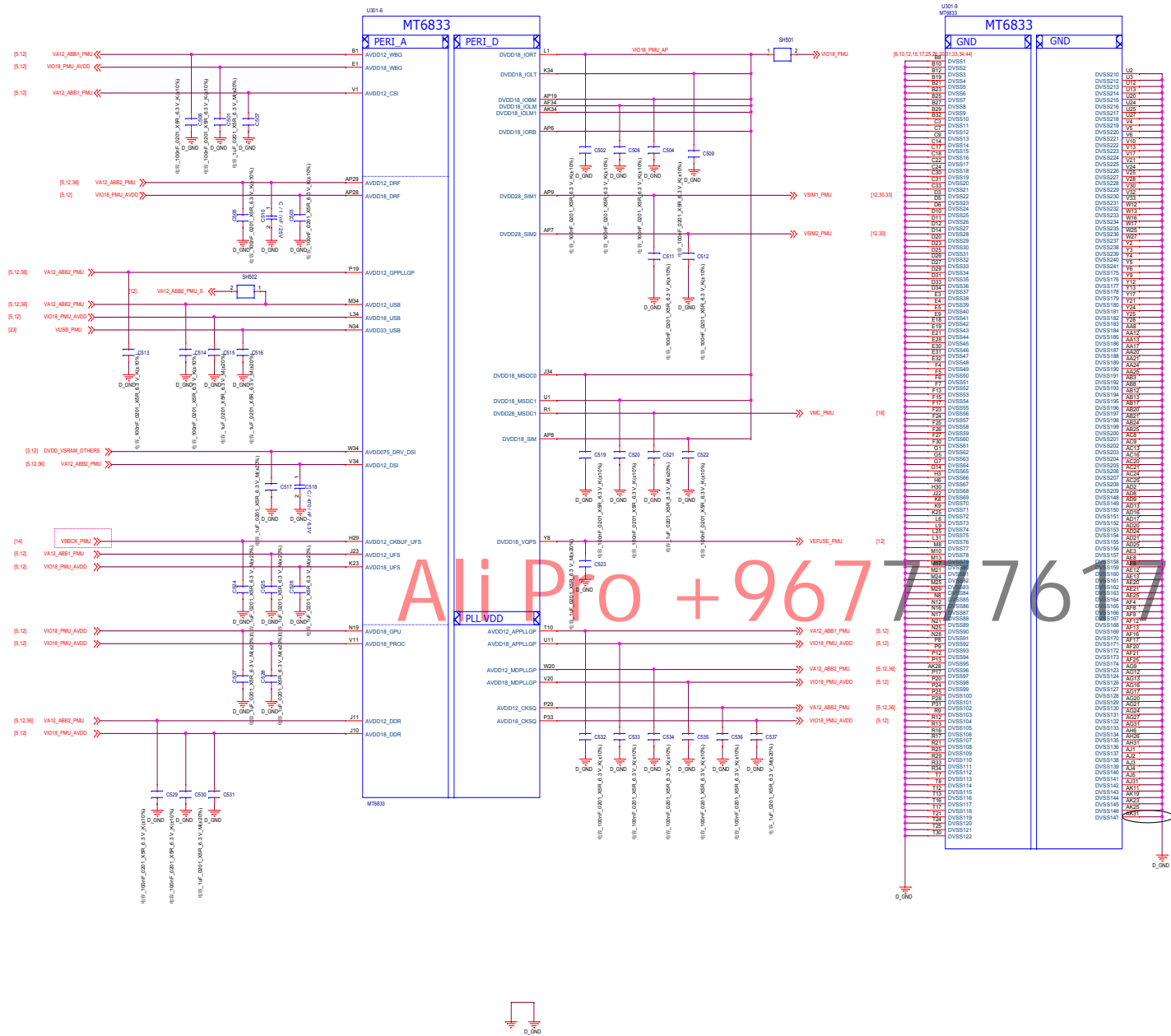
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华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title		03.BB_POWER_PDN1	
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DVS814 C08  
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 DVS818 C08  
 DVS819 C08  
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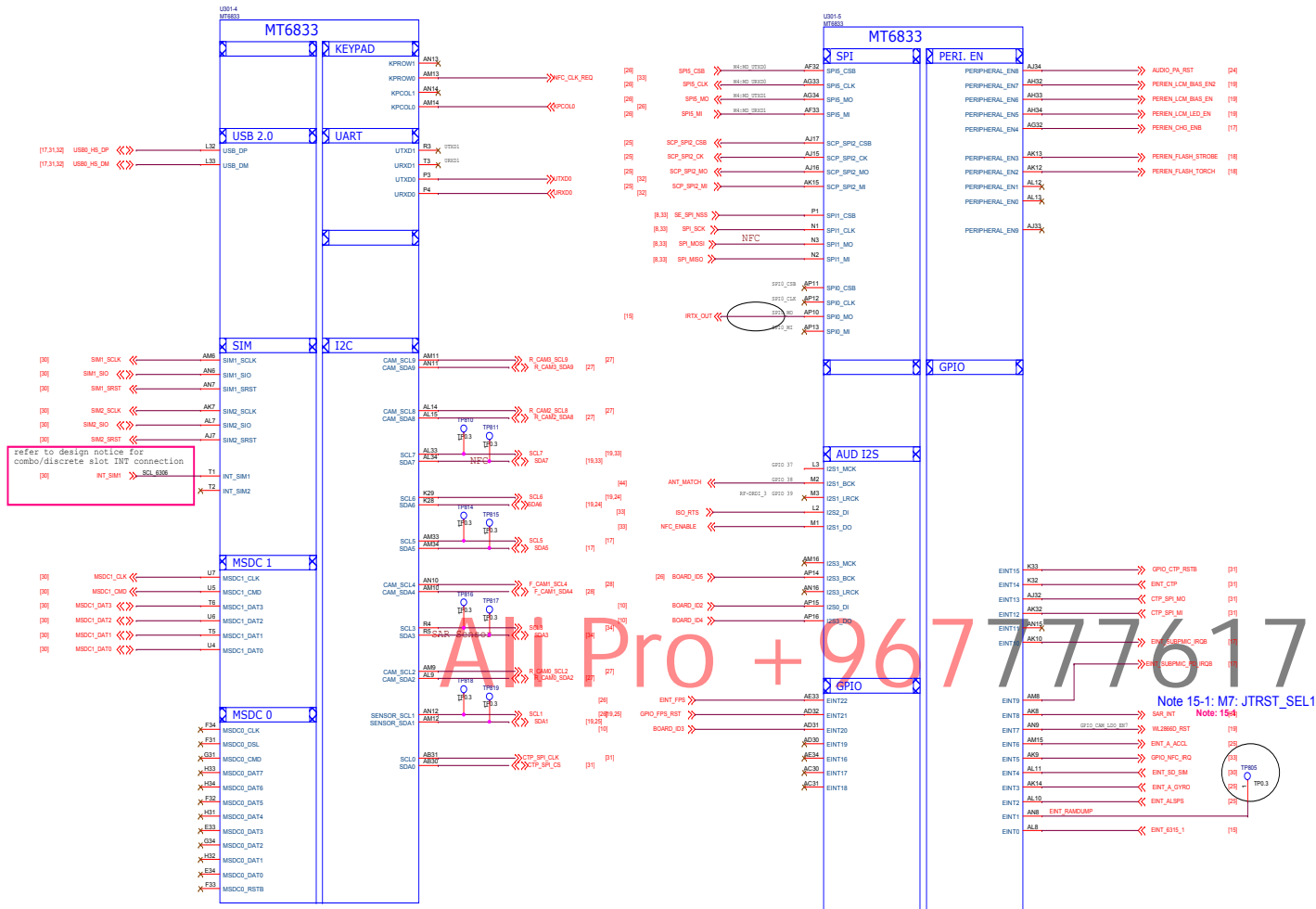
华勤通讯 Huaqin Telecom Technology Com.,Ltd		
Title                    04.BB_POWER_PDN2		
Size D	Project  <div>&lt;Doc&gt;</div>	Rev  <div>v1</div>
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Size	Project	<Doc>	Rev
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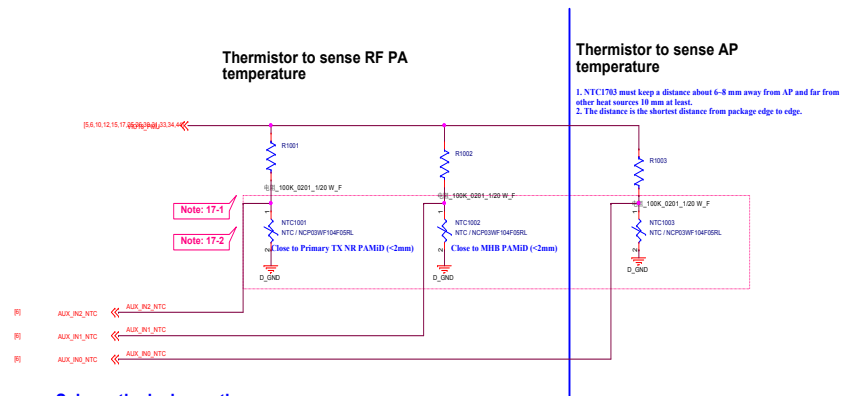
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Title		08.BB_3	
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D			
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Note 16-1: R4001 please select 60.4 ohm (1%) resistor

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Title <div style="text-align: center; font-size: 24px; margin-top: 10px;">           09.BB_4         </div>			
Size D	Project <div style="text-align: center; font-size: 24px; margin-top: 10px;">           &lt;Doc&gt;         </div>		Rev v1
Date: Tuesday, January 12, 2021		Sheet 9 of 48	

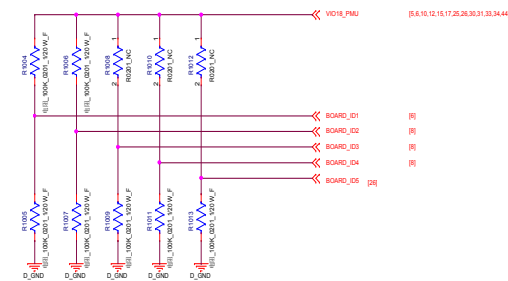




#### Schematic design notice:

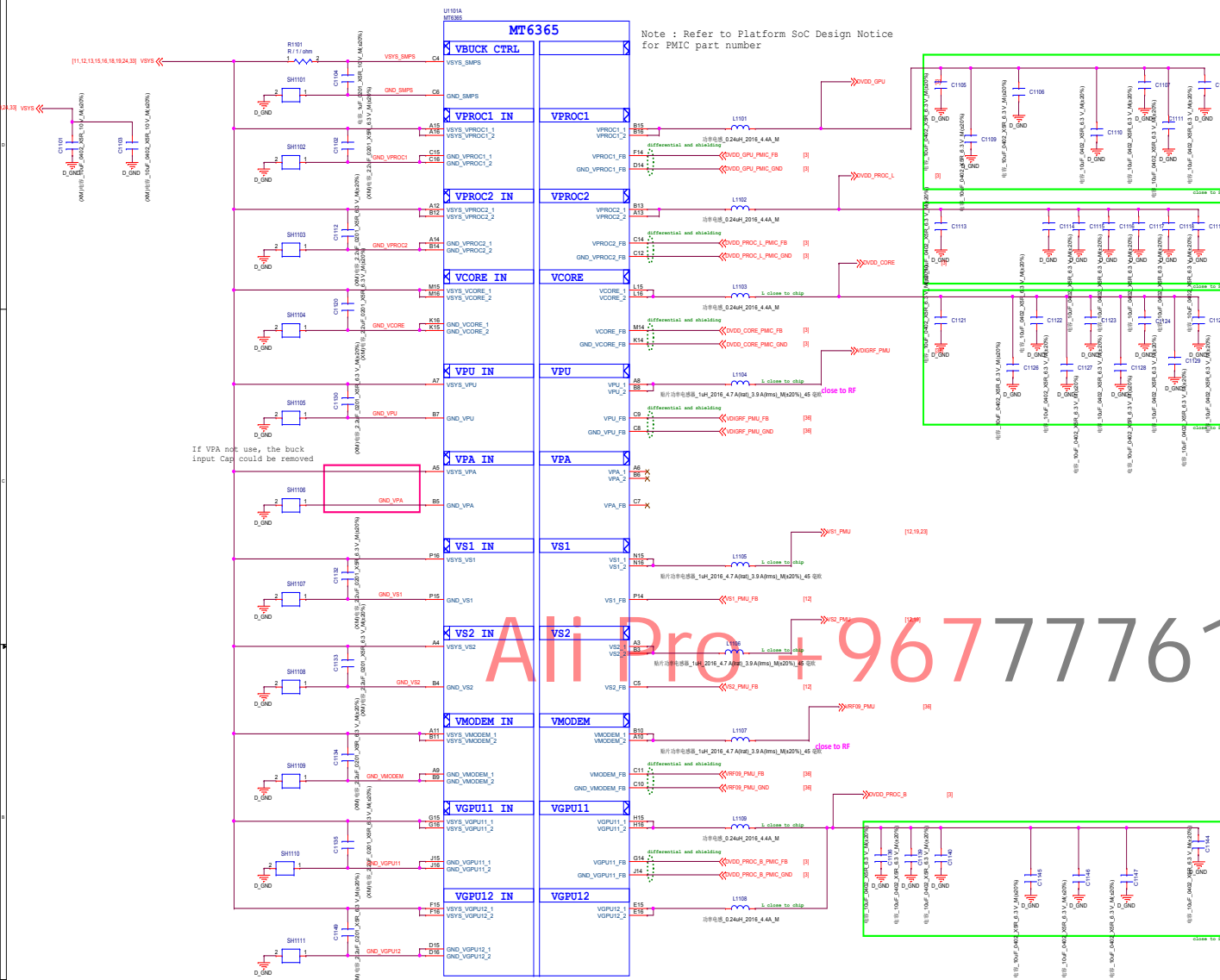
Note 17-1: SW Default Configuration is as followings  
AUX\_IN0\_NTC is for AP Temp.  
AUX\_IN1\_NTC is for MHB PAMID Temp.  
AUX\_IN2\_NTC is for NR PAMID Temp.

Note 17-2: If your design or placement is different with SW Default Config.  
Please refer to "MTXXXX Thermal User Manuel.docx" and modify SW setting.



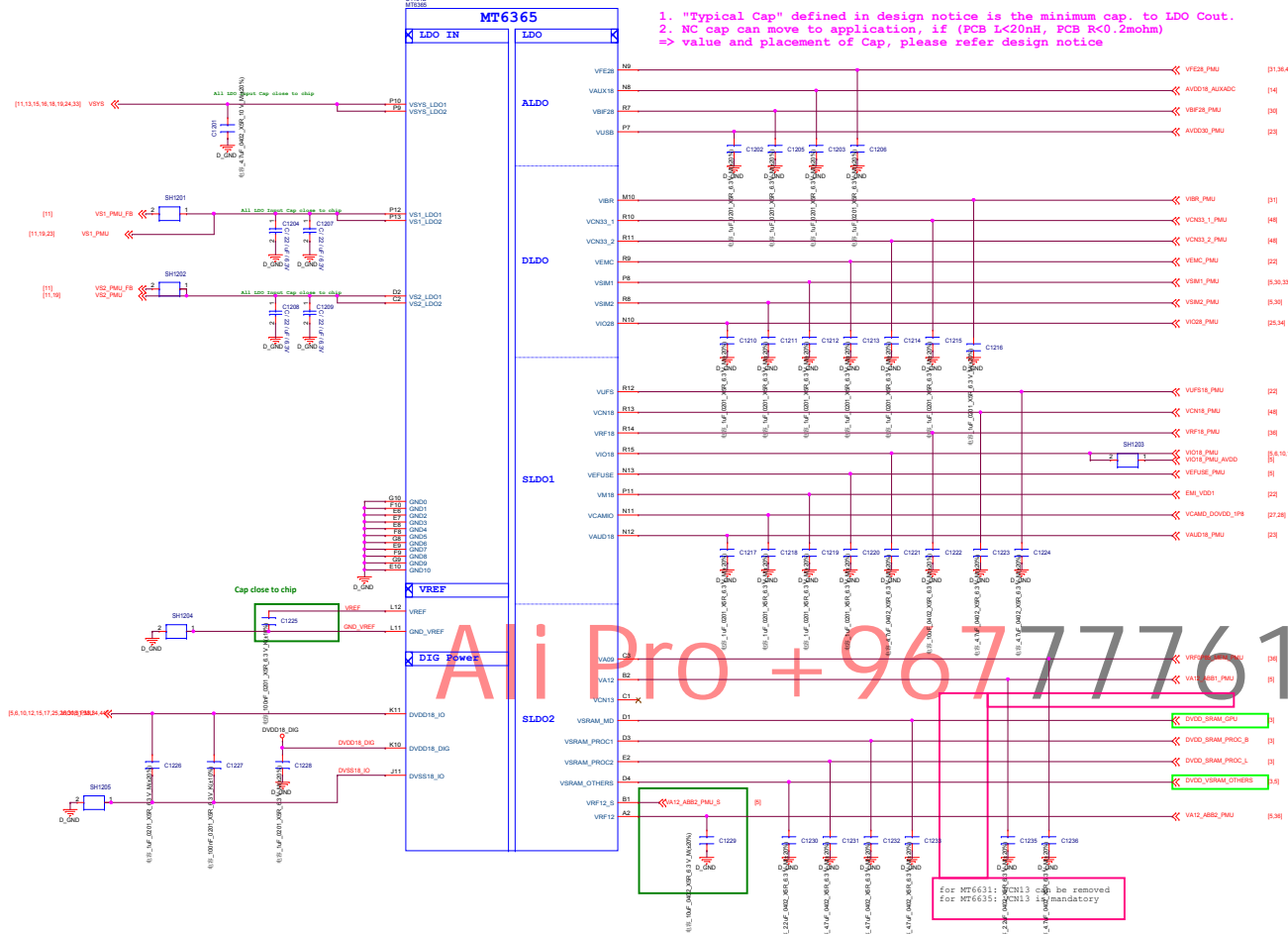
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Title		10.BB_AUXADC_Thermal/BOARD_ID	
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Circuit Type	Buck Name (Application name)	Output Voltage Range (V)	Boot Default (V)	Iout-Max (mA)
Buck	VPROC1 (DVDD_GPU)	0.40 ~ 1.19 (6.25mV/step)	ON (0.75)	4800
	VPROC2 (DVDD_PROC_L)	0.40 ~ 1.19 (6.25mV/step)	ON (0.75)	4800
	VGPU11+12 (DVDD_PROC_B)	0.40 ~ 1.19 (6.25mV/step)	ON (0.75)	4800 * 2
	VCORE (DVDD_CORE)	0.40 ~ 1.3 (6.25mV/step)	ON (0.75)	4800
	VMODEM (VRF09_PMU)	0.50 ~ 1.10 (6.25mV/step)	OFF	4800
	VPU (VDIGRF_PMU)	0.40 ~ 1.19 (12.5mV/step)	ON (0.7)	2400
	VS1 (VS1_PMU)	1.86 ~ 2.20 (12.5mV/step)	ON (2.0)	2200
	VS2 (VS2_PMU)	1.20 ~ 1.50 (12.5mV/step)	ON (1.35)	2500
	VPA (VPA_PMU)	0.50 ~ 3.40 (50mV/step)	OFF	1000

Note : Refer to Platform SoC Design Notice  
for PMIC part number



1. "Typical Cap" defined in design notice is the minimum cap. to LDO Cout.
2. NC cap can move to application, if (PCB L<20nH, PCB R<0.2mohm)  
=> value and placement of Cap, please refer design notice

Circuit Type	LDO Name	Output Voltage (V)	Boot Default (V)	I <sub>OUT,MAX</sub> (mA)	Expected use
ALDO	VFE28	2.80	OFF (2.8)	200	RFPE
	VUSB	3.07	ON (3.07)	200	USB
	VAUX18	1.84	ON (1.84)	50	AUXADC
	VXO22	2.24	ON (2.24)	25	DCXO
	VBIFF28	2.80	OFF (2.8)	50	Battery Interface
VRTC	VRTC	2.80	ON (2.8)	2	RTC
VDIG	DVDD18_DIG	1.80	ON (1.8)	10	PMIC Digital

Circuit Type	LDO Name	Output Voltage (V)	Boot Default (V)	I <sub>OUT,MAX</sub> (mA)	Expected use
DLDO	VSIM1	1.7/1.8/1.86/2.76/3.0/3.1	OFF (1.86)	200	SIM
	VSIM2	1.7/1.8/1.86/2.76/3.0/3.1	OFF (1.86)	200	SIM
	VCN33_1	3.3/3.4/3.5/3.6	OFF (3.3)	800	Connectivity
	VCN33_2	3.3/3.4/3.5/3.6	OFF (3.3)	800	Connectivity
	VEMC	2.55/2.9/3.0/3.3	ON (3.0)	800	eMMC / UFS
	VIO28	3.3/2.9/3.0/3.1/3.2/3.3	OFF (2.8)	200	IO & Sensor
	VIBR	2.7/2.8/3.3	OFF (2.8)	200	Vibrator

Circuit Type	LDO Name	Output Voltage (V)	Boot Default (V)	I <sub>OUT,MAX</sub> (mA)	Expected use
SLD01	VEFUSE	1.80	OFF (1.8)	300	EFUSE
	VAUD18	1.80	ON (1.8)	300	Audio
	VCAMIO	1.80	OFF (1.8)	300	Camera IO
	VM18	1.80	ON (1.8)	300	DRAM
	VRF18	1.80	OFF (1.8)	450	RF
	VIO18	1.80	ON (1.8)	600	IO & Sensor
	VCN18	1.80	OFF (1.8)	1200	Connectivity
	VUFS	1.80	ON (1.86)	1200	eMMC / UFS
	VREF	1.40	ON (1.4)	10	DCXO
	VBBCK	1.24	ON (1.24)	10	DCXO

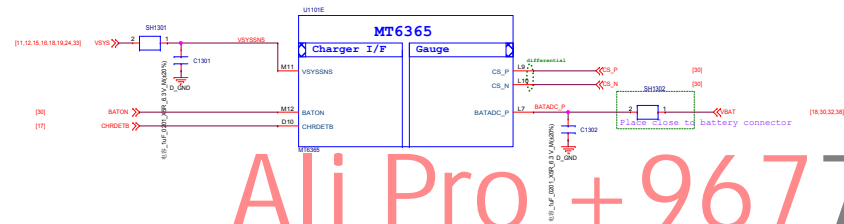
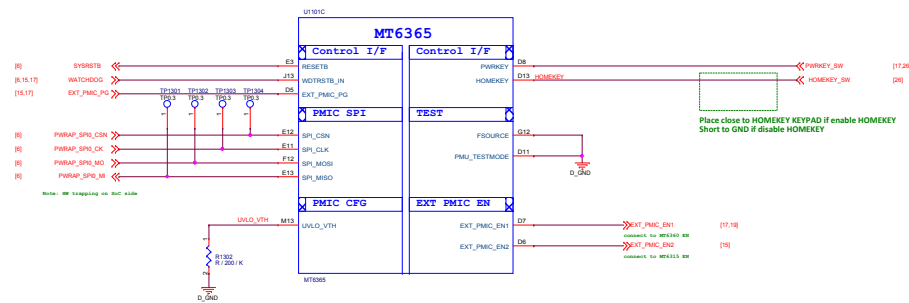
Circuit Type	LDO Name	Output Voltage (V)	Boot Default (V)	I <sub>OUT,MAX</sub> (mA)	Expected use
SLD02	VA09	0.85	ON (0.85)	300	DIGF SRAM (VRFOP85_MEM_PMIU)
	VA12	1.2	ON (1.2)	300	AP Analog Module
	VCN13	1.3	OFF (1.3)	350	Connectivity
	VS RAM_PROCI	0.60 ~ 1.2 (6.25mV/step)	ON (0.85)	600	CPUB SRAM
	VS RAM_PROCI	0.60 ~ 1.2 (6.25mV/step)	ON (0.85)	600	CPUL SRAM
	VS RAM_PROCI	0.60 ~ 1.2 (6.25mV/step)	ON (0.85)	600	OTHERS SRAM
	VS RAM_MD	0.60 ~ 1.2 (6.25mV/step)	ON (0.85)	600	GPU SRAM
	VRF12	1.2	ON (1.2)	800	AP Analog Module
	VRF12	1.2	ON (1.2)	800	AP Analog Module

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Title 12.POWER\_MT6365\_LDO

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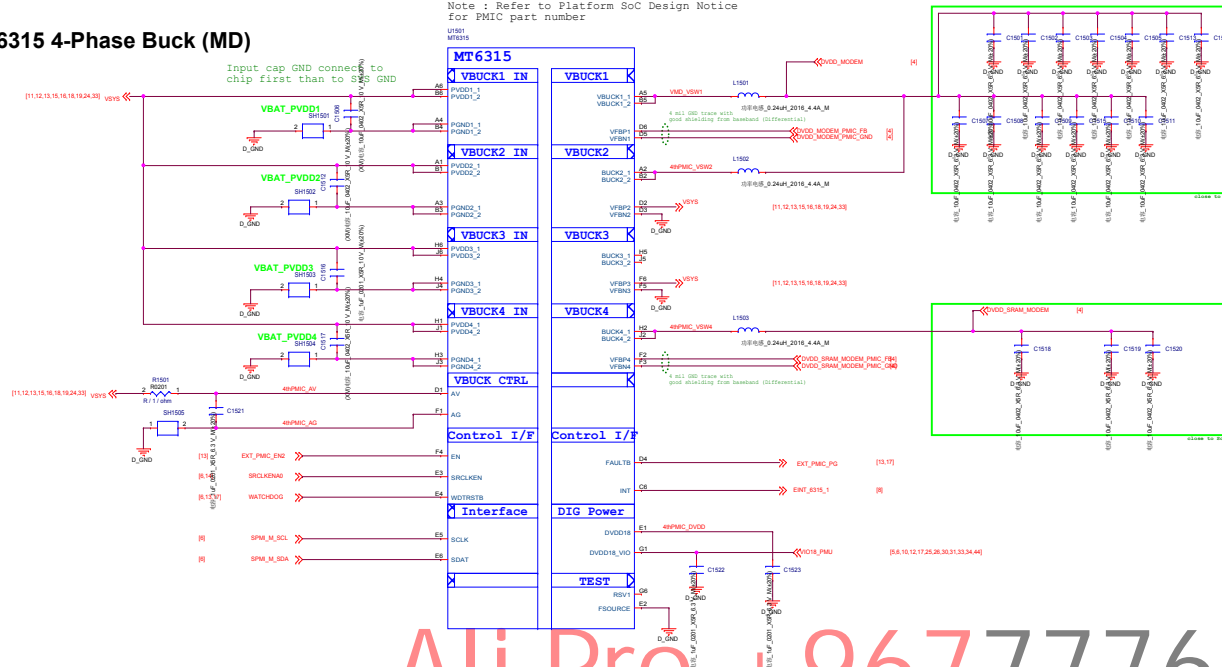
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Title		13.POWER_MT6365_General	
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### MT6315 4-Phase Buck (MD)

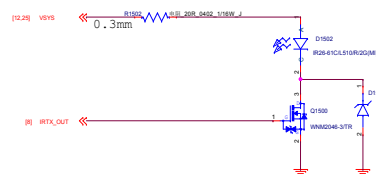
Note : Refer to Platform SoC Design Notice  
for PMIC part number



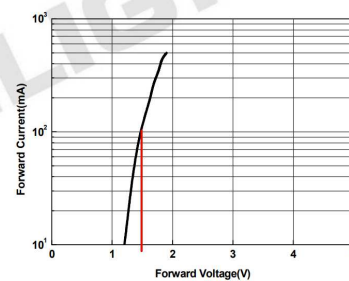
MT6315NP/B	Output Voltage Range (V)	Boot Default (V)	I <sub>OUT-MAX</sub> (mA)	Expected use
BUCK1+2	0.40 ~ 1.19375 (6.25mV/step)	ON (0.825)	5000*2	DVDD_MODEM
BUCK3	0.40 ~ 1.19375 (6.25mV/step)	ON (0.75)	5000	--
BUCK4	0.40 ~ 1.19375 (6.25mV/step)	ON (0.825)	5000	DVDD_SRAM_MODEM

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**IR LED**



### Forward Current vs. Forward Voltage



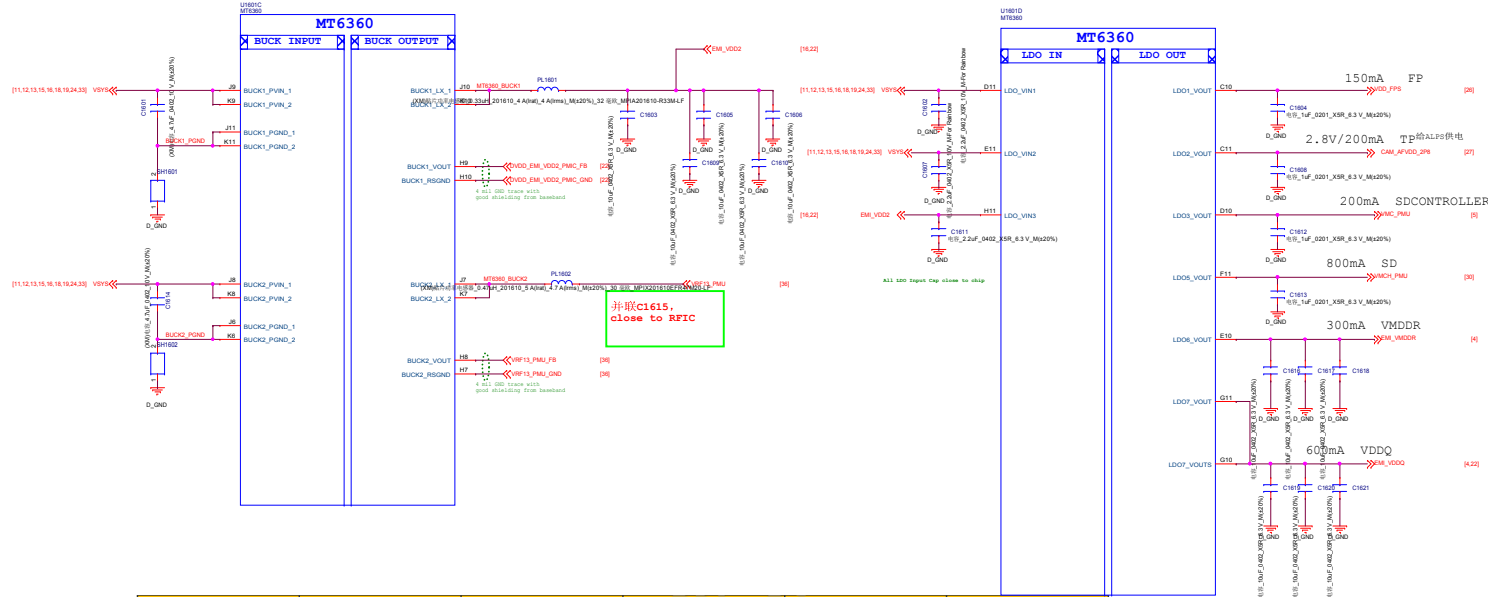
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Title	15.POWER_MT6315_VMD/IR_LED
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Note : Refer to Platform SoC Design Notice  
for PMIC part number



LDO1 VFP  
LDO2 VTP  
LDO3 VMC  
LDO5 VMCH  
LDO6 EMI\_VMDDR  
LDO7 EMI\_VDDQ

Circuit Type	Name	Output Voltage Range (V)	Boot Default (V)	I <sub>OUT-MAX</sub> (mA)	Expected use
Buck	BUCK1	0.3 ~ 1.3 (5mV/step)	ON (1.125)	3000	EMI_VDD2
	BUCK2	0.3 ~ 1.3 (5mV/step)	OFF (1.3)	3000	VRF13_PMU

Circuit Type	Name	Output Voltage (V)	Boot Default (V)	I <sub>OUT-MAX</sub> (mA)	Expected use
LDO	LDO1	1.8/2.0/2.1/2.5/2.7/ 2.8/2.9/3.0/3.1/3.3	OFF (1.8)	150	Fingerprint
	LDO2	1.8/2.0/2.1/2.5/2.7/ 2.8/2.9/3.0/3.1/3.3	OFF (1.8)	200	Touch Panel
	LDO3	1.8/2.9/2.8/3.0/3.3	OFF (3.0)	200	SD Card
	LDO5	2.9/3.0/3.3	OFF (3.0)	800	SD Card
	LDO6	0.75	ON (0.75)	300	EMI_VMDDR
	LDO7	0.6	ON (0.6)	600	EMI_VDDQ

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Title 16.POWER_MT6360_BUCK_LDO*		
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厂家	12C ADDRESS
LM3497	0110110 binary (or 36h)
WTD3136	0110110 binary (or 36h)

厂家	I2C ADDRESS
WL266D	0x50H
XT5904	0x50H

厂家	INC ADDRESS
OC92111	0111110 binary (or 3Eh)
SM5109	0111110 binary (or 3Eh)

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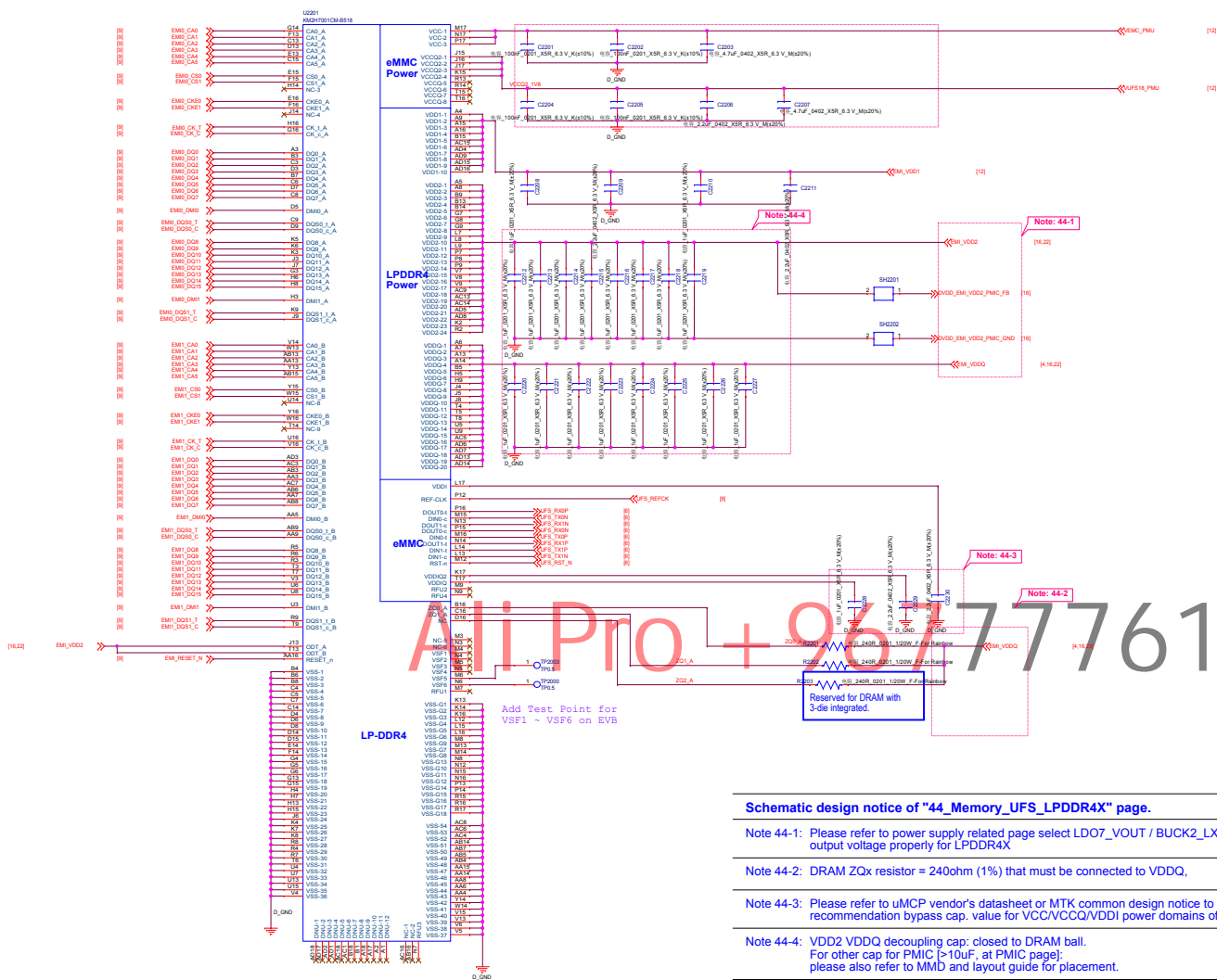
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Title 19.POWER_EXT			
Size D	Project <Doc>		Rev V1
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Size E	Project <Doc>	Rev v1	
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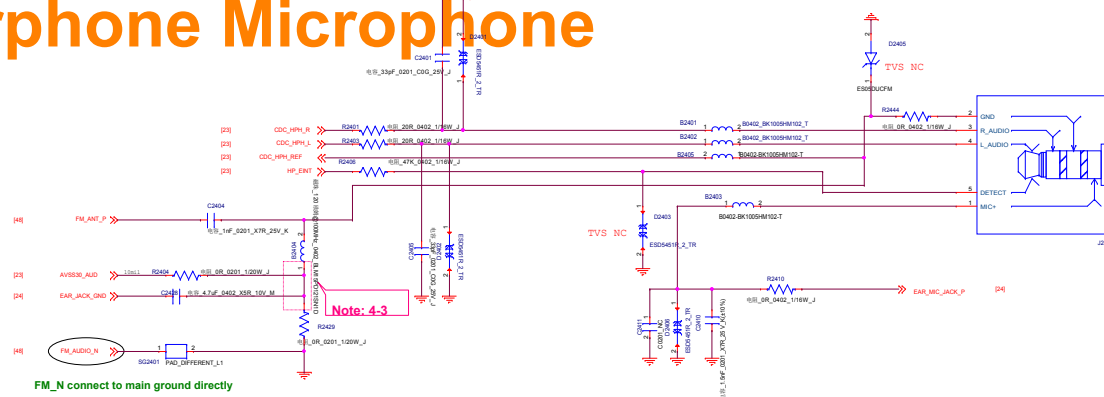
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Title PM7250B_MISC/GPIO/GND		
Size D	Project <Doc>	Rev v1
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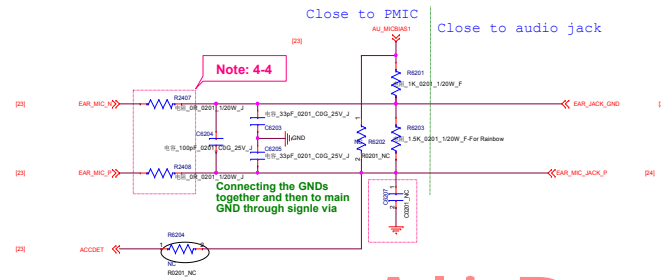


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Title 23.PERI_AUDIO_PMIC-IF			
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**RECIPIENT**



### Earphone Microphone

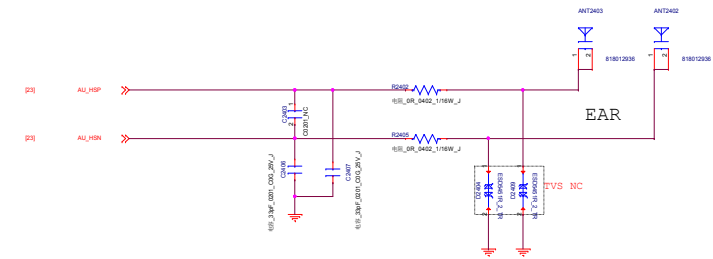


Note 4-3:

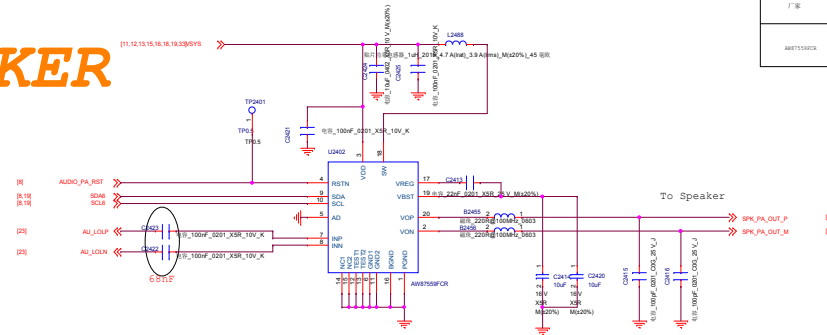
	Earphone Jack # Main board	Earphone Jack # Sub board
PA504_2	0 ohm	100nF

Note 4-4: 1. Need do SW configuration for DCC mode  
2. Analog input impedance is as below,  
please customer check the detail requirement  
with mic vendor if the mic used has special  
requirement on the input impedance of analog input

MT6357	UL Mode	MIN	TYP	MAX	Unit
C	DCC	-	-	11p	F

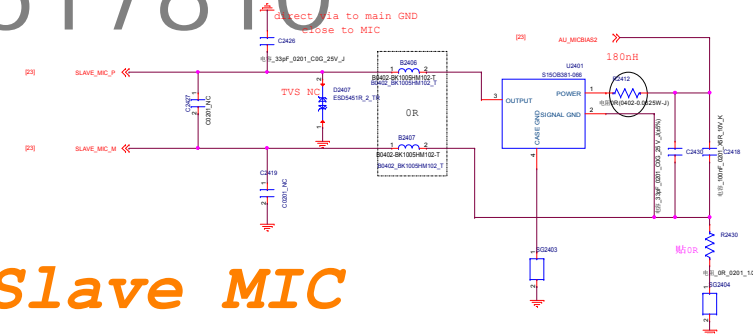


***SPEAKER***



厂家	ICC ALCOZSS
AM87559PCR	0X58(7 bit)

## Slave MIC



华勤通讯 Huaqin Telecom Technology Com., Ltd

Title	24.MIC/RCV/SPK/Earphone
-------	-------------------------

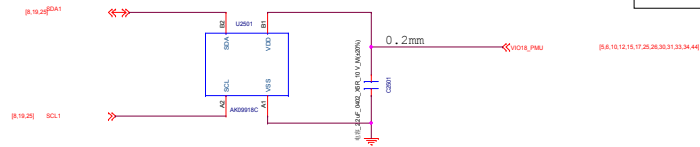
Size D	Project  <Doc>	Rev v1
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**eCOMPASS**

## M-Sensor

厂家	12C ADDRESS
AD1991BC	0ch
QAC6308-TR	2ch

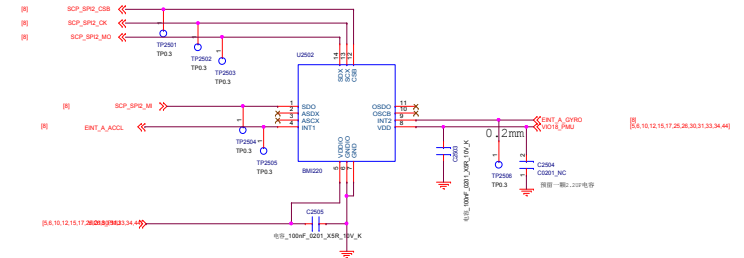


## A+Gsensor

```
default SPI (if sensor hub support)
```

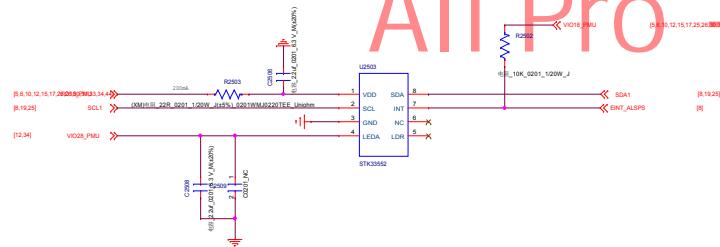
厂家	22C AC09222
22C220	
22C-40607-A	

Gyro+A sensor



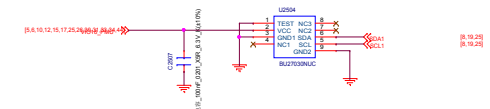
# ALS+PS

厂家	IPC ADDRESS
STK33552	0x46



## 后光感 BU27030NUC

厂家	IPC ADDRESS
800 1030000	2438



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Title	25.SENSOR
-------	-----------

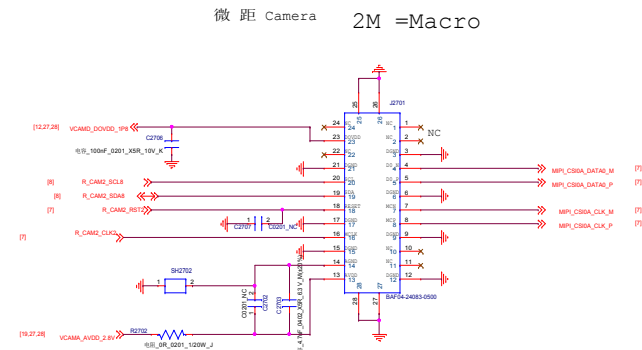
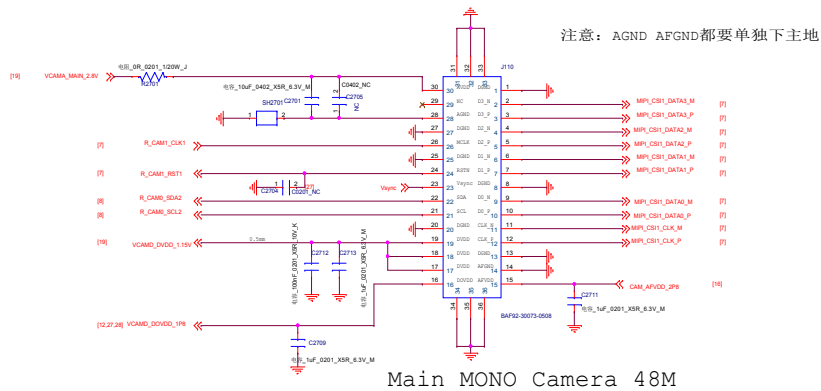
Size	Project	Rev
D	<Doc>	v1

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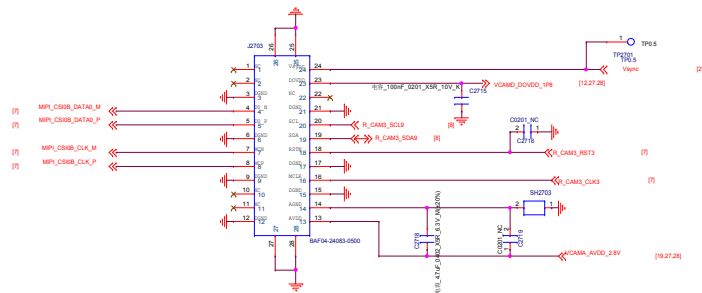
The schematic diagram illustrates the antenna matching network for the Ali Pro. It features three antenna inputs: ANT2001, ANT2002, and ANT2003. ANT2001 is connected to a 100Ω resistor (R0002) and a 100pF capacitor (C0001). ANT2002 is connected to a 100Ω resistor (R0003) and a 100pF capacitor (C0002). ANT2003 is connected to a 100Ω resistor (R0004) and a 100pF capacitor (C0003). The matching network is connected to the Volume Up and Volume Down pins of the Ali Pro module. A TVS diode (D0001) is connected between the Volume Up and Volume Down pins to protect the module from electrostatic discharge (ESD).

华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title <b>26.FP/KEY</b>			
Size D	Project <b>&lt;Doc&gt;</b>		Rev v1
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景深 Camera 2M=Depth

Flash LED 5V Boost



Flash 下方地要完整且足够大，散热一定要好

华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title		27.REAR_CAM	
Size	Project	<Doc>	Rev
D			v1
Date: Tuesday, January 12, 2021		Sheet 27 of 48	

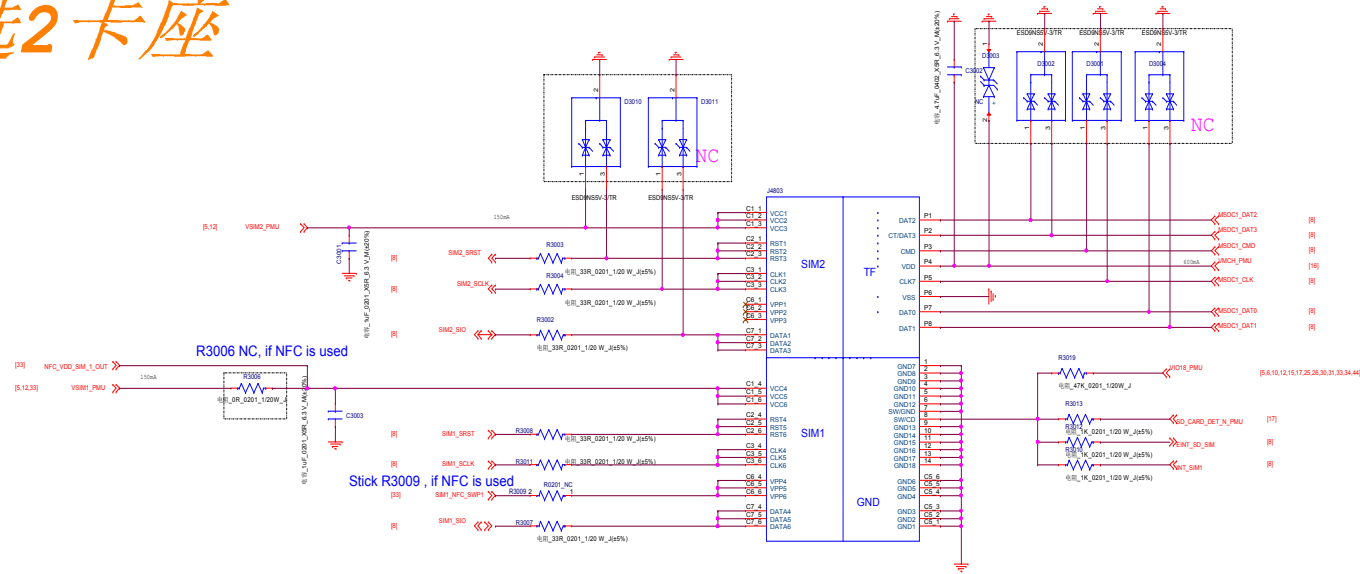
[illegible]

华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title 28.FRONT_CAM			
Size D	Project <Doc>		Rev V1
Date:	Tuesday, January 12, 2021	Sheet 28 of	48

Ali Pro +967777617810

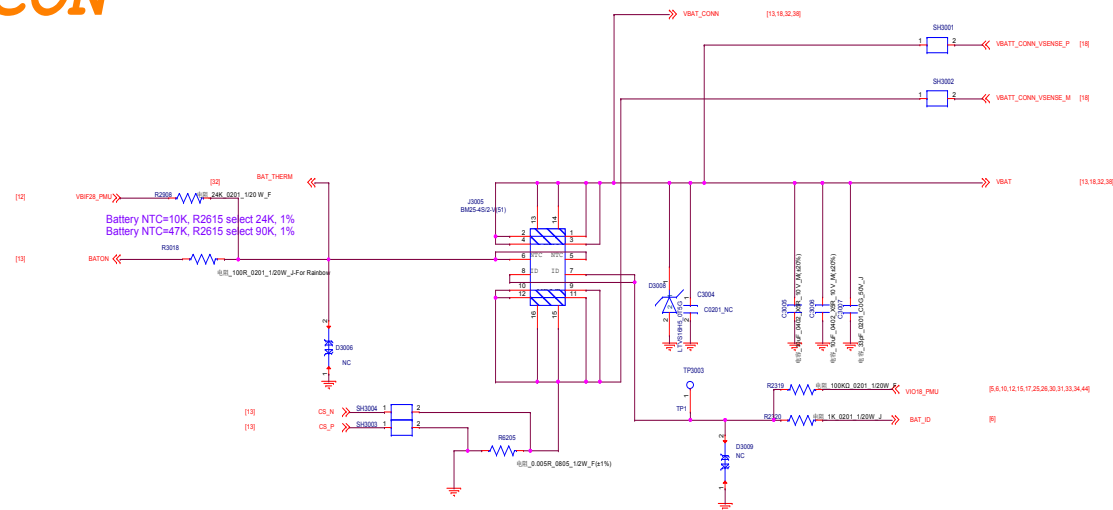
华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title		Sub_Charger_SMB1396	
Size	Project		Rev
D	<Doc>		V1
Date: Tuesday, January 12, 2021		Sheet 29 of 48	

# 3选2卡座



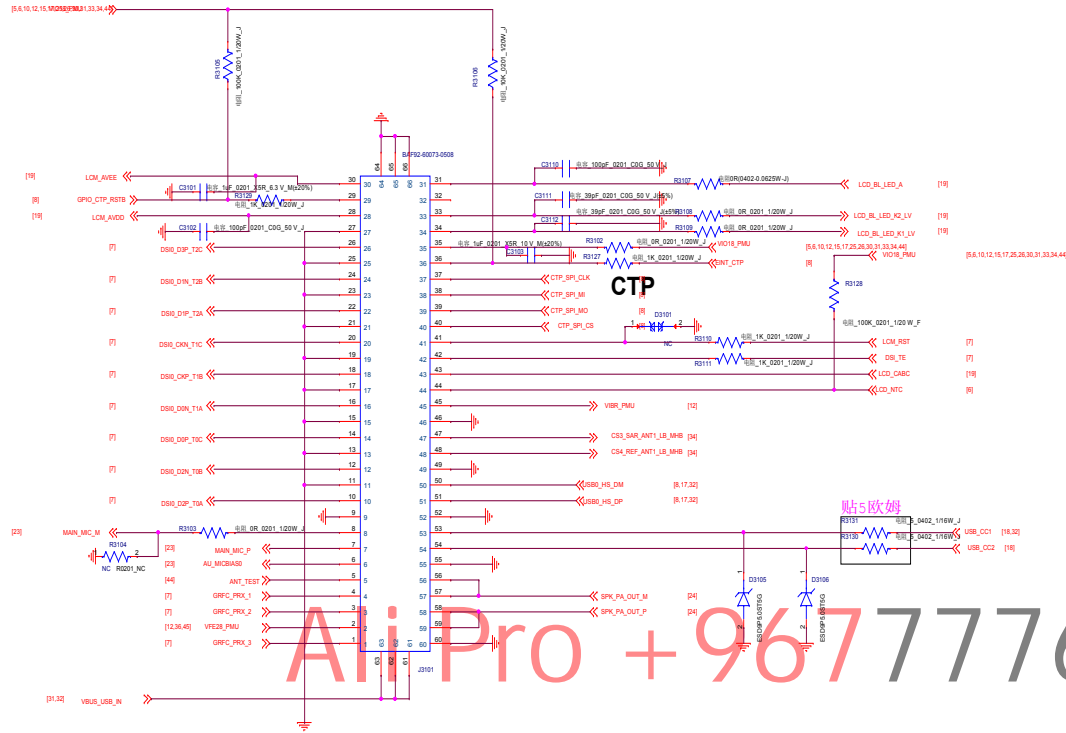
Ali Pro +967777617810

# BATT CON

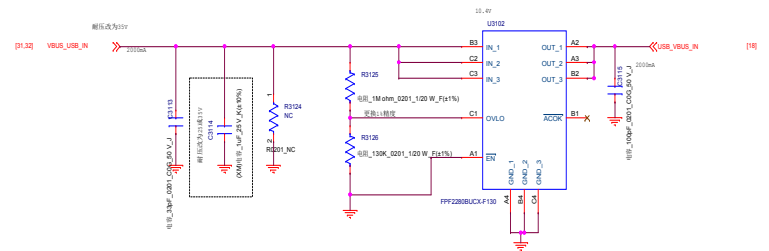


华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title		30.SIM/BATT	
Size	Project	<Doc>	Rev
D			V1
Date: Tuesday, January 12, 2021		Sheet	30 of 48

**LINK**

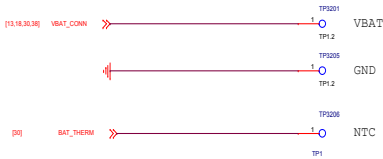


**OVP**



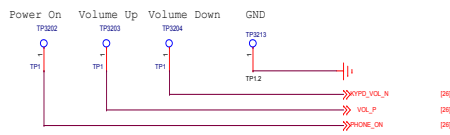
华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title		31.60PIN_LCD_LINK/OVP	
Size D	Project  <div>&lt;Doc&gt;</div>		Rev  v1
Date: Tuesday, January 12, 2021		Sheet 31 of 48	

VBAT TEST POINT

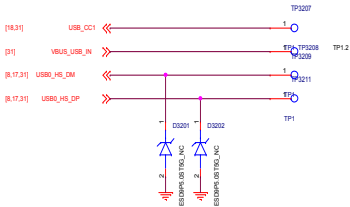


VBAT

KEY BTB



KEYPAD



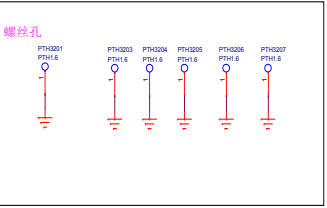
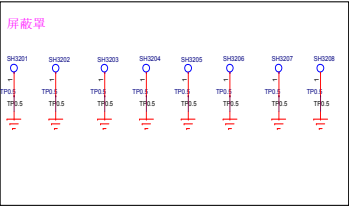
VCHG

UART TP



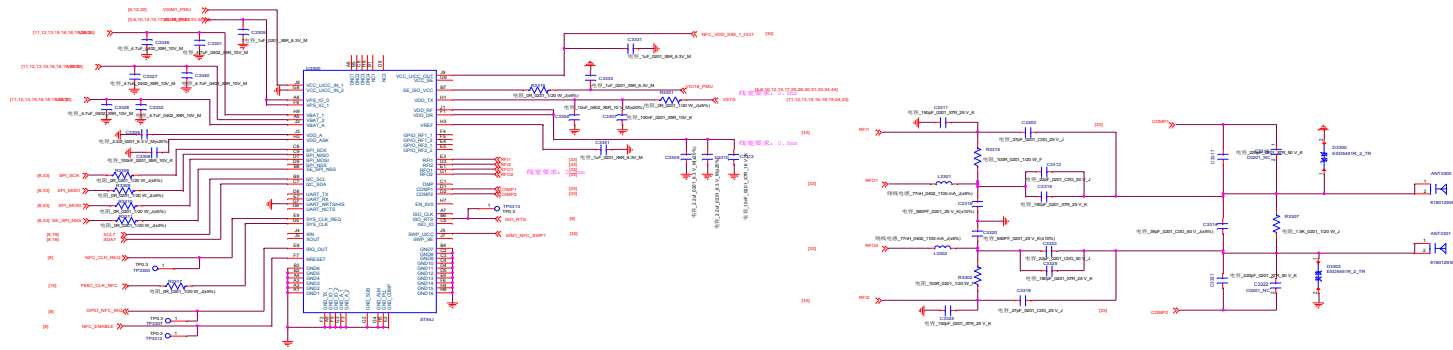
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JTAG



华勤通讯 Huaqin Telecom Technology Com.,Ltd		
Title 32.TEST_POINT/SHIELD		
Size D	Project <Doc>	Rev v1
Date: Tuesday, January 12, 2021	Sheet 32 of 50	

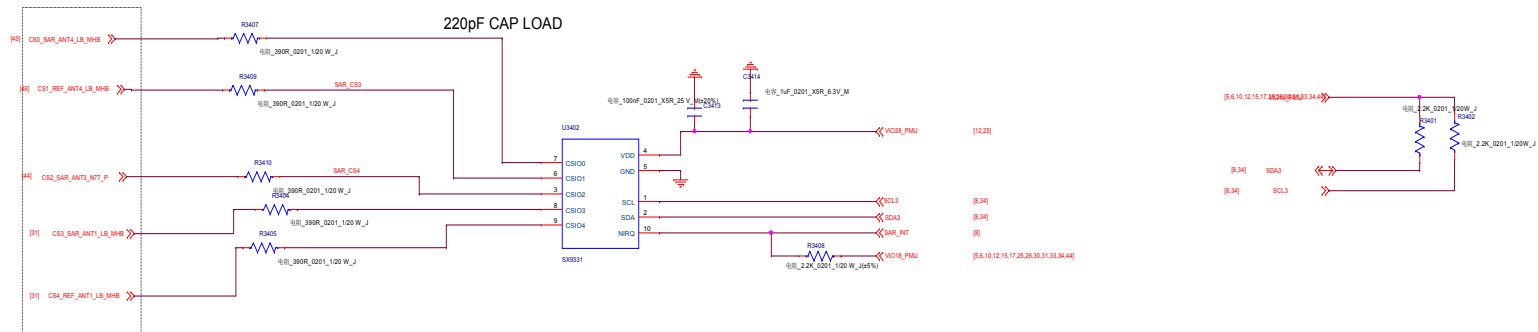
RF\_NFC\_not support



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SAR\_sensor Not support

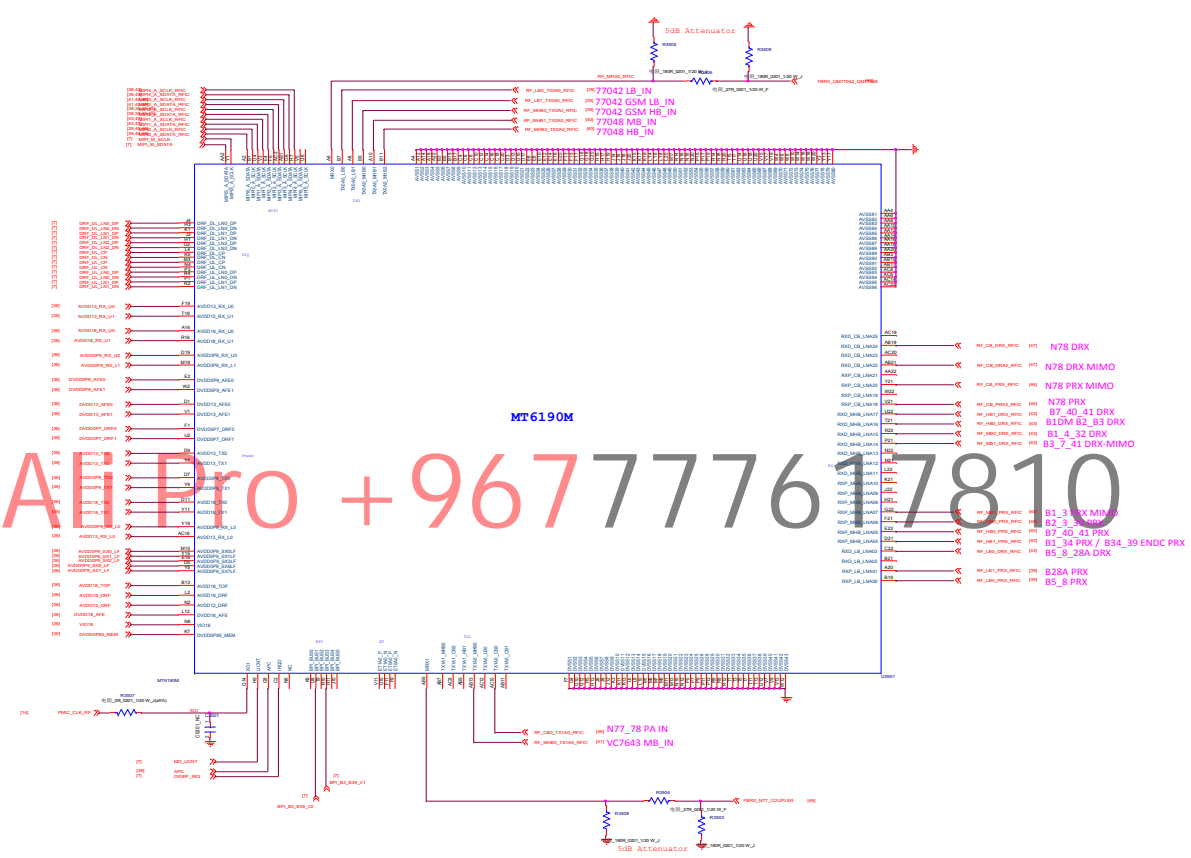


Design notice:

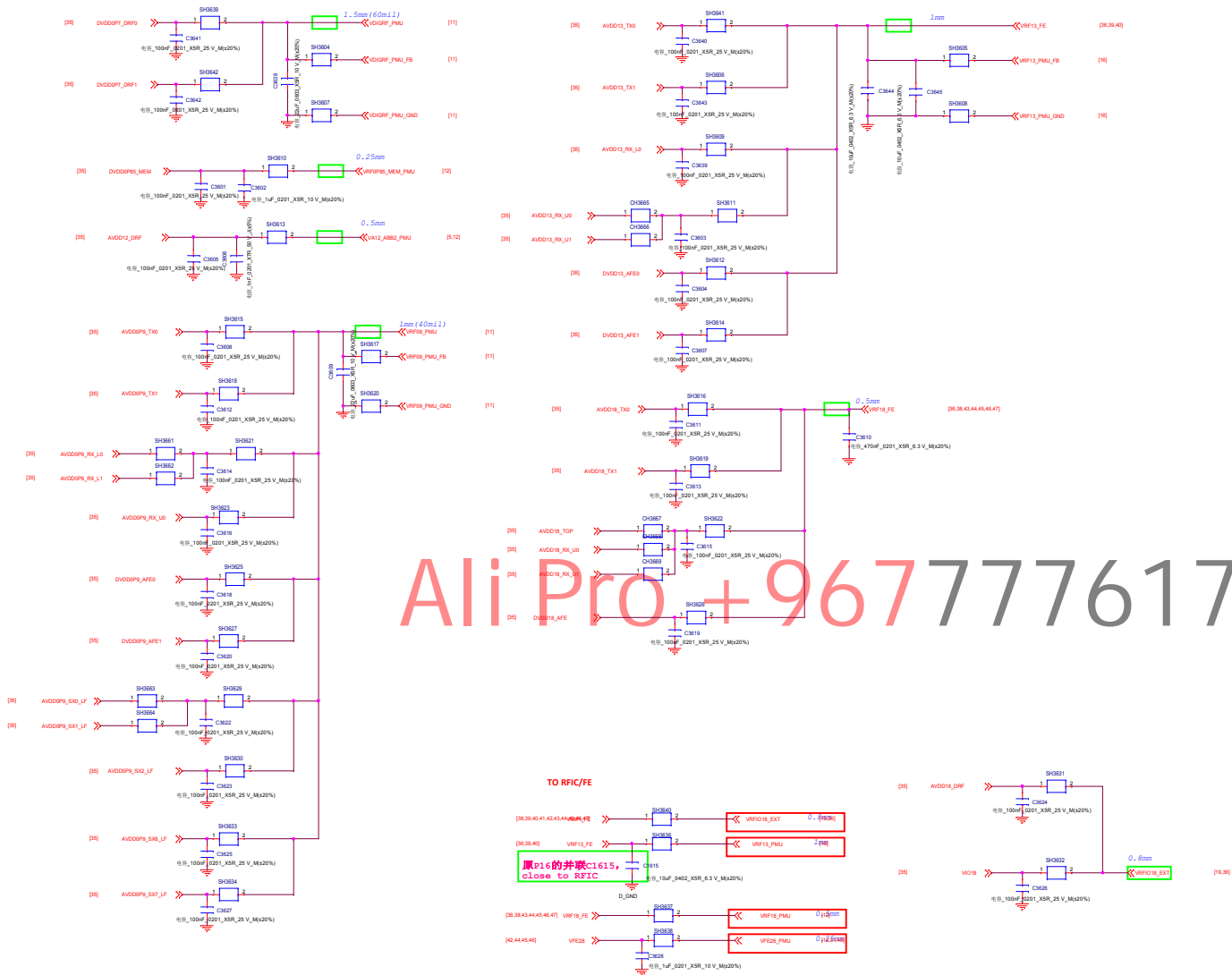
- Design Notice:
- 1) CS0\_1 nets are differential pairs;
  - 2) The width for each line, should be less than 0.1mm;
  - 3) 0.2mm should be guaranteed between the ground and differential pairs;
  - 4) And also, the distance between the differential line should be more than 0.2mm;
  - 5) All the CSx nets should be protected by surrounding GND;

3) 0.2mm should be guaranteed between the ground and differential pairs;  
4) And also, the distance between the differential line should be more than 0.2mm;  
5) All the CSX nets should be protected by surrounding GND;

华勤通讯 Huaqin Telecom Technology Com.,Ltd		
Title <div style="text-align: center;">SAR_sensor</div>		
Size D	Project <div style="text-align: center;">&lt;Doc&gt;</div>	Rev v1
Date: Tuesday, January 12, 2021		Sheet 34 of 48



MT6190M\_PWR



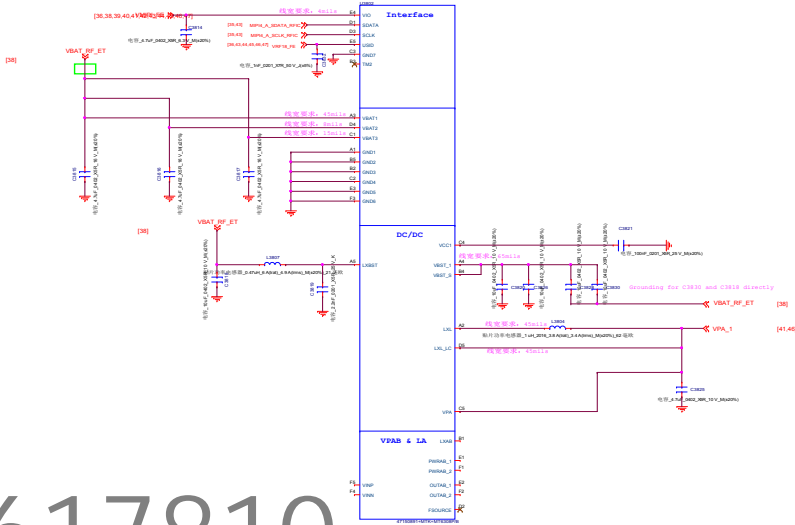
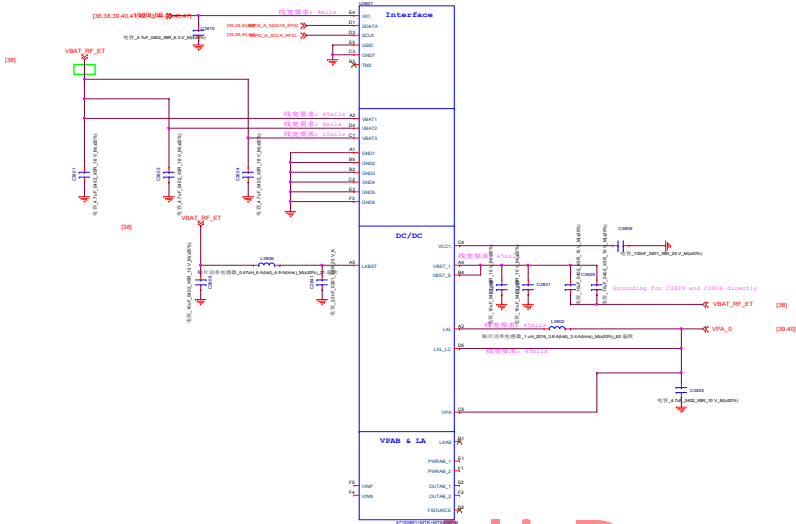
华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title <div>MT6190M_PWR</div>			
Size D	Project <div>&lt;Doc&gt;</div>		Rev v1
Date:   Tuesday, January 12, 2021		Sheet   36 of   48	

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华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title		NC_FBRX	
Size D	Project <Doc>		Rev v1
Date: Tuesday, January 12, 2021		Sheet 37 of	48

QM77042 and QM77048 for one APT

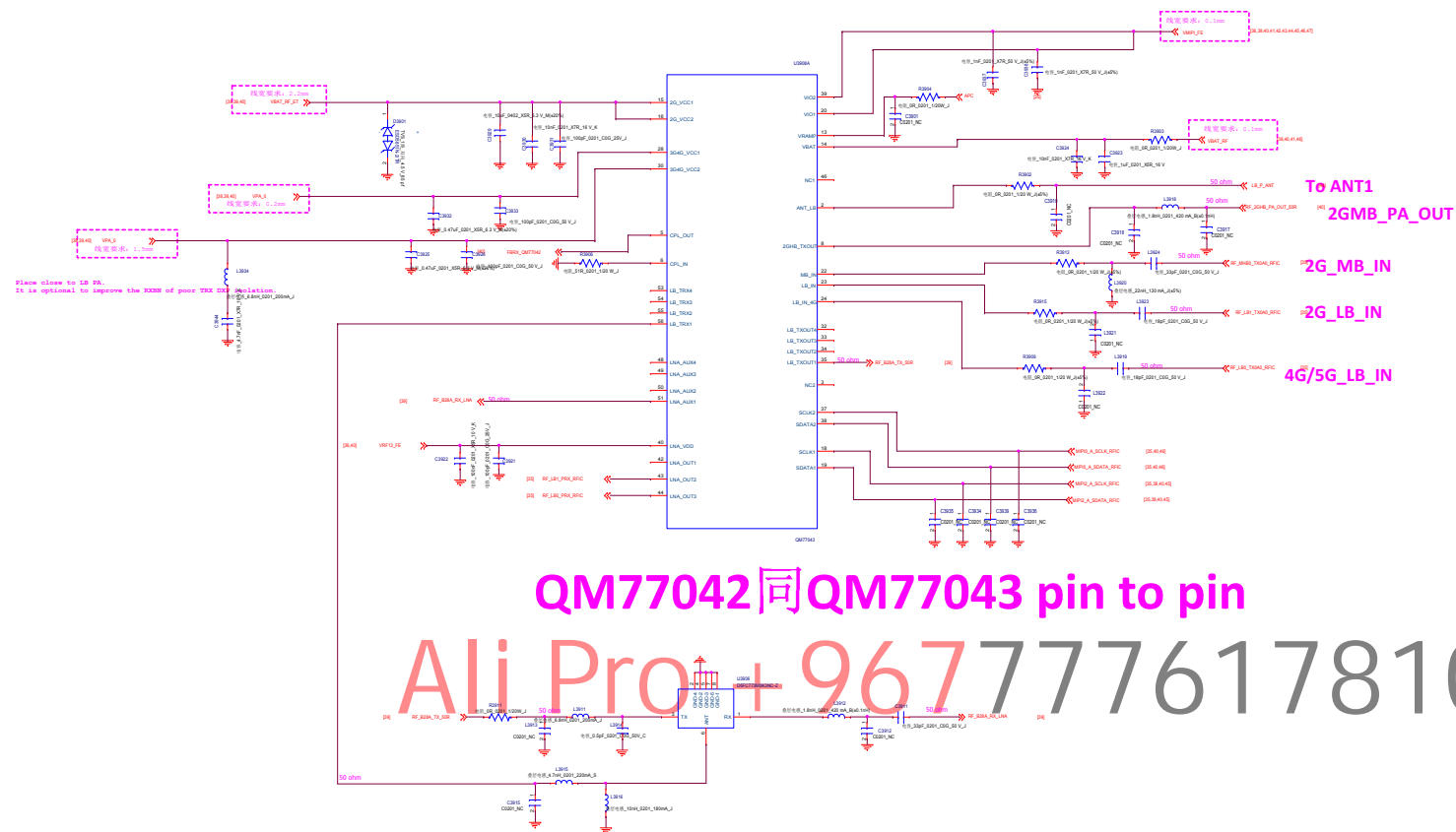
VC7643 and QM78200 for the other APT



Ali Pro +967777617810



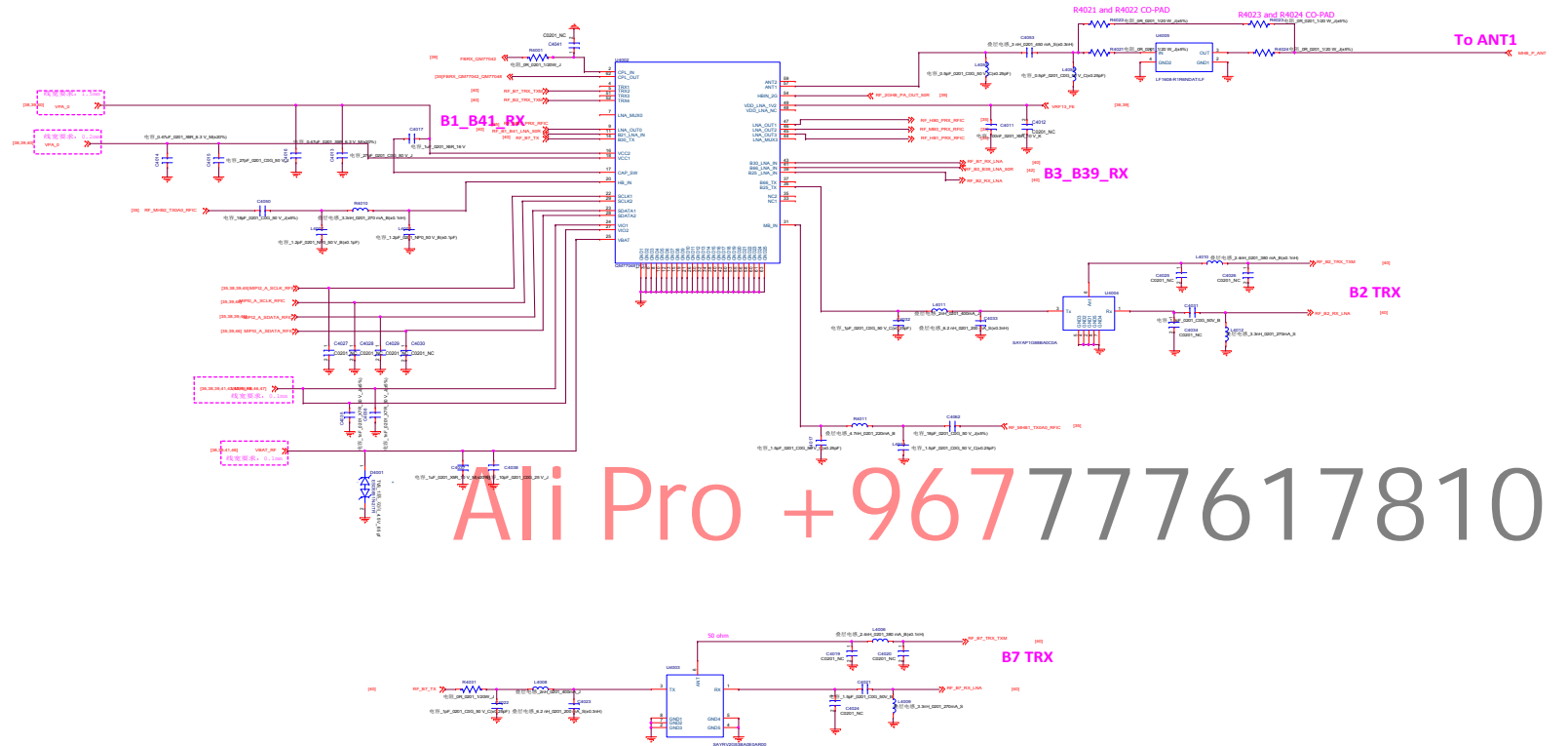
# 2G\_3G/4G/5G LB PA\_QM77042



NTC温度检测  
见P10的R1001 and NTC1001

华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title			
QM77042			
Size	Project	Rev	
A0	<Doc>	v1	
Date:	Tuesday, January 12, 2021	Sheet	39 of 48

4G/5G MHB PA\_QM77048E

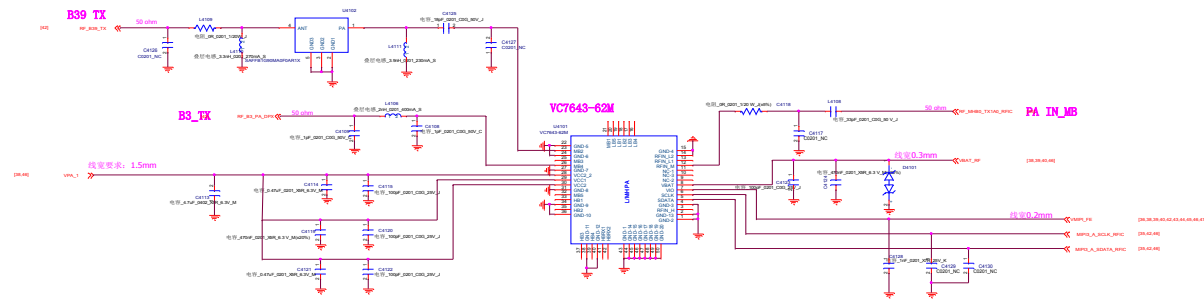


Ali Pro + 967777617810

## NTC温度检测

见P10的R1002 and NTC1002

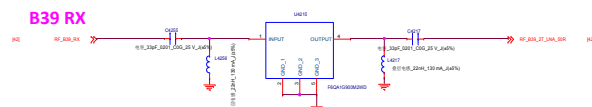
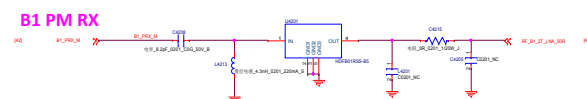
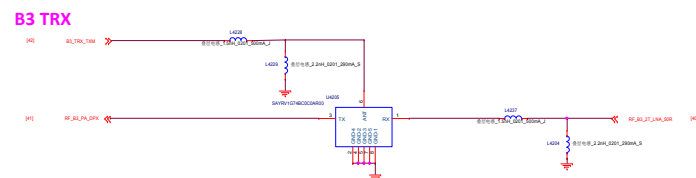
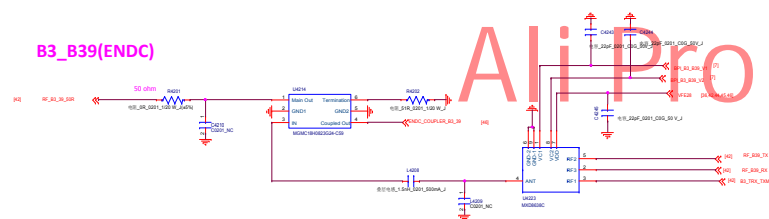
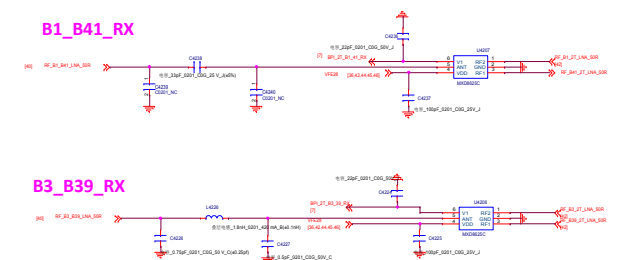
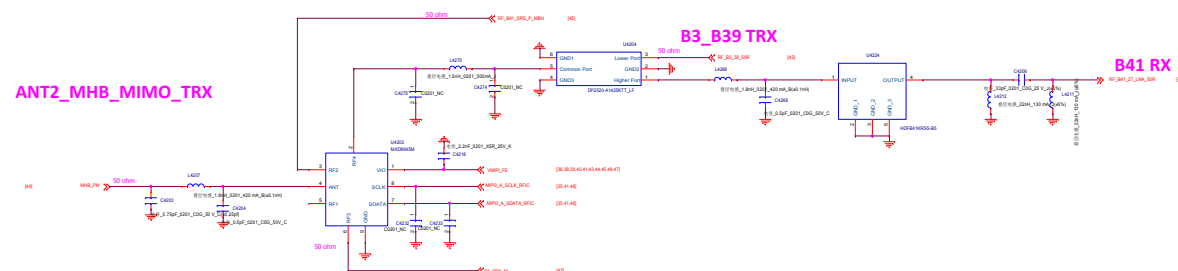
## MMMB PA\_MB ENDC



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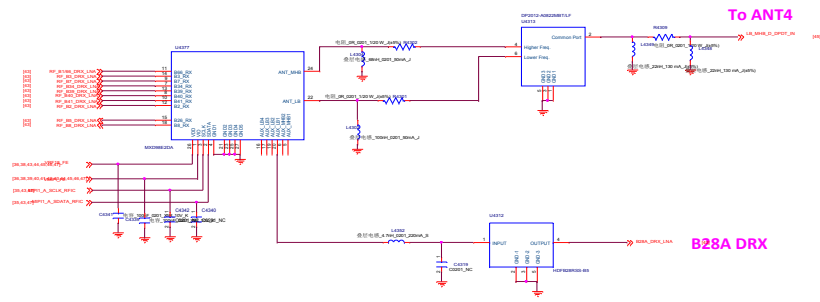
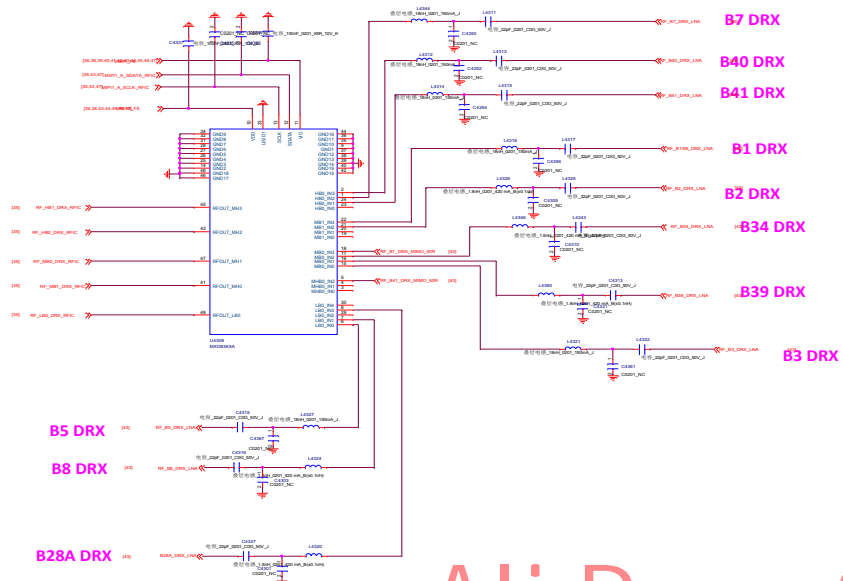
MMMB PA\_VC7643\_ENDC\_MHB\_TRX



AliPro + 967777617810

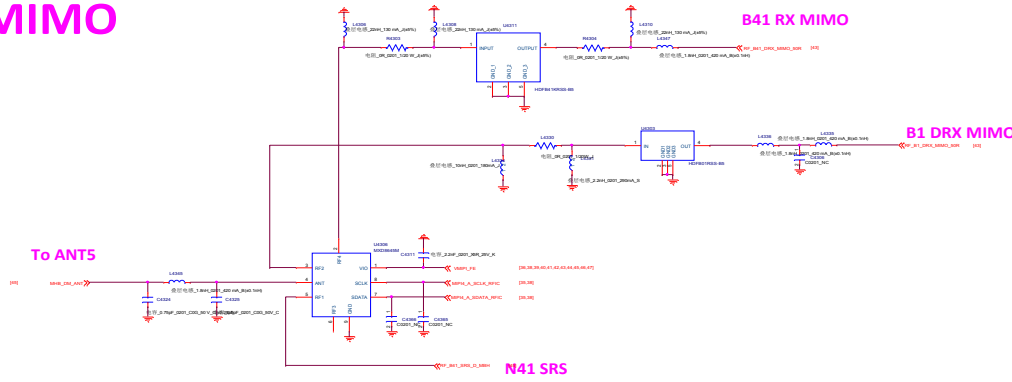
华勤通讯 Huaqin Telecom Technology Com., Ltd		
Title MMB PA_VC7643_ENDC_MHB_TRX		
Size A0	Project <Doc>	Rev v1
Date: Tuesday, January 12, 2021	Sheet 42 of 48	

DRX\_MXD98E2DA+MXD83K5A



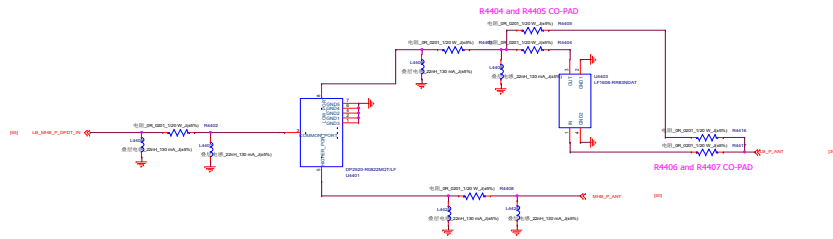
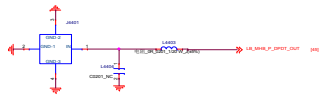
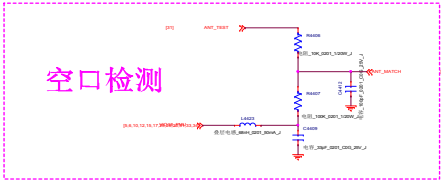
Ali Pro +967777617810

DRX\_MIMO

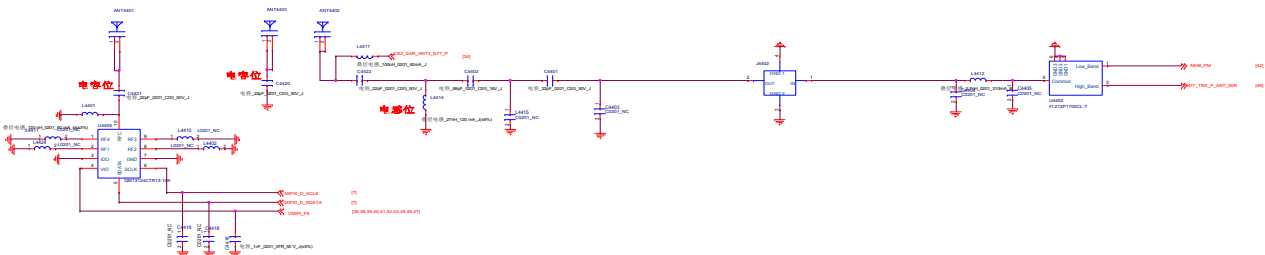


华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title MXD98E2DA_MXD83K5A			
Size E	Project	<Doc>	
Date: Tuesday, January 12, 2021	Sheet	43 of	48

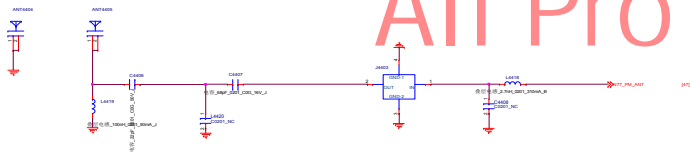
ANT1 (LB\_MHB\_P) to 小板



ANT2 (MHB\_PM/N77\_TRX)

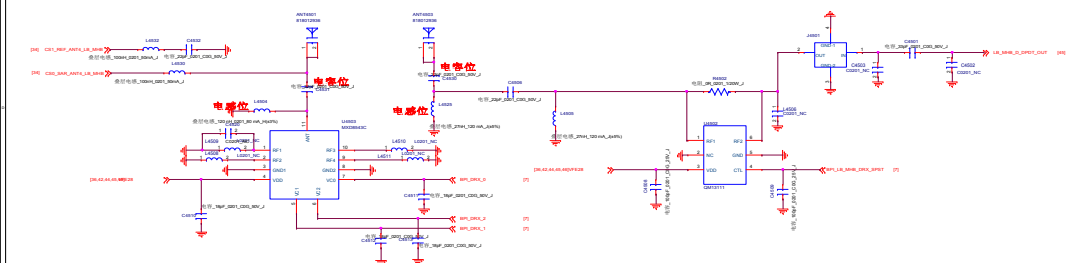


ANT3 (N77\_PM)

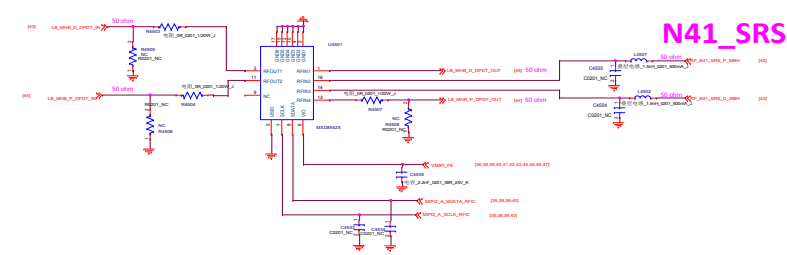


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# ANT4 (LB\_MHB\_D)

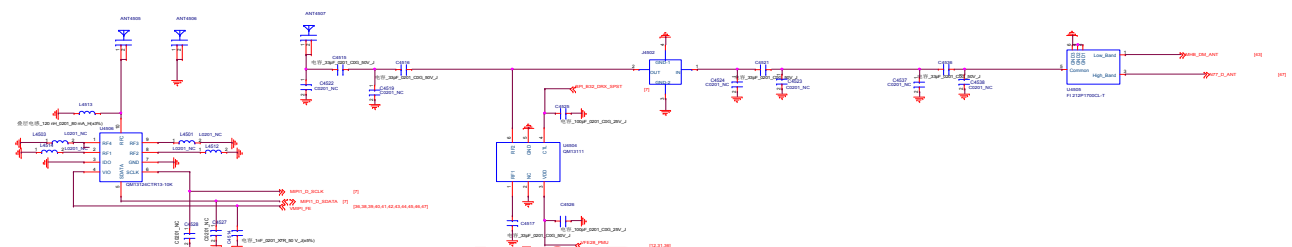


# AsDIV\_2P4T

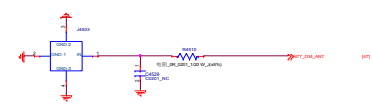


# N41\_SRS

# ANT5 (MHB\_DM/N77\_D)

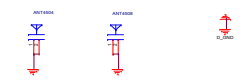


# ANT6 (N77\_DM) to 小板



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接地弹片

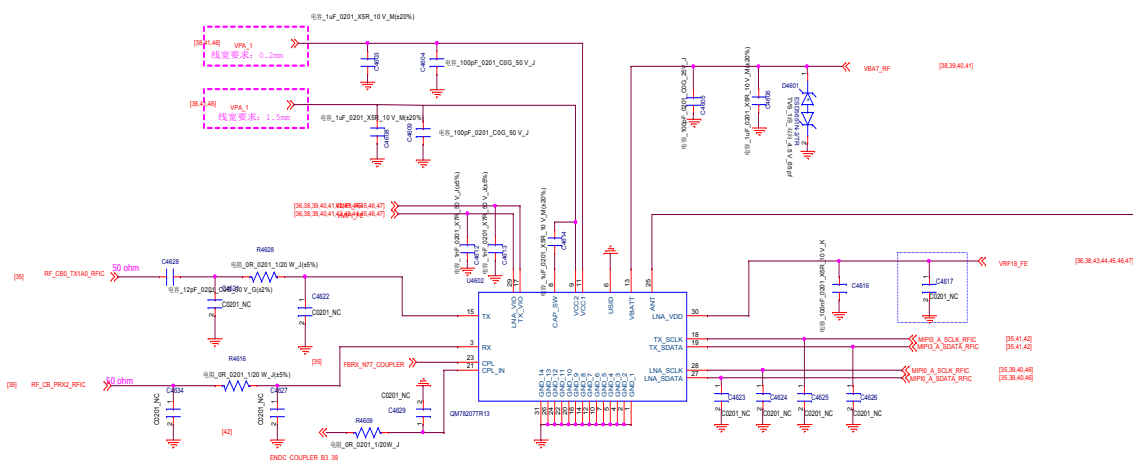


华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title ANT_D_DM			
Size E	Project <Doc>		Rev v1
Date: Tuesday, January 12, 2021		Sheet 45 of	48

N77/78\_TRX0\_QM78207

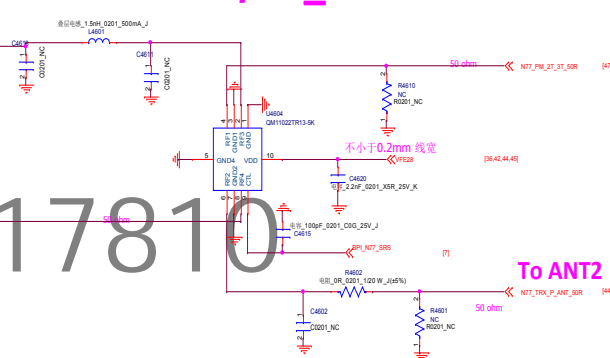
## NTC温度检测

见P10的R1002 and NTC1002



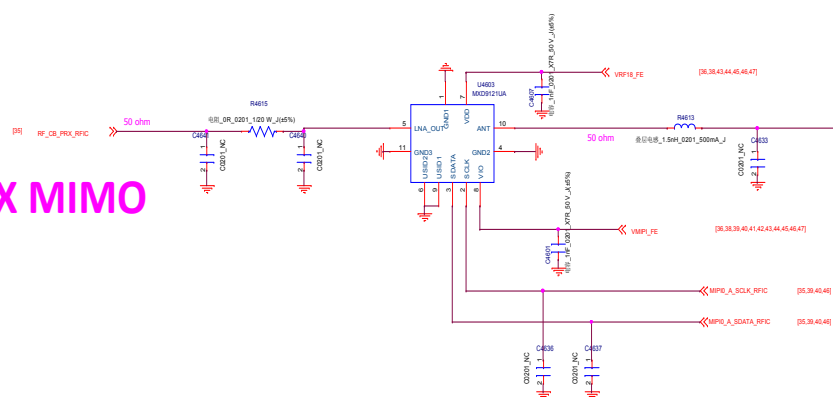
**N77\_78 PRX**

N77/78\_SRS



Ali Pro + 967777617810

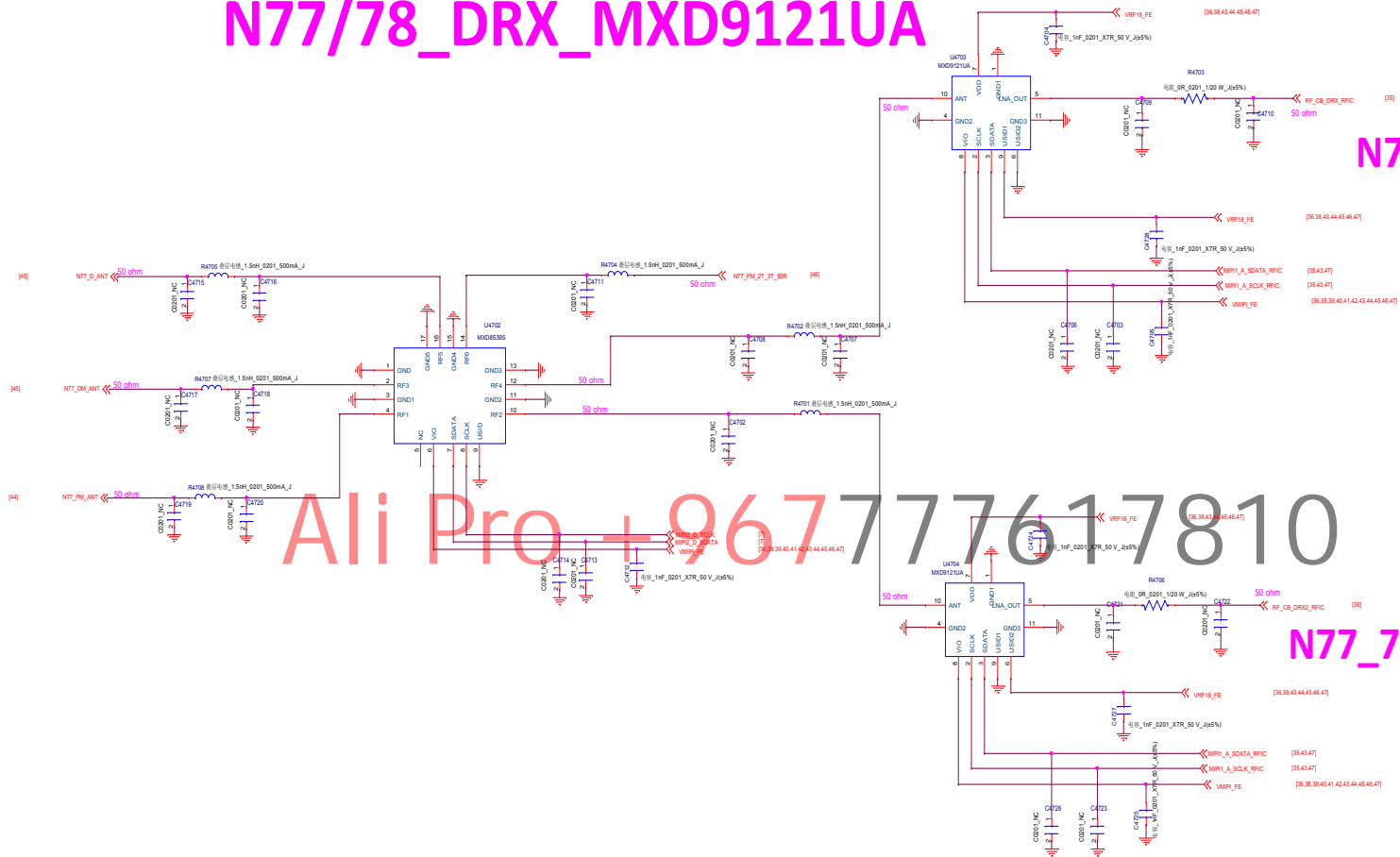
N77/78\_MIMO\_MXD9121UA



# N77\_78 PRX MIMO

<b>华勤通讯</b> Huaqin Telecom Technology Com.,Ltd			
<b>Title</b> <div style="text-align: center; font-size: 1.2em;">N77/78_TRX0_QM78207</div>			
<b>Size</b> <div style="text-align: center;">D</div>	<b>Project</b> <div style="text-align: center; font-size: 1.2em;">&lt;Doc&gt;</div>		<b>Rev</b> <div style="text-align: center;">v1</div>
<b>Date:</b> Tuesday, January 12, 2021		<b>Sheet</b> 46 of 48	

# N77/78\_DRX\_MXD9121UA

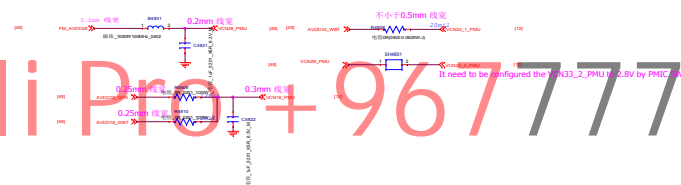


## N77\_78 DRX

## N77\_78 DRX MIMO

华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title <div style="text-align: center; font-size: 1.2em;">N77/78_DRX_MXD9121UA</div>			
Size D	Project <div style="text-align: center; font-size: 1.2em;">&lt;Doc&gt;</div>		Rev v1
Date: Tuesday, January 12, 2021		Sheet 47 of 48	

# MT6631



Ali Pro + 96777617810

<b>华勤通讯</b> Huaqin Telecom Technology Com., Ltd			
<b>Title</b> <div style="text-align: center; font-size: 1.2em; font-weight: bold;">MT6631</div>			
<b>Size</b> E	<b>Project</b> <div style="text-align: center; font-size: 1.2em; font-weight: bold;">&lt;Doc&gt;</div>	<b>Rev</b> v1	
<b>Date:</b> Tuesday, January 12, 2021		<b>Sheet</b> 48 of 48	