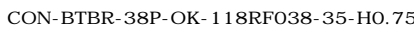


## Exchange IO



The schematic diagram illustrates the FM LNA (Low Noise Amplifier) circuit. The main components and their connections are as follows:

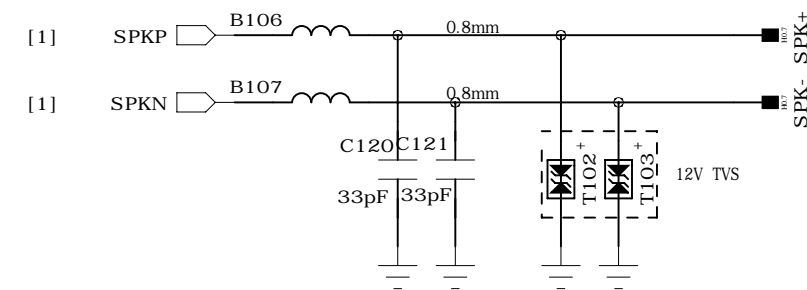
- Input Stage:** The FM\_ANT\_P and FM\_ANT\_N pins are connected to the input of the FM LNA chip (U101). The input matching network includes capacitors C104, C105, C107, and C108, and inductors B108, B109, B110, and B111.
- Matching Network:** The input matching network is designed to match the 50Ω impedance of the antenna to the input impedance of the FM LNA chip. It includes a series inductor B110 (3.3K) and a shunt capacitor C110 (15R).
- DC Biasing:** The FM LNA chip is biased using a 1.2V supply (VFE28\_PMU) through a 100nF capacitor (C128) and a 100nF capacitor (C129). The biasing network also includes a 100nF capacitor (C125) and a 100nF capacitor (C126).
- Output Stage:** The output of the FM LNA chip is connected to the FM\_ANT\_P and FM\_ANT\_N pins through a 100nF capacitor (C129) and a 100pF capacitor (C107).
- Component Values:**
  - Capacitors: C104, C105, C107, C108, C110, C111, C112, C117, C125, C126, C127, C128, C129.
  - Resistors: R110, R112, R129.
  - Inductors: B108, B109, B110, B111, L102, L103.
  - Transistors: T105, T106, T107, T111, T112.

The diagram also includes a detailed view of the FM LNA chip (U101) and its connections to the VFE28\_PMU and the FM\_ANT\_P and FM\_ANT\_N pins. The chip is labeled "IC-FMLNA-AW5017DNR-H0.6" and "U101".

TL101  
TRANSSION-2.5H

WPL101

MARK101  
1 ⊕



COMPANY: TRANSSION HOLDINGS				MODEL: H6915		Modified Date: 2021/8/31	
DRAWN	ZY/DLA	DATED	20180613	TITLE: 1_SUB_AU		VERSION: V1.2	SHEET: 1 OF 2
CHECKED	< CHECKED >	DATED	< >	Confidentiality	CONFIDENTIAL		



