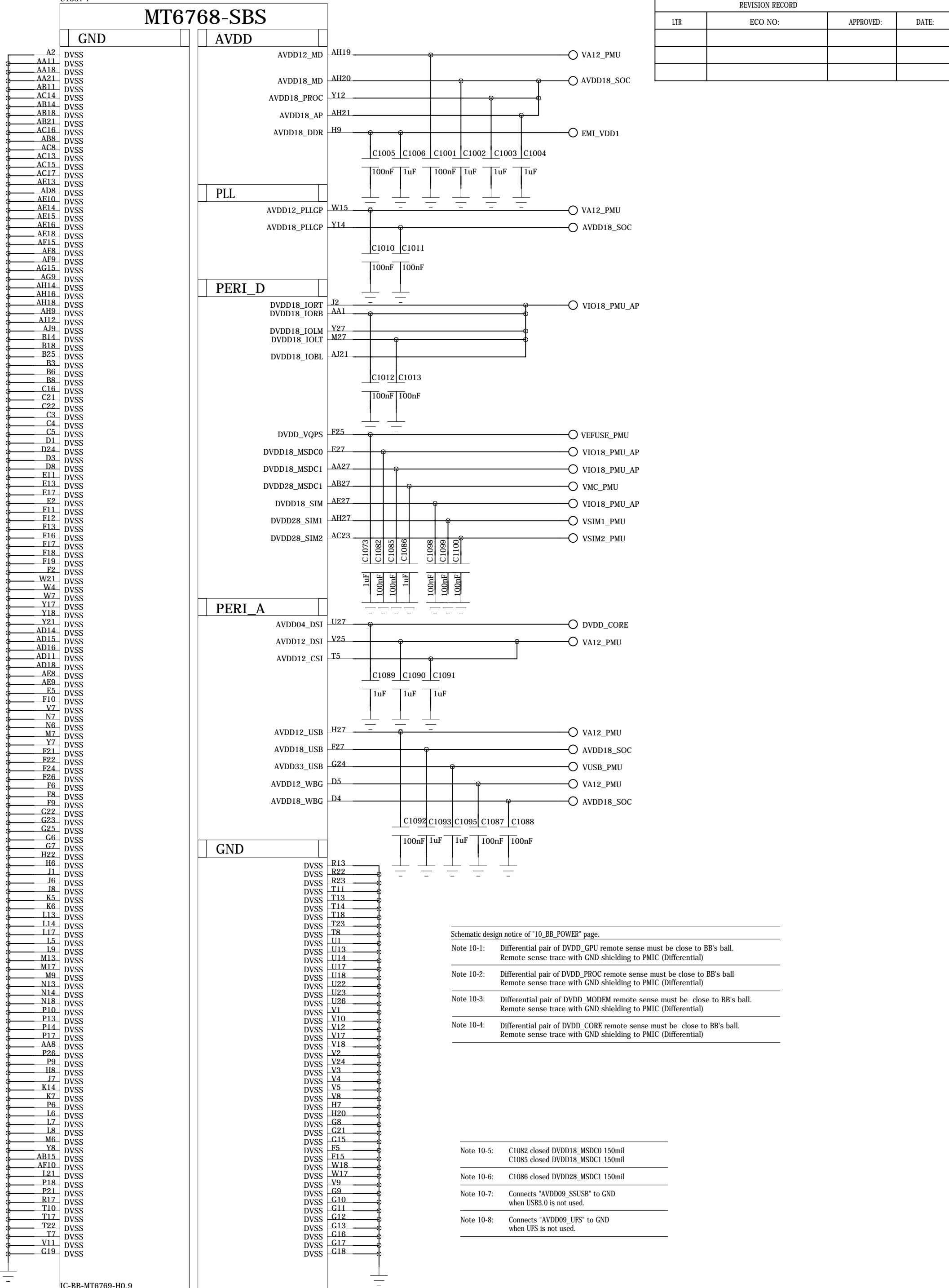
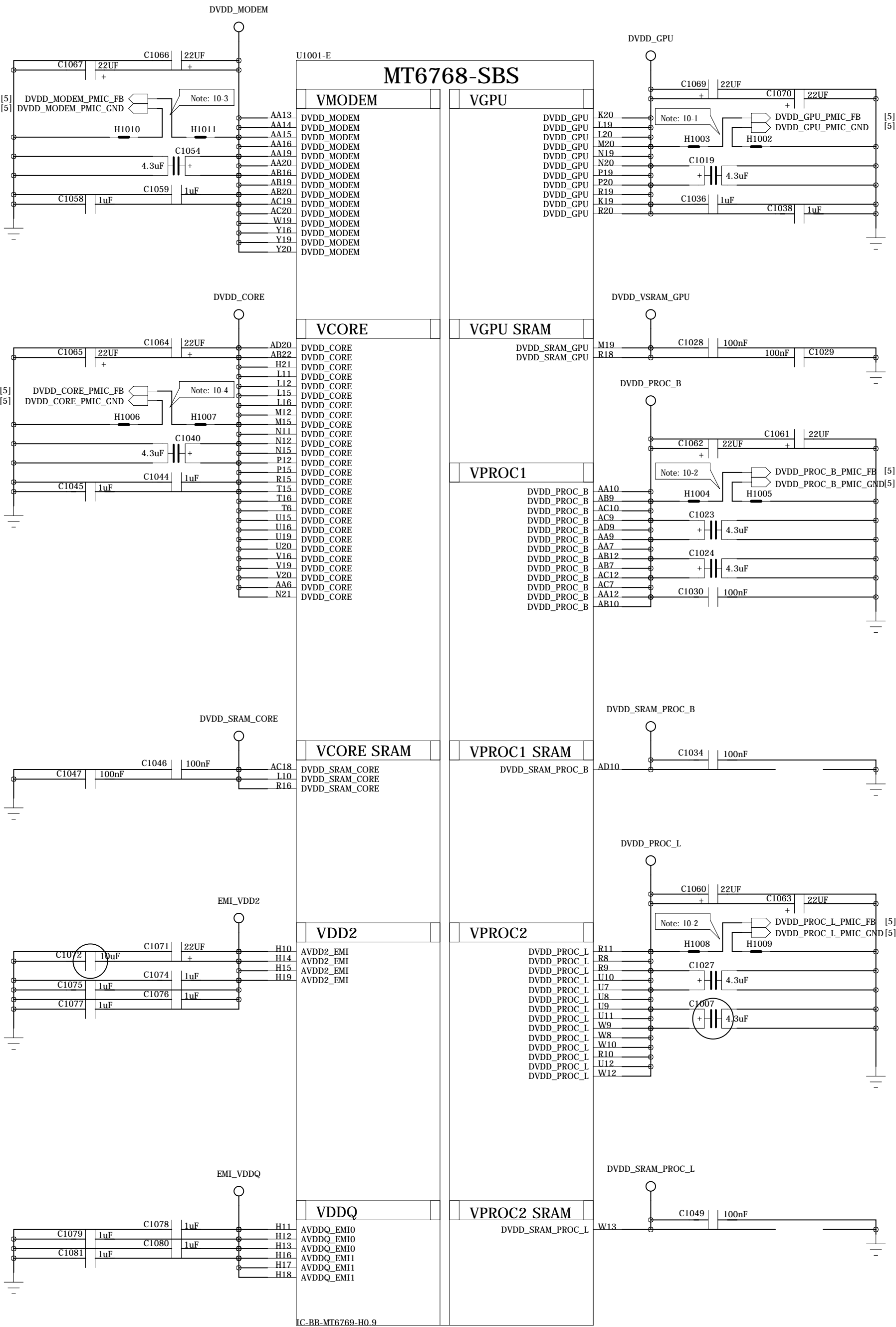


BB_POWER



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

Schematic design notice of "10_BB_POWER" page.

Note 10-1: Differential pair of DVDD_GPU remote sense must be close to BB's ball.
Remote sense trace with GND shielding to PMIC (Differential)

Note 10-2: Differential pair of DVDD_PROC remote sense must be close to BB's ball
Remote sense trace with GND shielding to PMIC (Differential)

Note 10-3: Differential pair of DVDD_MODEM remote sense must be close to BB's ball.
Remote sense trace with GND shielding to PMIC (Differential)

Note 10-4: Differential pair of DVDD_CORE remote sense must be close to BB's ball.
Remote sense trace with GND shielding to PMIC (Differential)

Note 10-5: C1082 closed DVDD18_MSDC0 150mil
C1085 closed DVDD18_MSDC1 150mil

Note 10-6: C1086 closed DVDD28_MSDC1 150mil

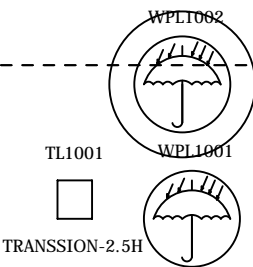
Note 10-7: Connects "AVDD09_SSUSB" to GND
when USB3.0 is not used.

Note 10-8: Connects "AVDD09_UFS" to GND
when UFS is not used.

SHIELDING&LOGO&SN&MARK

SH1001 SH1002 SH1003 SH1004 SH1005 SH1006

SH1007 SH1008 Reserve for CAM fixation J1001 default NC



MARK1001 MARK1002 MARK1003 MARK1004
MARK_1.0 MARK_1.0 MARK_1.0 MARK_1.0

COMPANY: TRANSSION HOLDINGS

MODEL: H696

Modified Date: 2020/12/30

DRAWN

DJF/TS

DATED2020/08/13

TITLE: 10_BB_POWER

CHECKED

<CHECKED>

DATED

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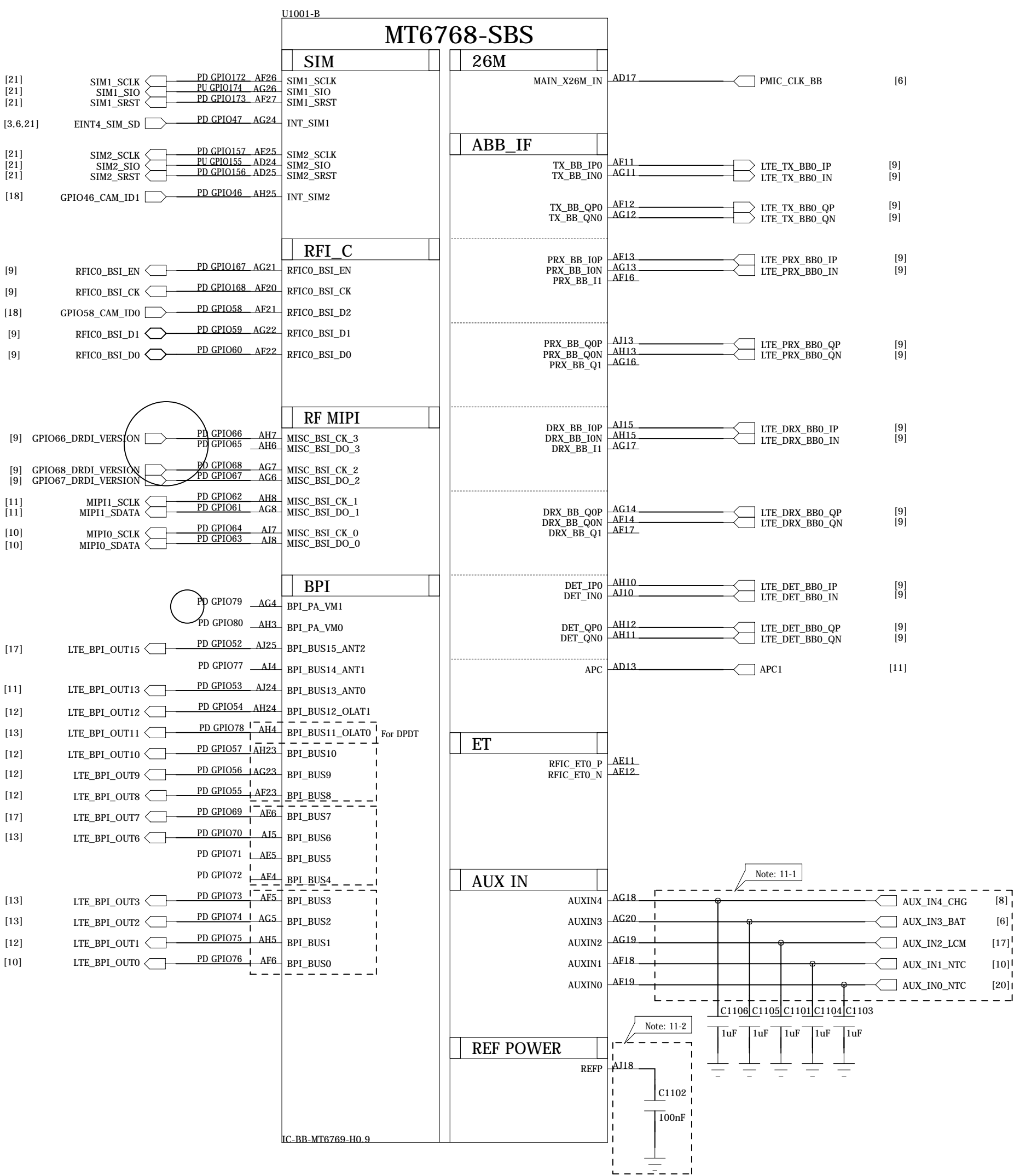
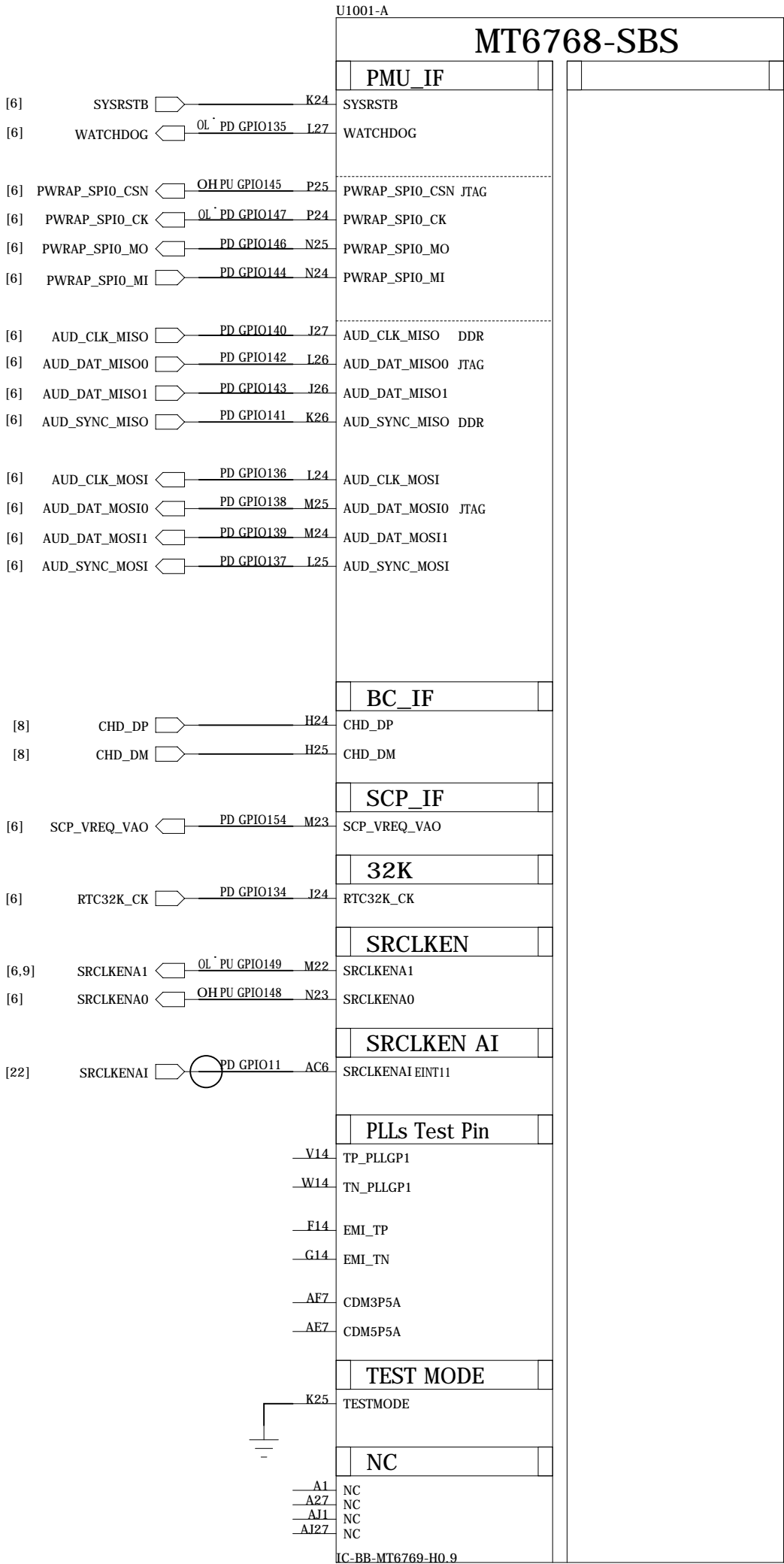
Confidentiality

CONFIDENTIAL

VERSION: V1.0

SHEET: 1 OF 22

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



*PWRAP_SPI0_CSN" and "AUD_DAT_MOSI0" are bootstrap pin to slect which interface will be the JTAG pin out.			
Default	PWRAP_SPI0_CSN	AUD_DAT_MOSI0	AP_JTAG
	HI	LO	N/A
	HI	HI	SPI0+EINT8
	LO (by ext. PU)	LO (by ext. PU)	SPI0+EINT8
	LO (by ext. PU)	HI (by ext. PU)	N/A

AUD_SYNC_MISO and AUD_CLK_MISO are DDR type feature in bootstrap			
Default	AUD_SYNC_MISO	AUD_CLK_MISO	DDR Type
	LO	LO	LP4X eMCP
	LO	HI (by ext. PU)	Reserved
	HI (by ext. PU)	LO	LP3 eMCP
	HI (by ext. PU)	HI (by ext. PU)	Reserved

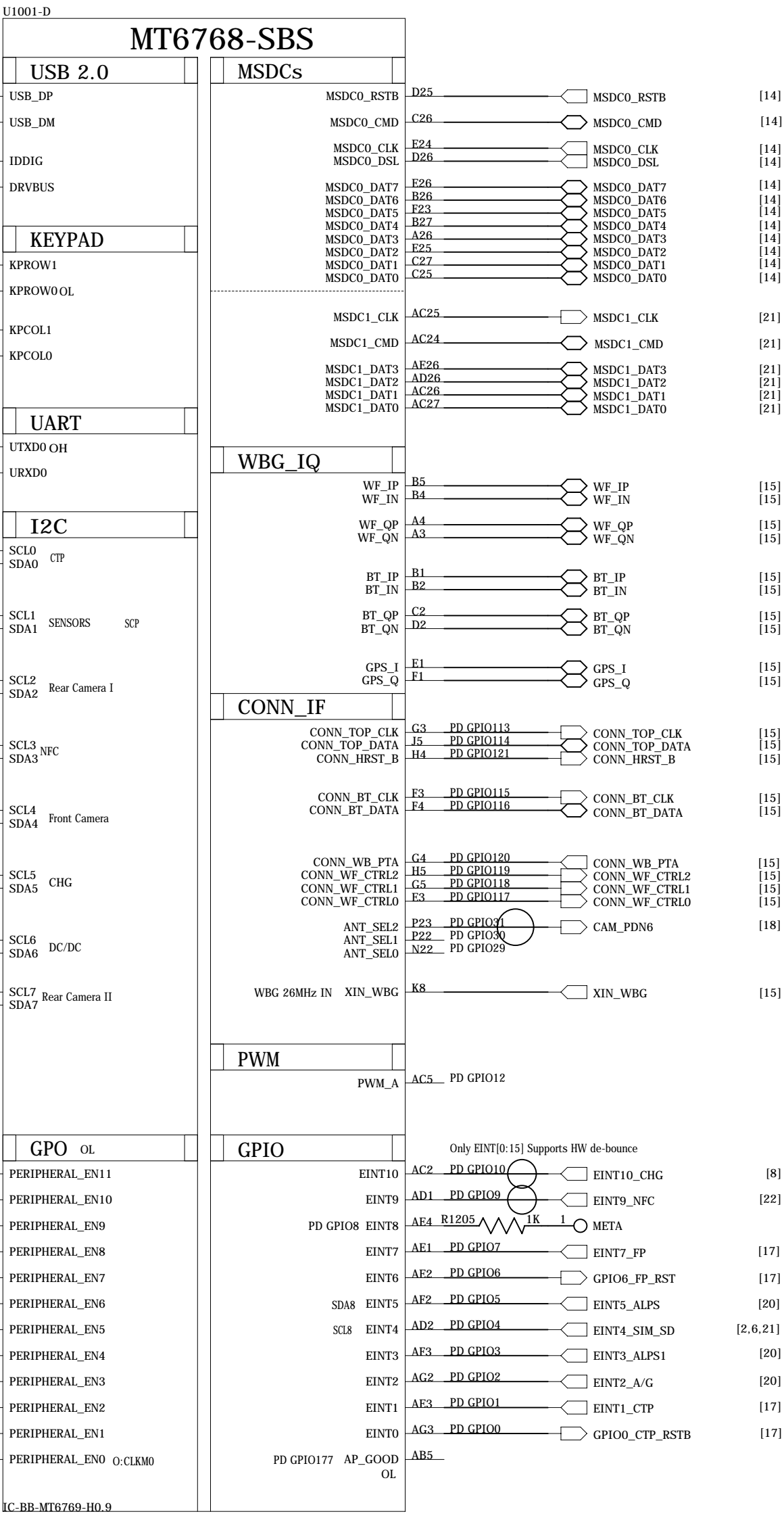
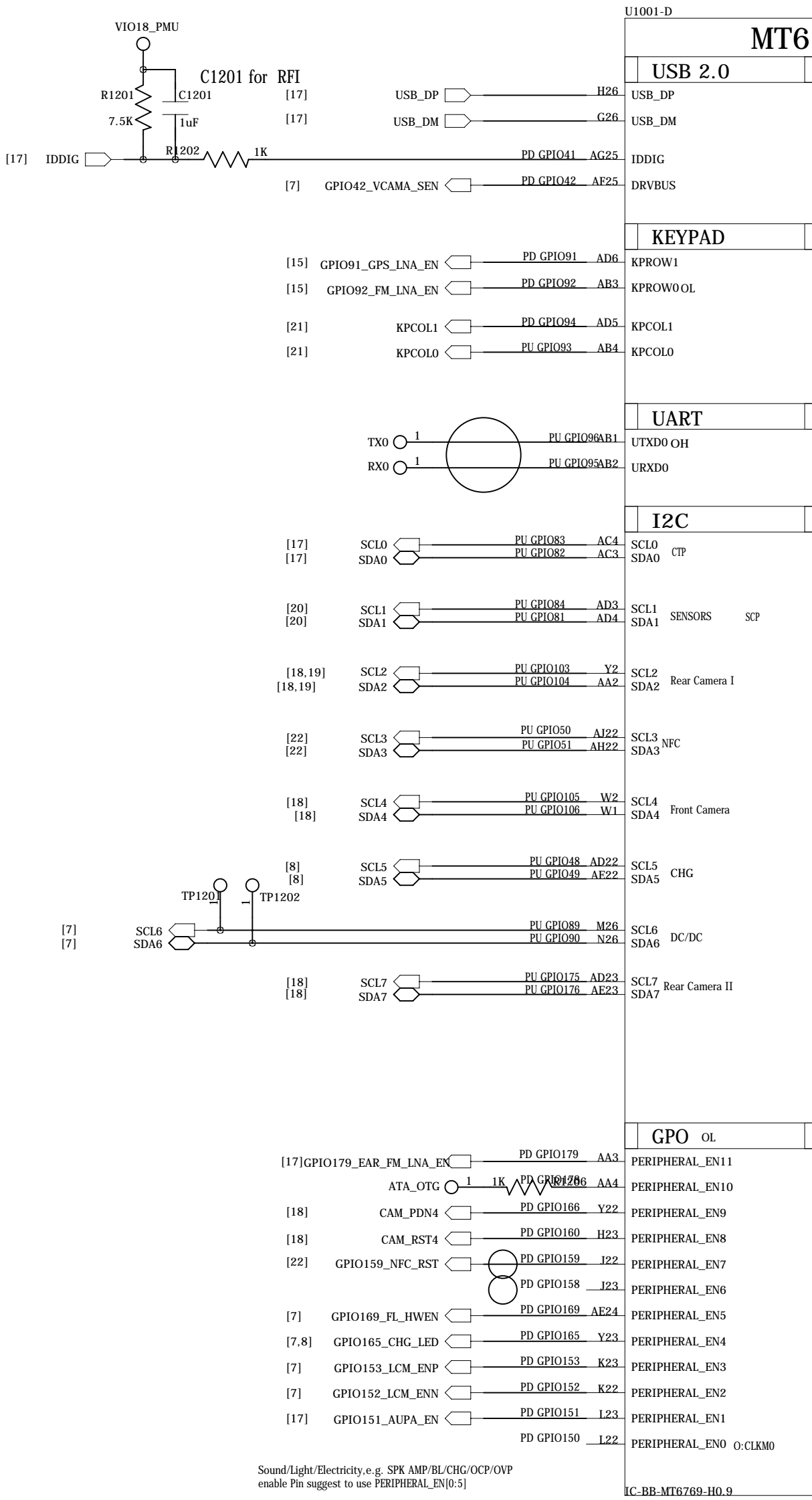
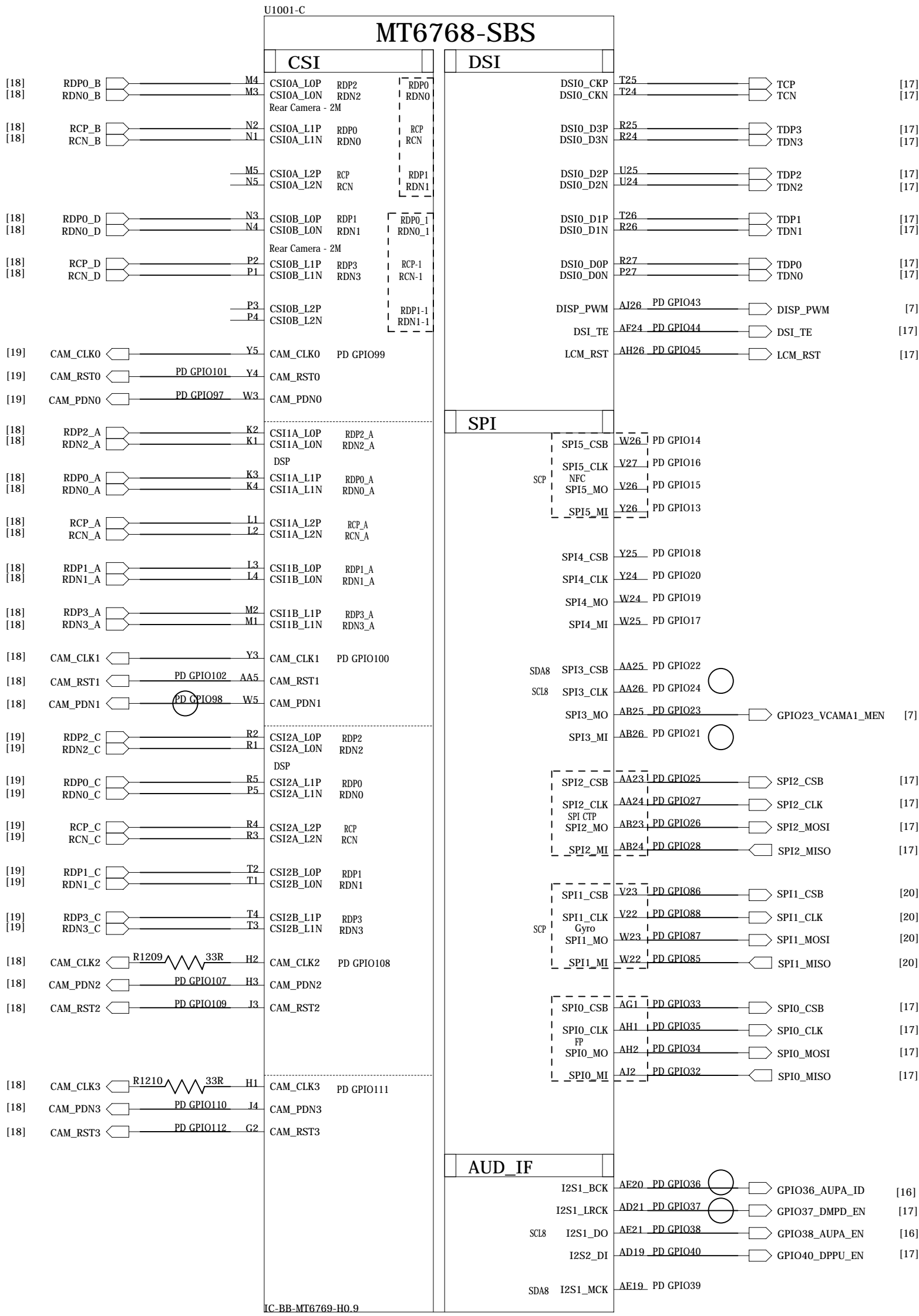
Schematic design notice of "11_BB_I" page.

Note 11-1: To shunt a 1uF capacitor in the AUXIN ADC input to prevent noise coupling. It should be placed as close to BB as possible. Connect the unused AUX ADC input to GND.

Note 11-2: The de-coupling cap. for REFP (AJ21 ball) have to be placed as close to BB as possible.

COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 11_BB_I		VERSION: V1.0	SHEET: 2 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

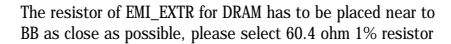
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 12_BB_II		VERSION: V1.0	SHEET: 3 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

U1001-G

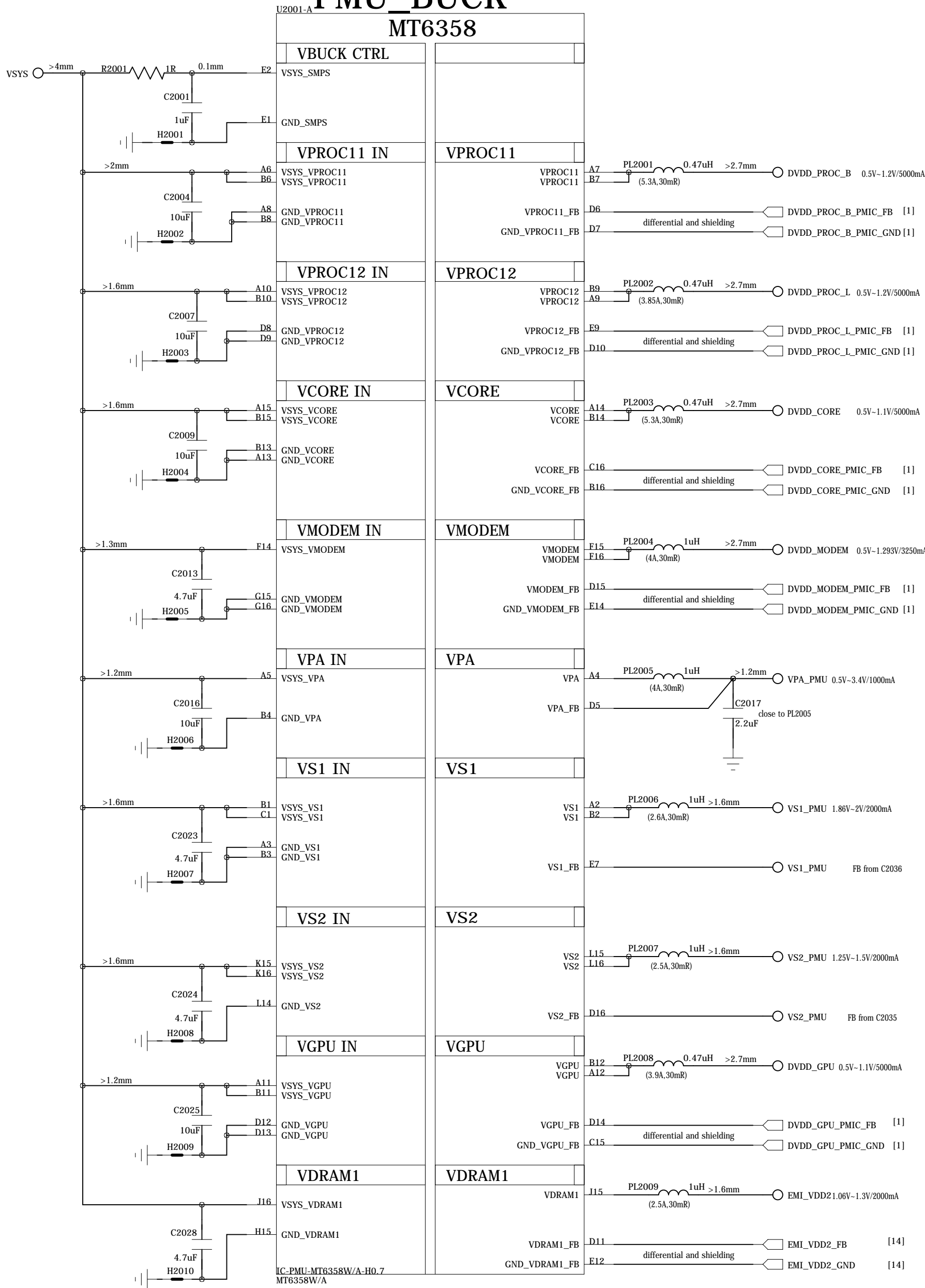
MT6768-SBS



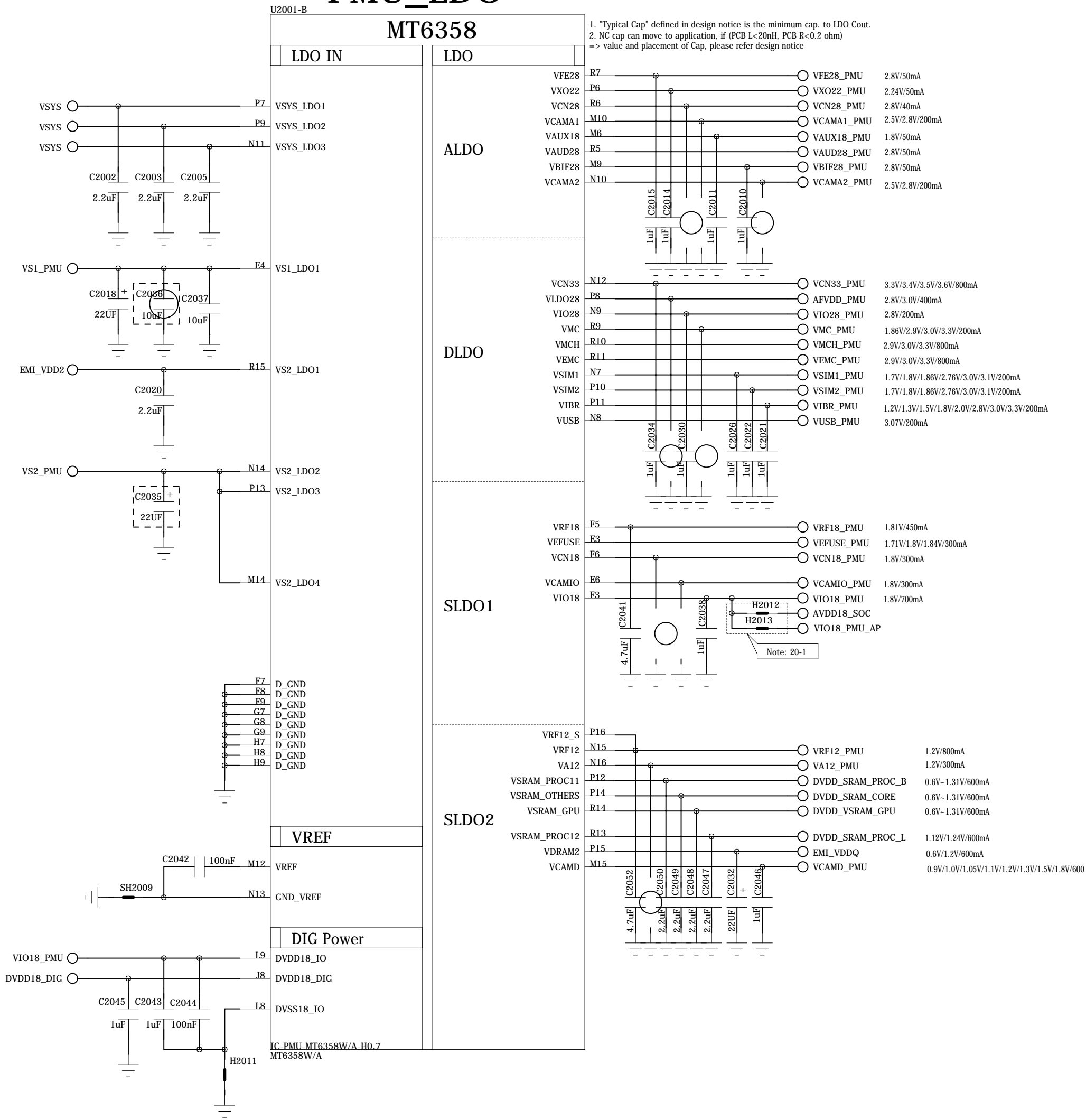
COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 13_BB_III		VERSION: V1.0	SHEET: 4 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

POWER_MT6358_I

PMU_BUCK



PMU_LDO



Schematic design notice of "21_POWER_MT6358-LDO" page.

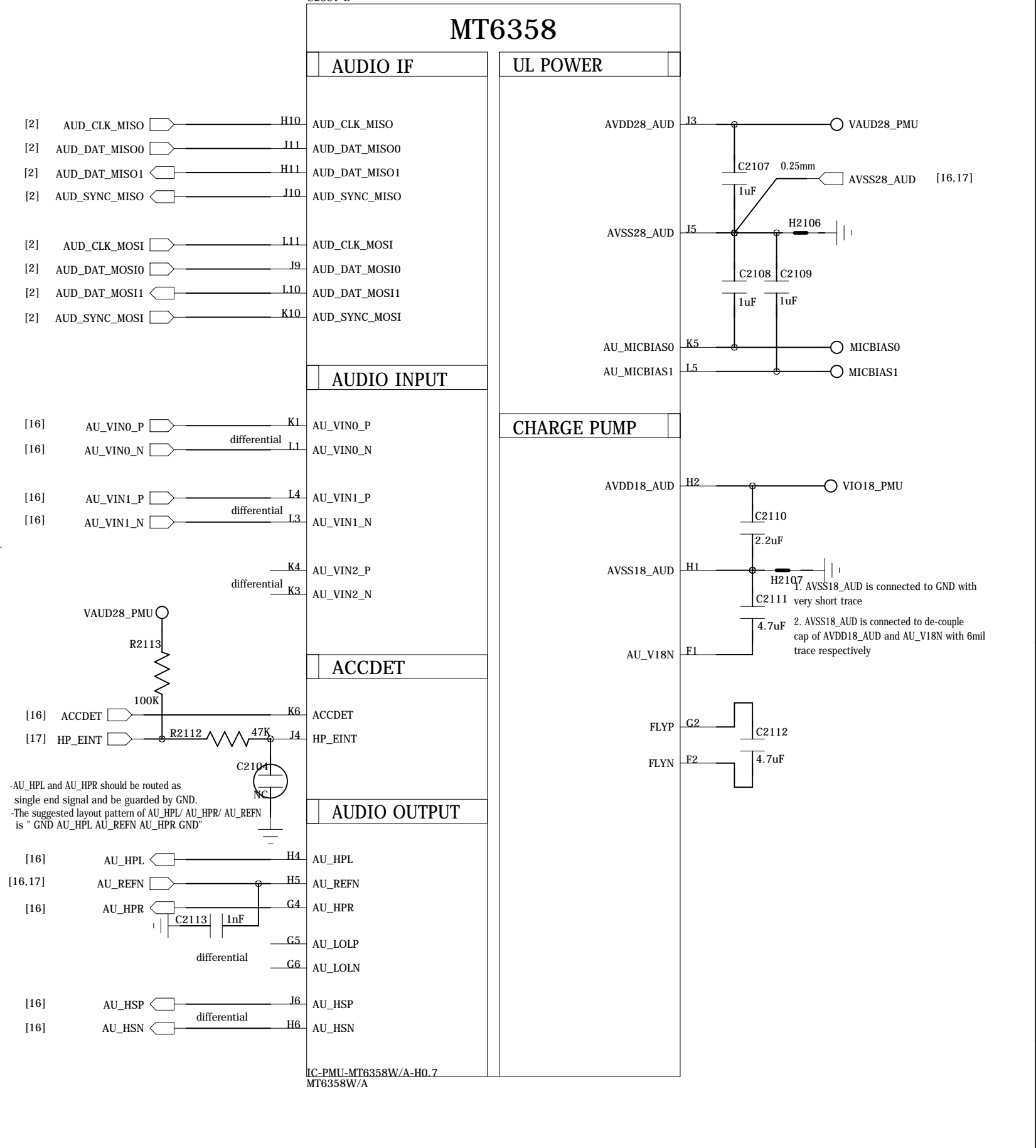
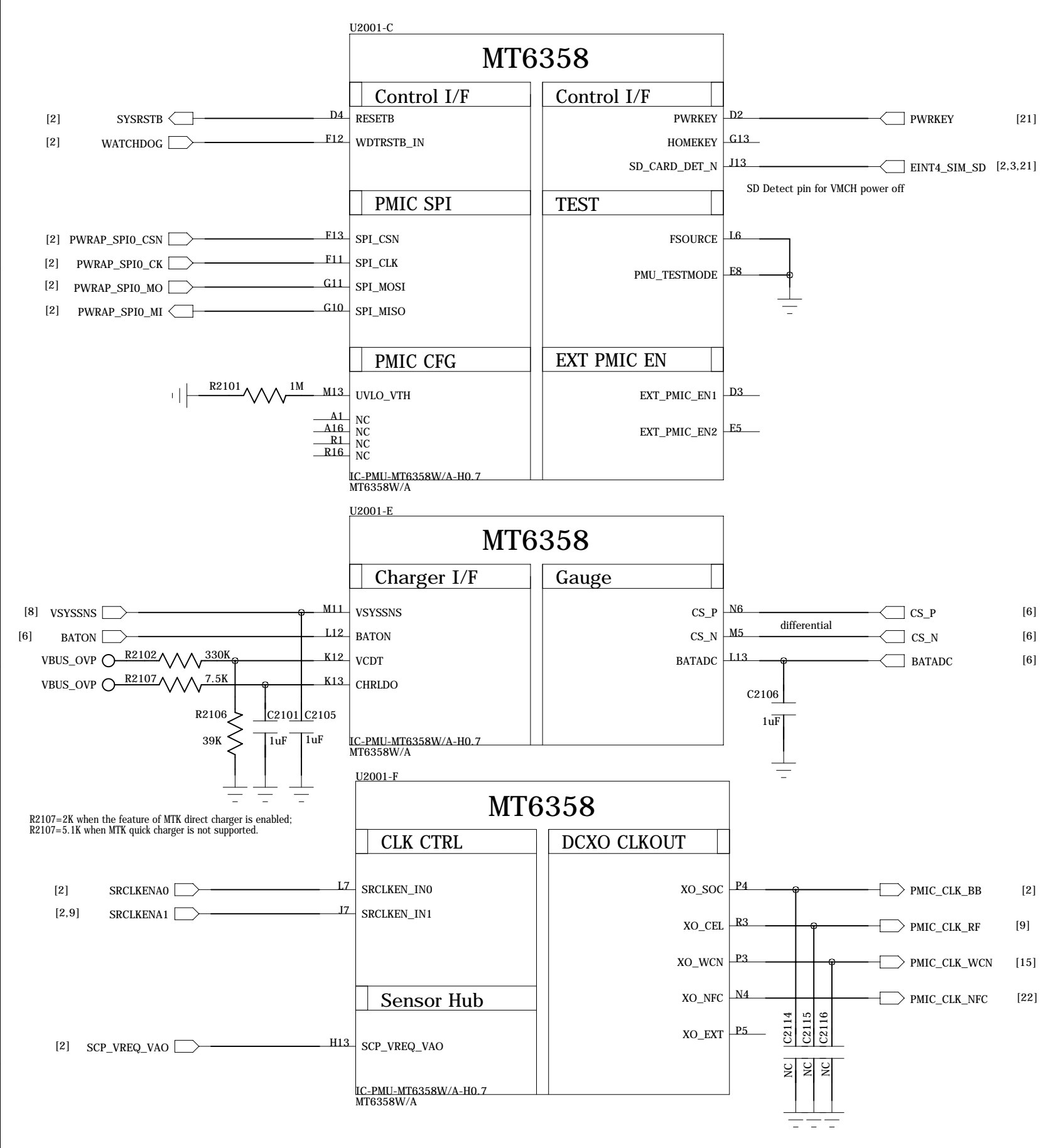
Note 21-1: Please set H2012/H2013 close to C2038, making star connection between VIO18_PMU and AVDD18_SOC/VIO18_PMU_AP near to LDO cap. C2038

Please also refer to MT6358 design notice for further detail design information

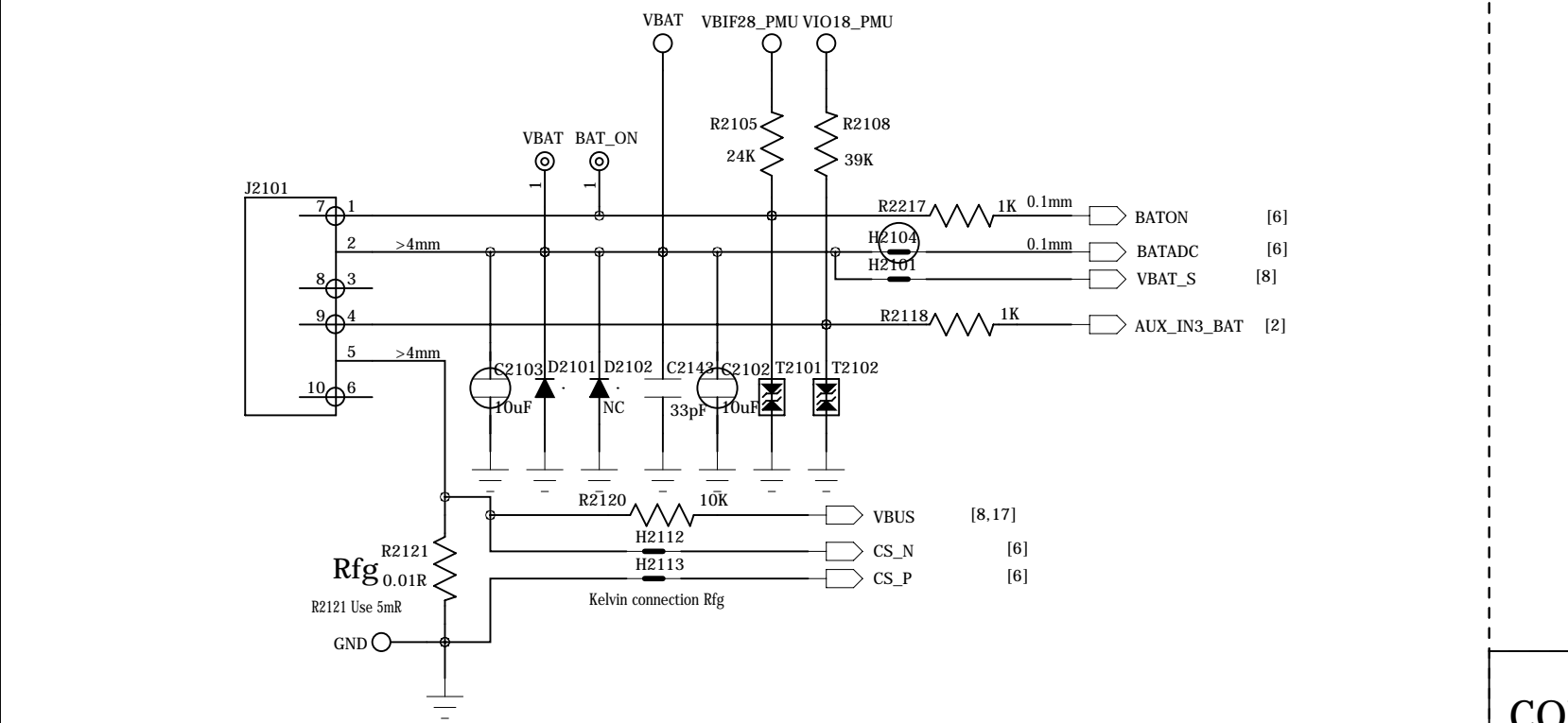
COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 20_POWER_MT6358_I		VERSION: V1.0	SHEET: 5 OF 22
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POWER_MT6358_II

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



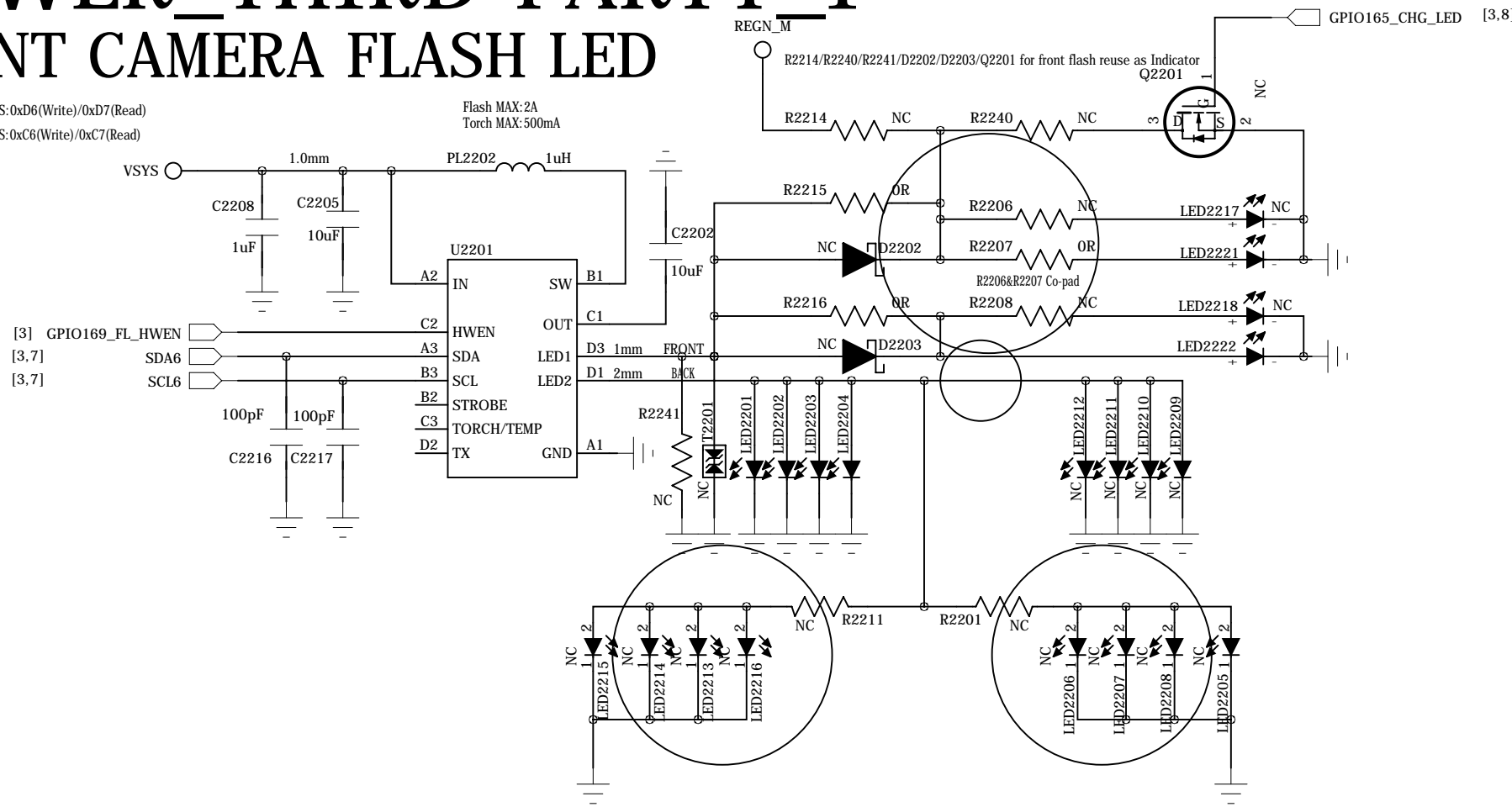
BATTERY CONNECTOR



COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 21_POWER_MT6358_II		VERSION: V1.0	SHEET: 6 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

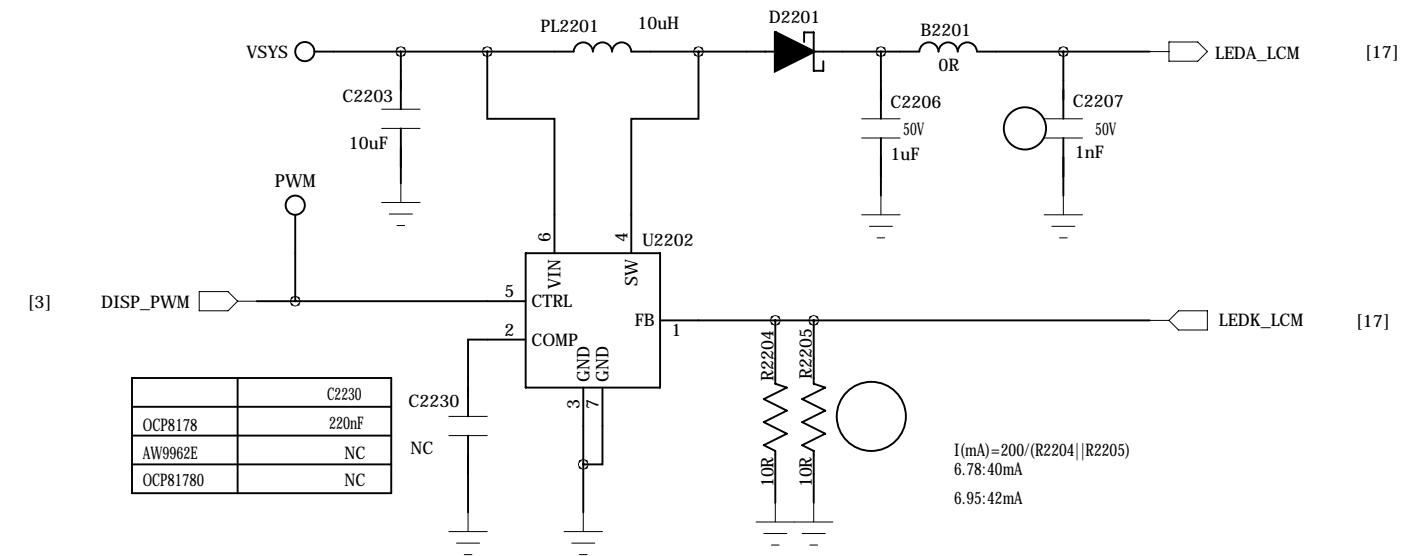
POWER_THIRD-PARTY_I
FRONT CAMERA FLASH LED

AW36515:I2C ADDRESS:0xD6(Write)/0xD7(Read)
OCP81375:I2C ADDRESS:0xC6(Write)/0xC7(Read)

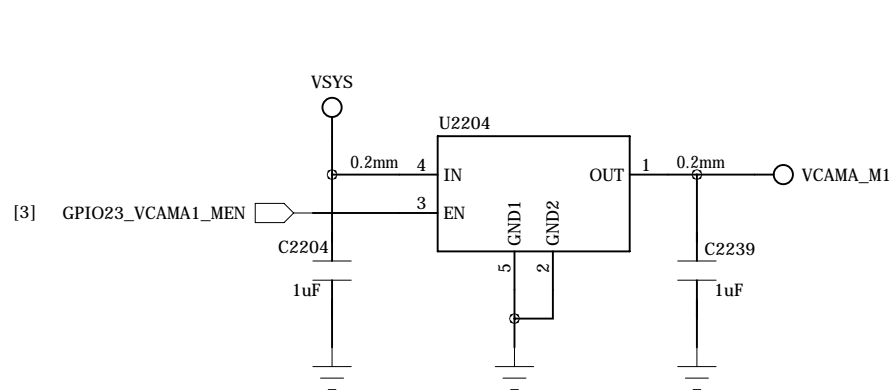


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

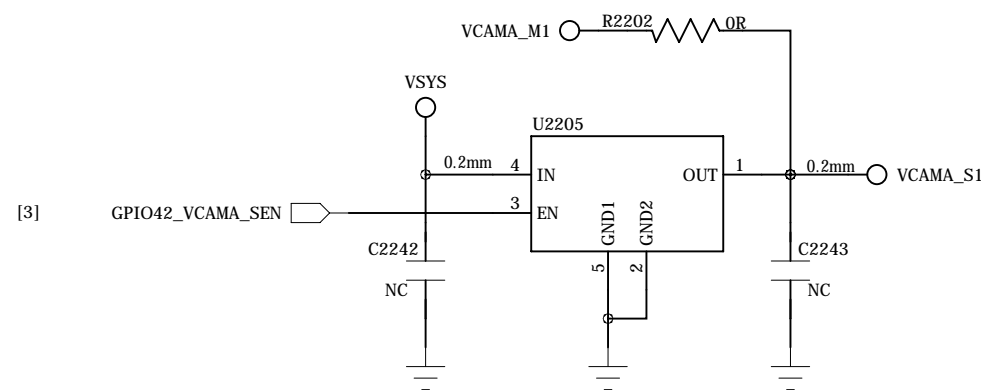
LCM Backlight LED Driver



VCAMA_MAIN_1 (2.8V)

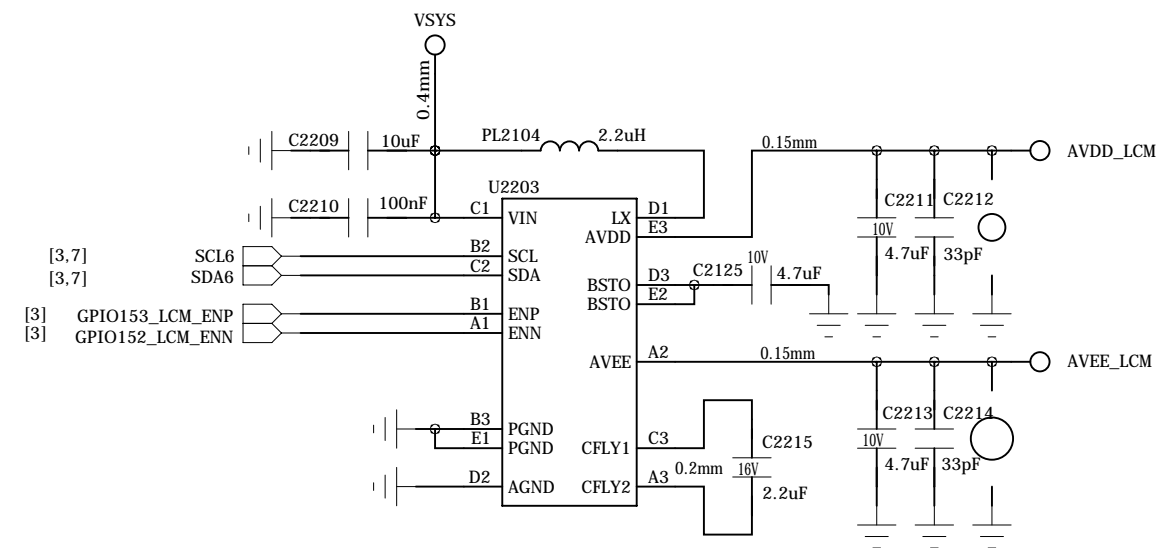


VCAMA_SUB_1 (2.8V)



LCM BIAS

SM5109:I2C ADDRESS:0x7C(Write)/0x7D(Read)



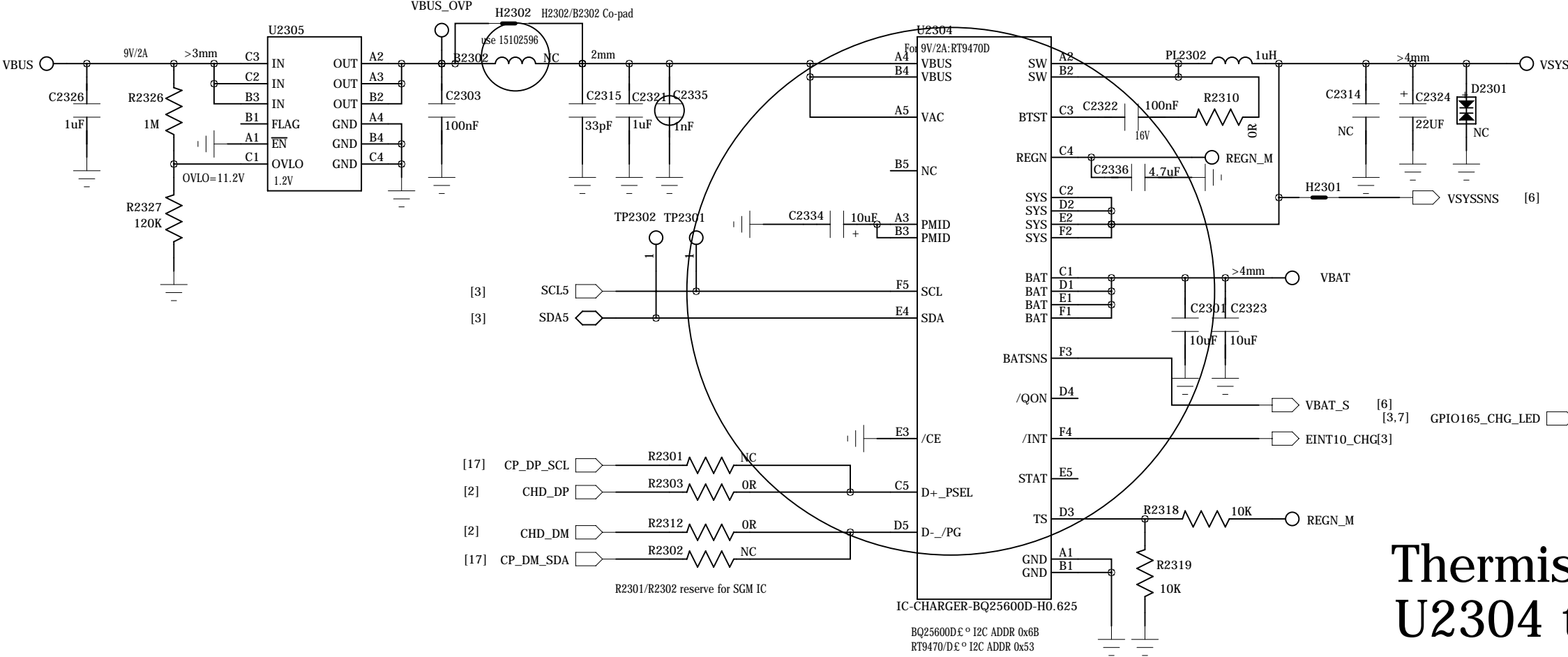
COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 22_POWER_THIRD-PARTY_I		VERSION: V1.0	SHEET: 7 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

POWER_THIRD-PARTY_II

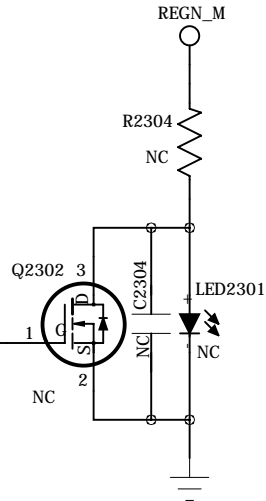
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

OVP

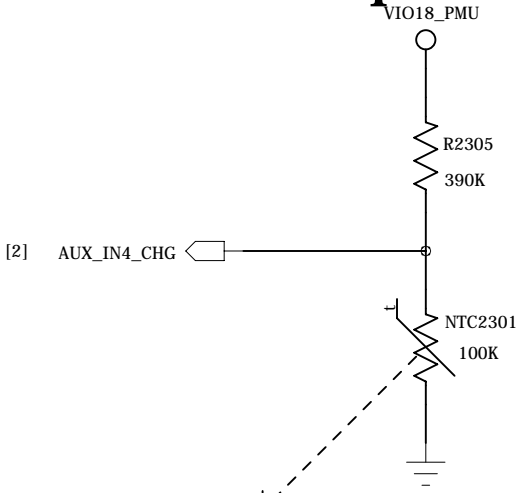
CHARGER_I



CHG LED

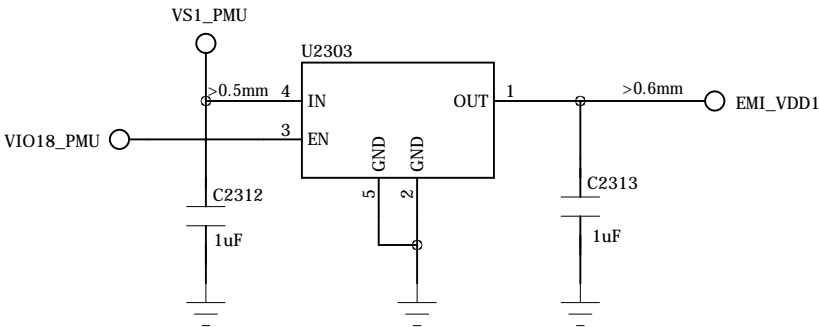


Thermistor to sense U2304 temperature

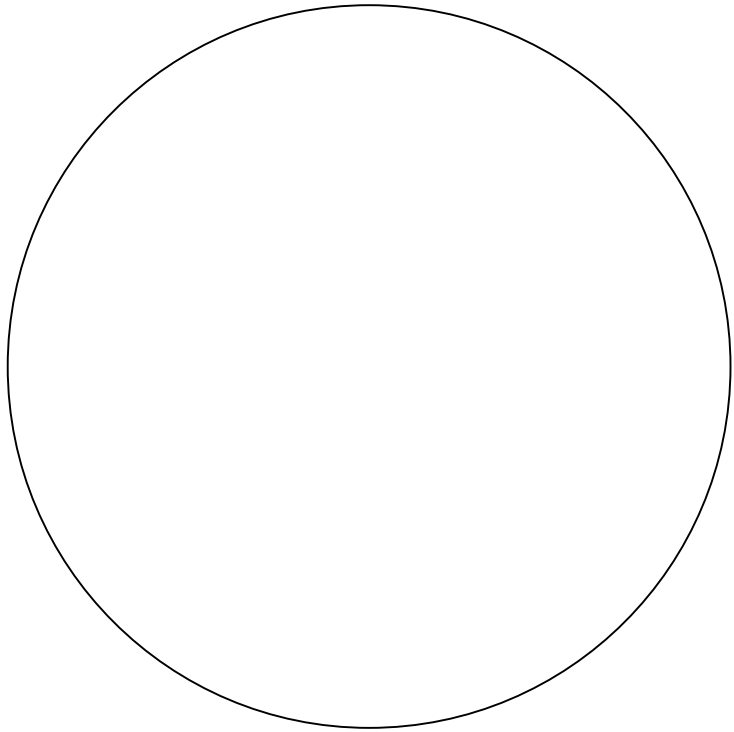


- 1. NTC2301 must keep a distance about 2 mm away from U2304 and far from other heat sources 10 mm at least.
- 2. The distance is the shortest distance from package edge to edge.

LPDDR4X VDD1 1.8V LDO



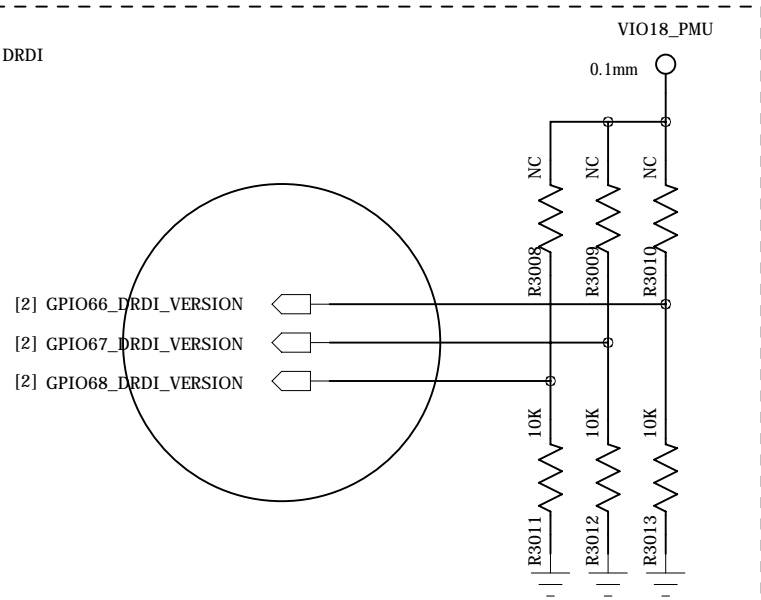
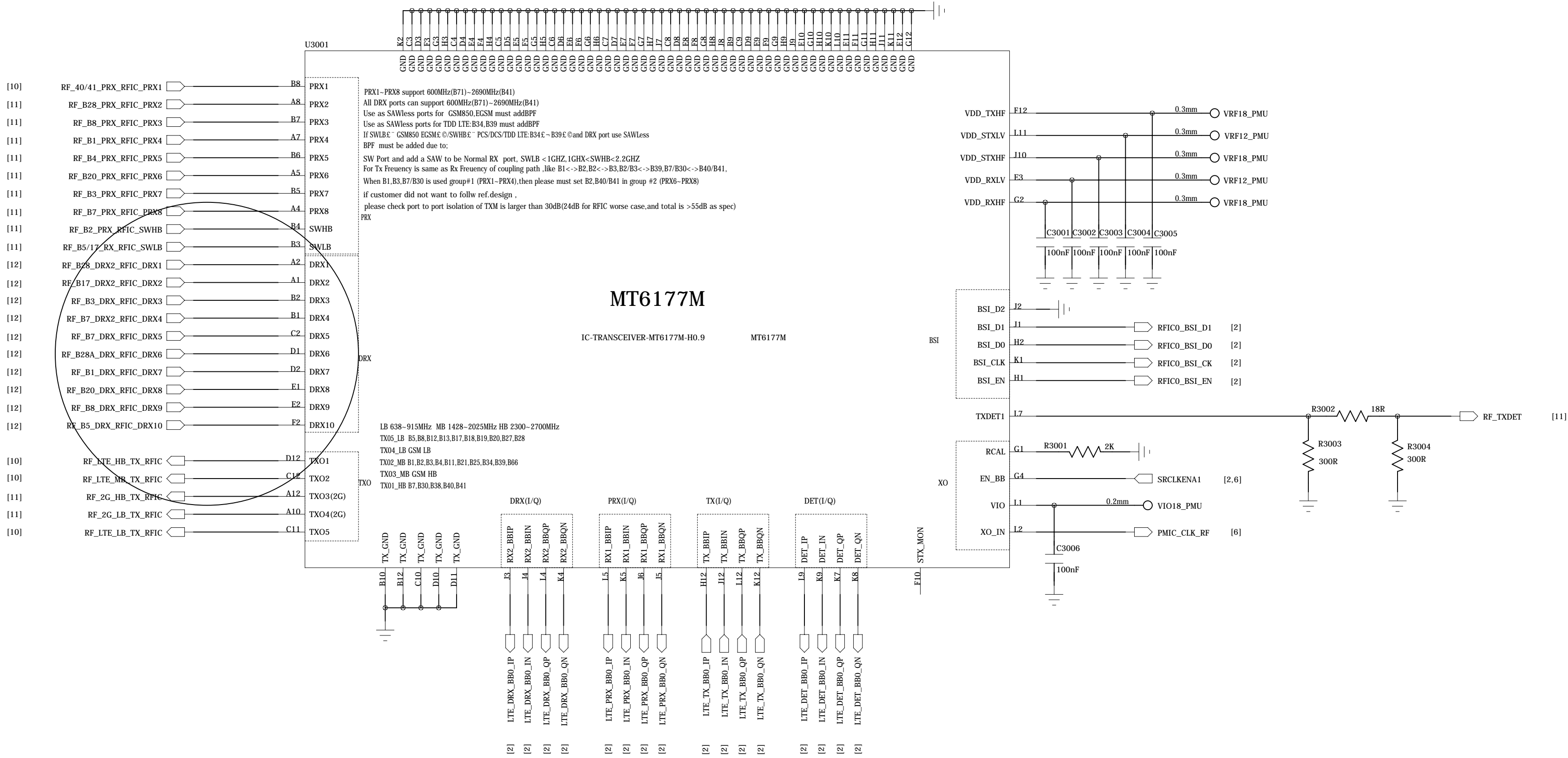
CHARGER_II
18W SUB SW CHG



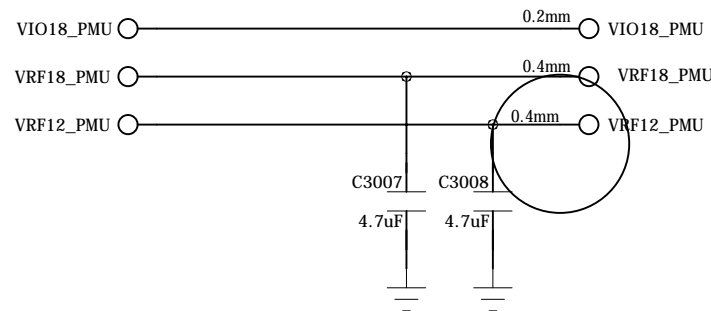
COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
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CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

RF_MT6177M_PIN_OUT

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



Power domain of MT6177M

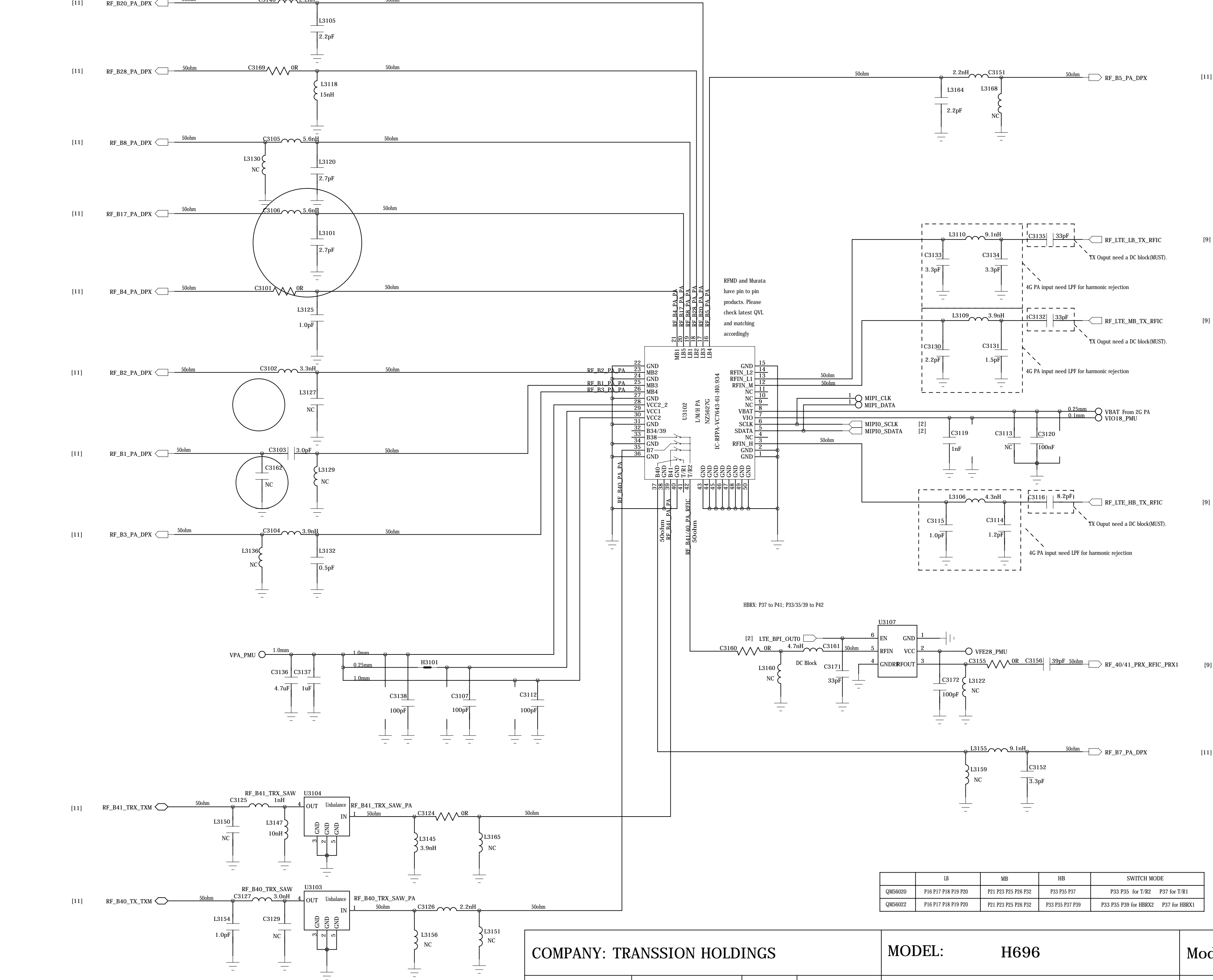


Star Power

COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 30_RF_MT6177M_PIN_OUT		VERSION: V1.0	SHEET: 9 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

RF_MT6177M_RF_TX

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

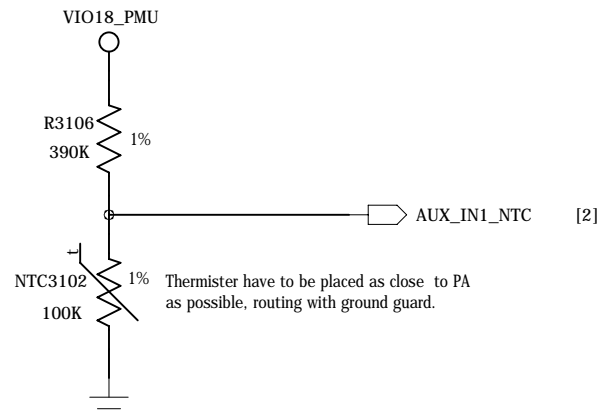


[9] 3/4G_PAIN_LB

[9] 3/4G_PAIN_MB

[9] 3/4G_PAIN_HB

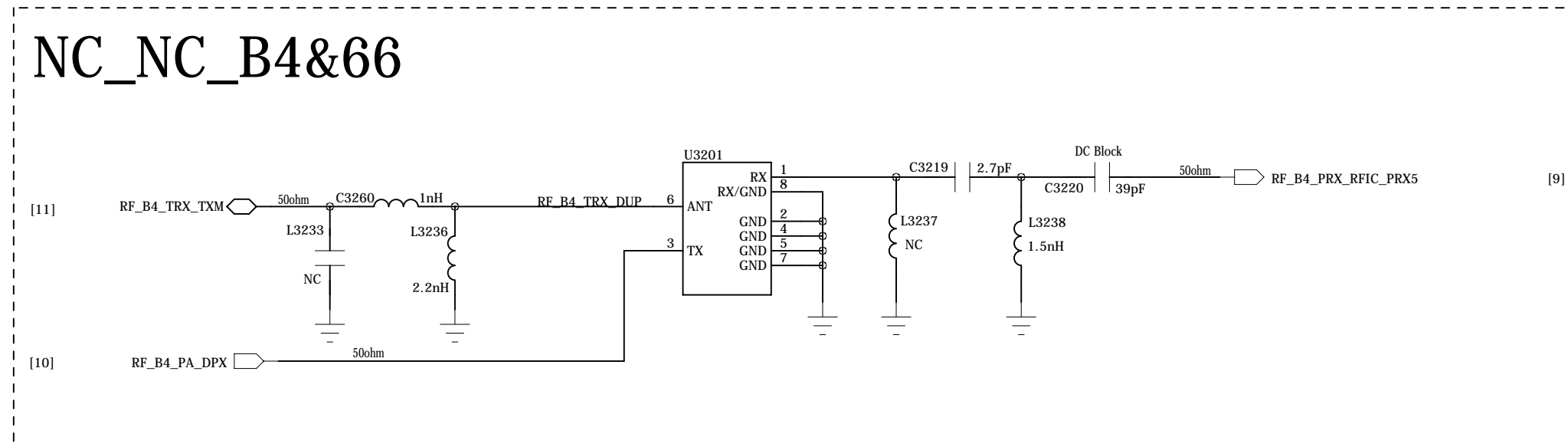
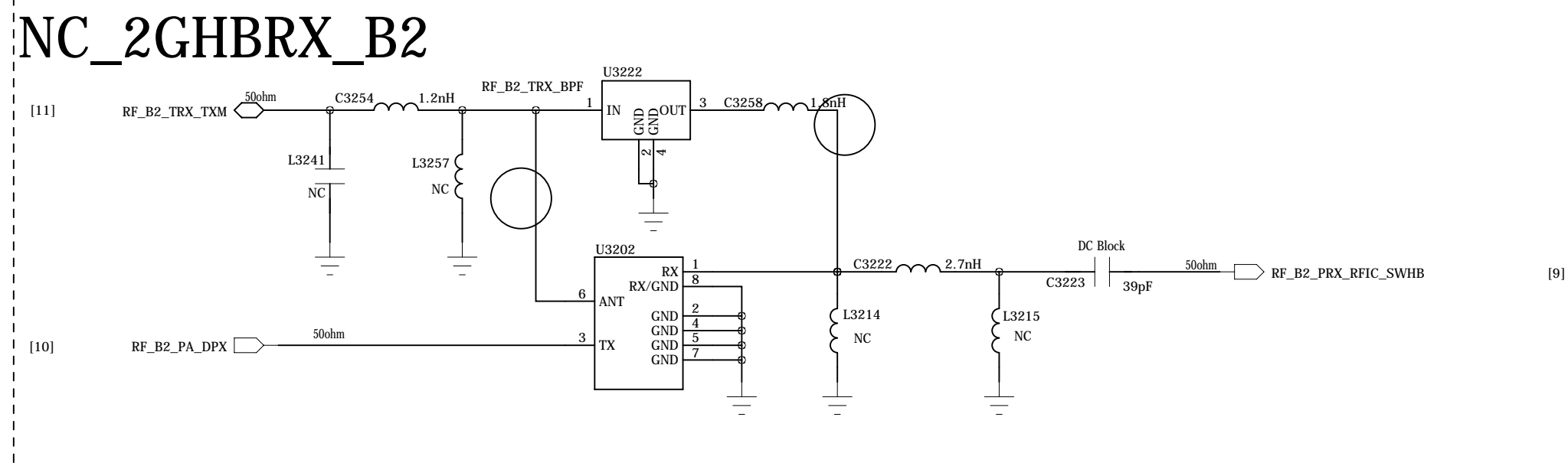
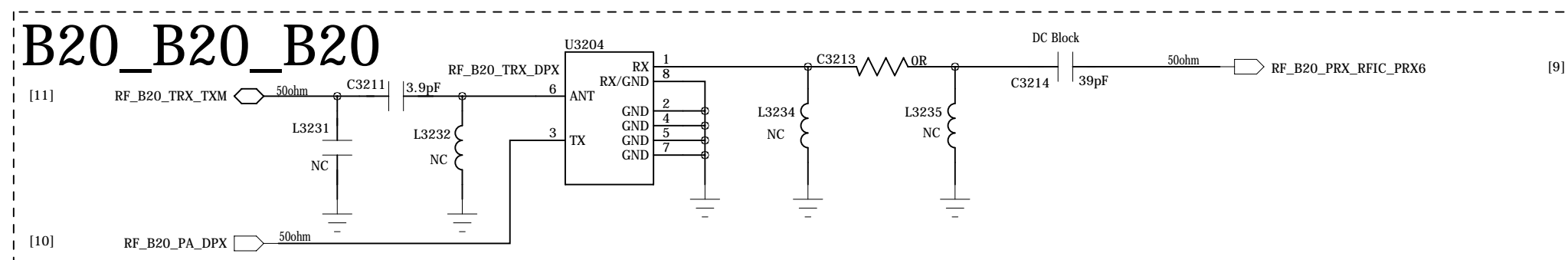
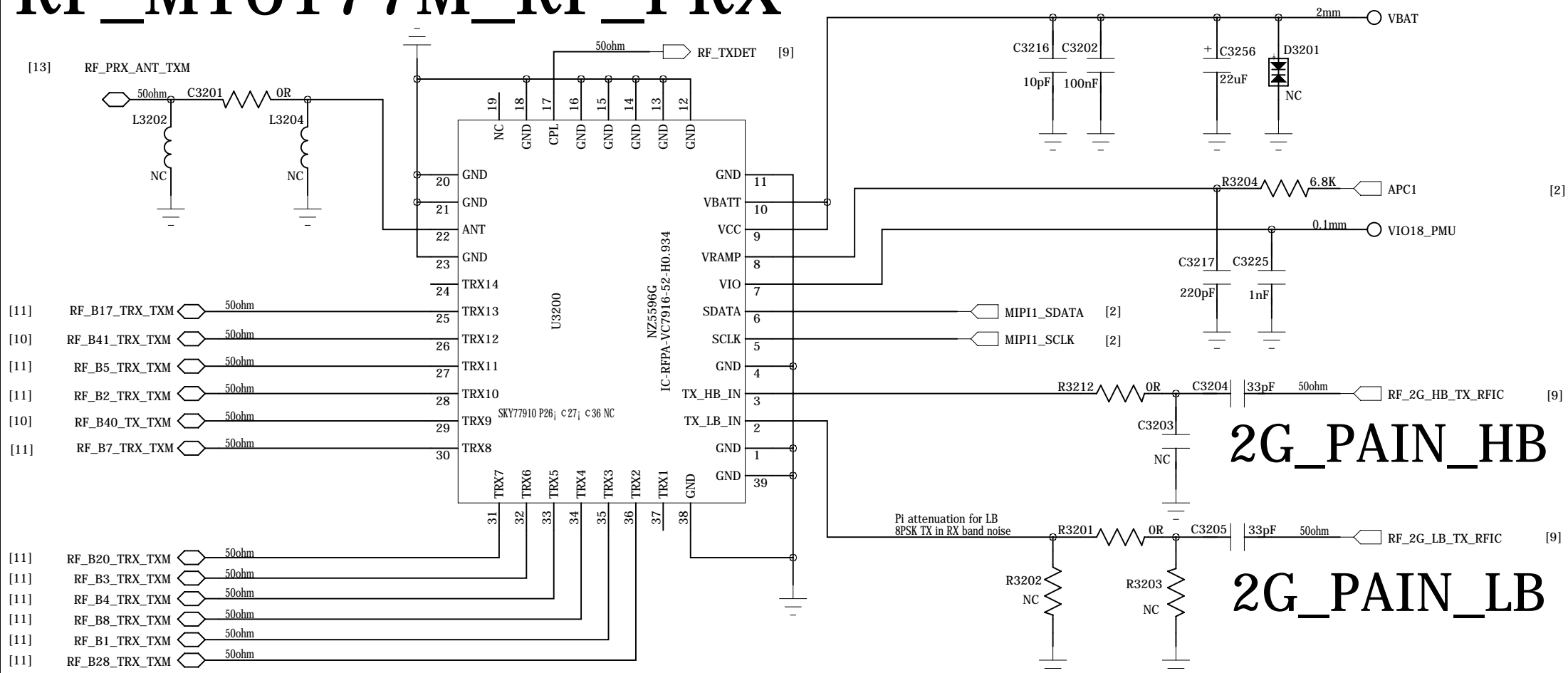
Temp Sensor



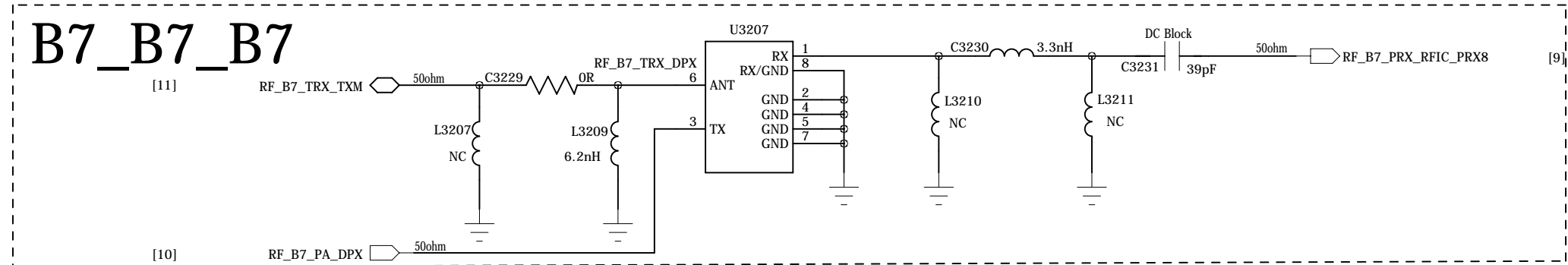
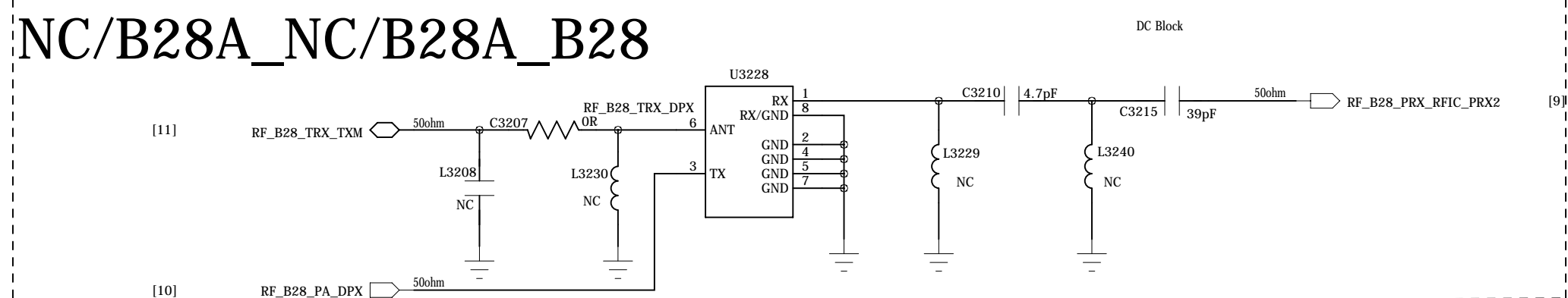
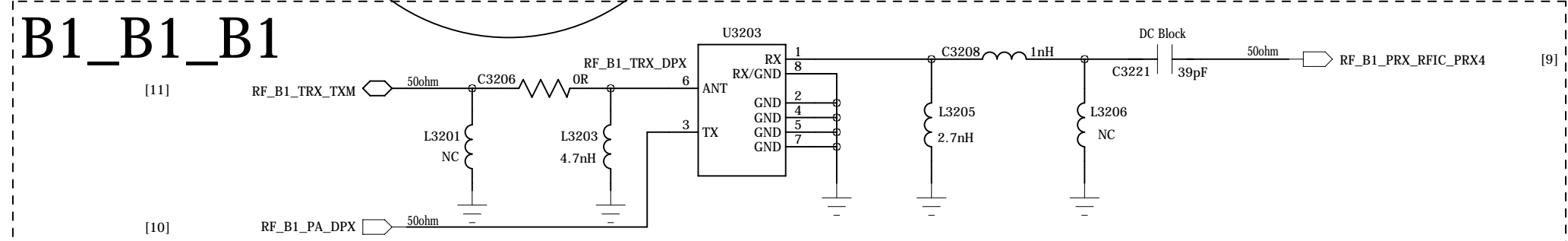
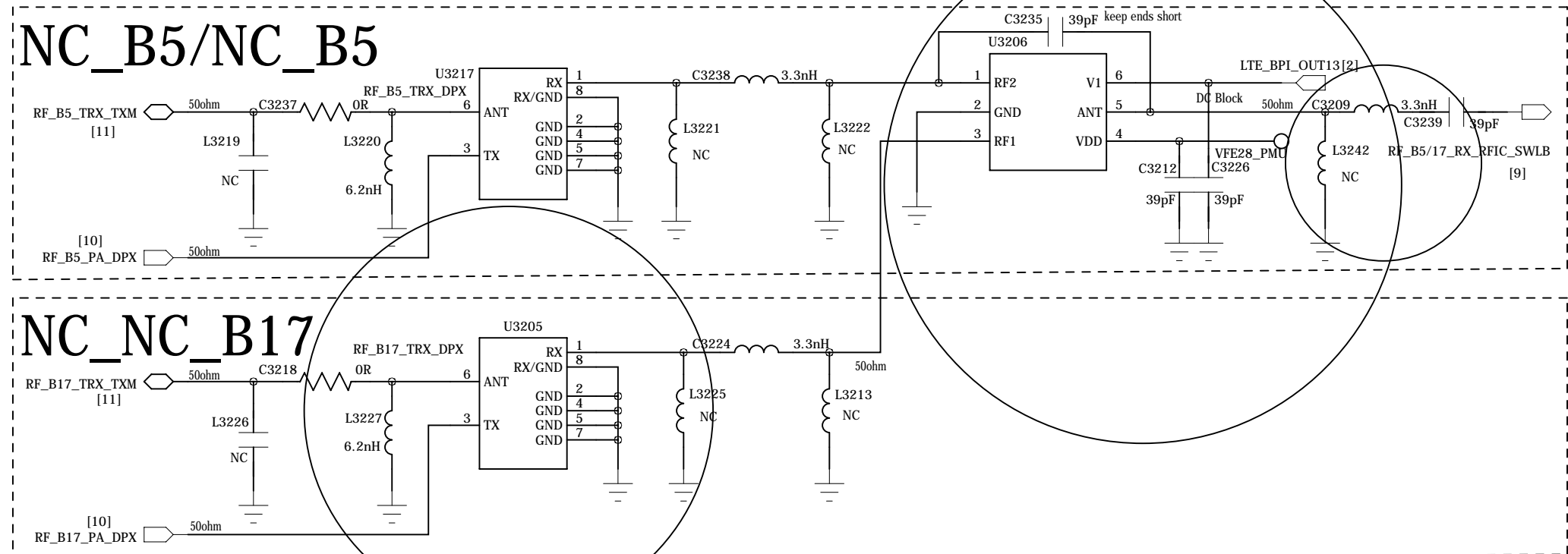
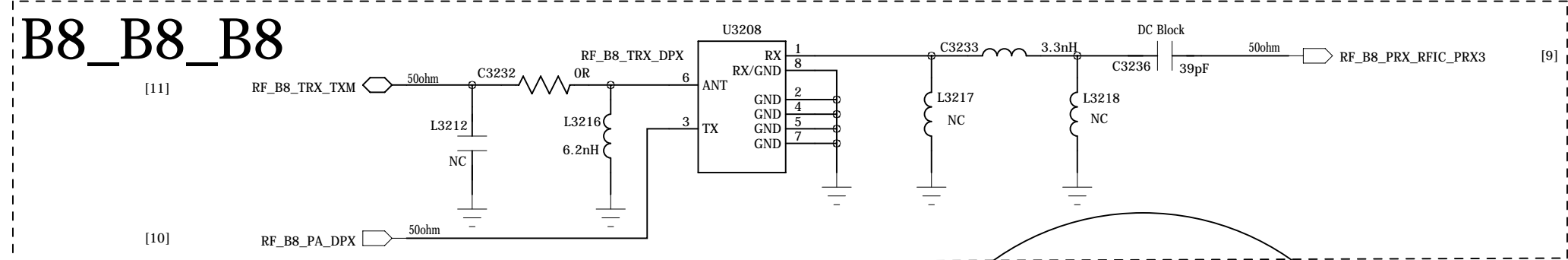
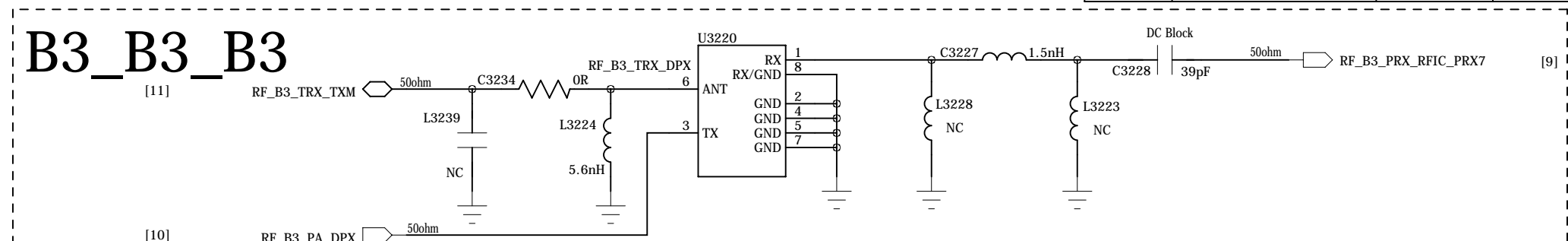
COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 31_RF_MT6177M_RF_TRX		VERSION: V1.0	SHEET: 10 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

RF MT6177M RF PRX

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



Notice:
NC_PCS_B2:Africa Bom NC;India Bom PCS;South America BOM B2;



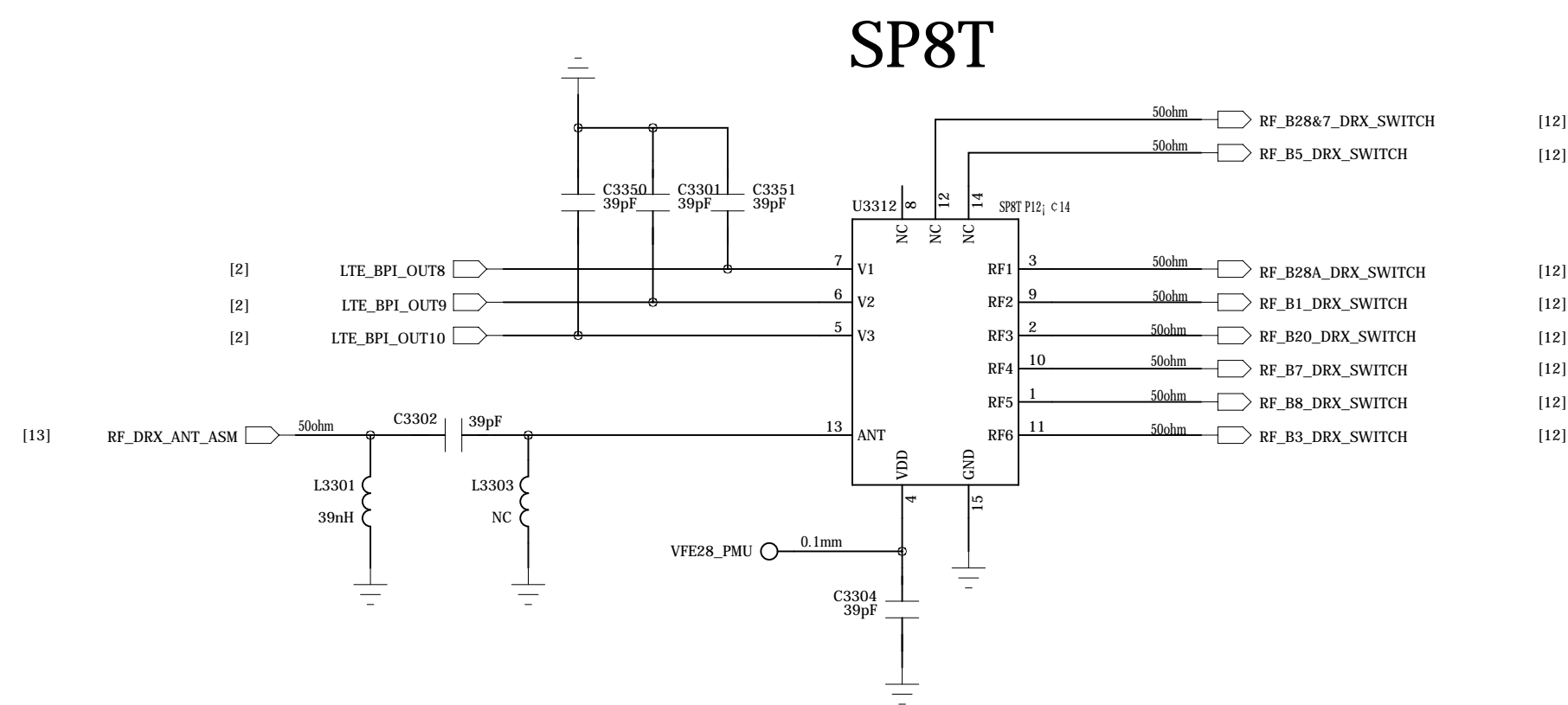
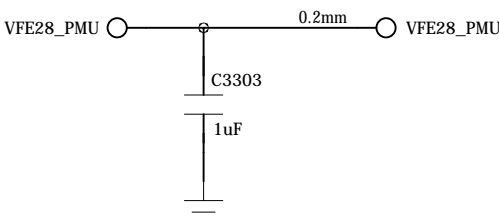
COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 32_RF_MT6177M_RF_PRX		VERSION: V1.0	SHEET: 11 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

RF_MT6177M_RF_DRX

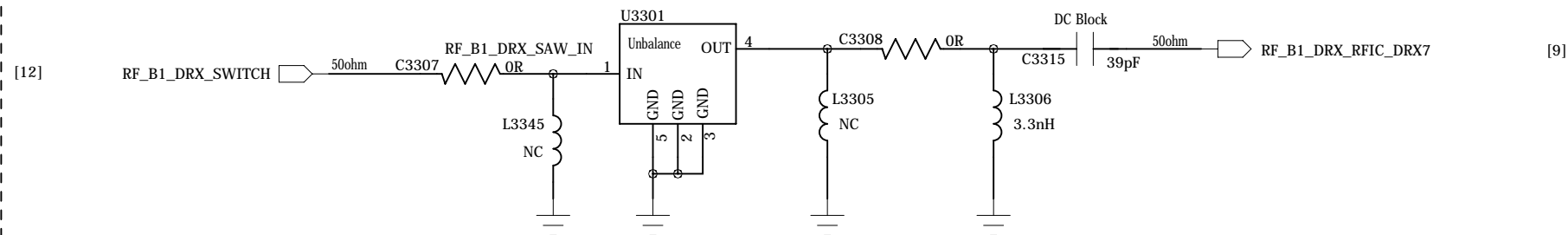
SP8T control logic											
SP6T control logic											
V1	V2	V3	RF1	RF2	RF3	RF4	RF5	RF6	RF7	RF8	
L	L	L	Y	N	N	N	N	N	N	N	
L	L	H	N	Y	N	N	N	N	N	N	
L	H	L	N	N	Y	N	N	N	N	N	
H	L	H	N	N	N	Y	N	N	N	N	
L	H	L	N	N	N	N	Y	N	N	N	
H	L	H	N	N	N	N	N	Y	N	N	
H	H	L	N	N	N	N	N	N	Y	N	
H	H	H	N	N	N	N	N	N	N	Y	

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

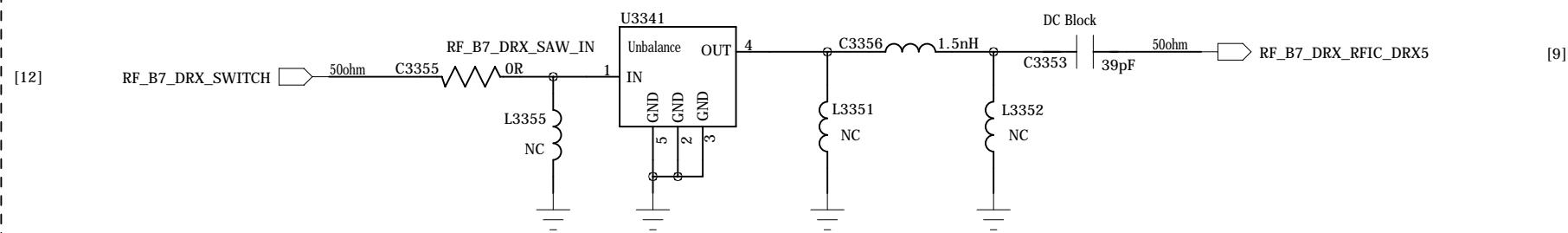
Power domain



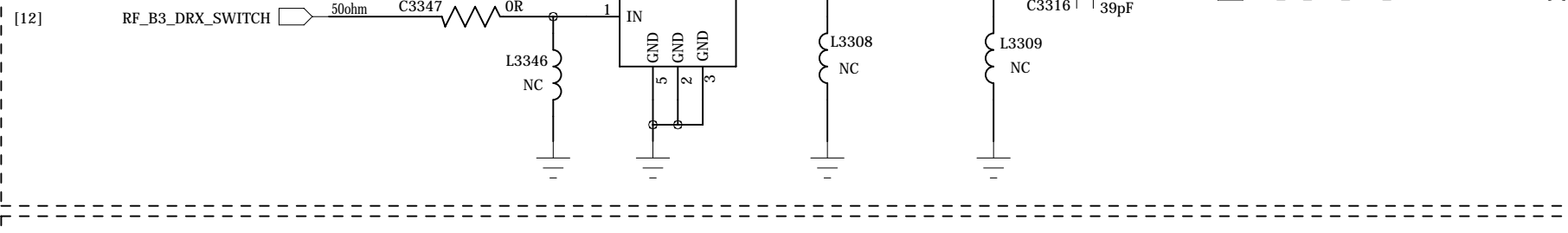
B1/NC_B1_B1&4&66



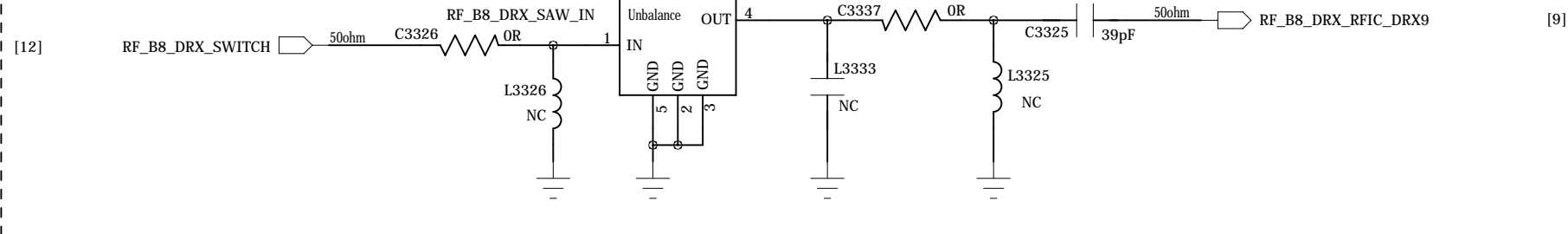
B7_B41_B41



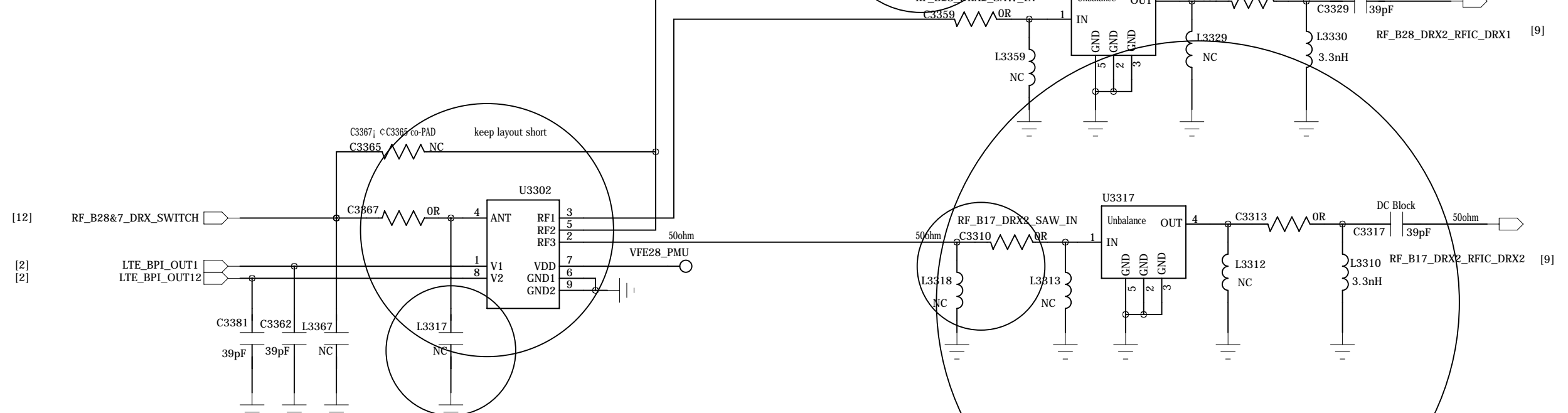
B3_B3_B3



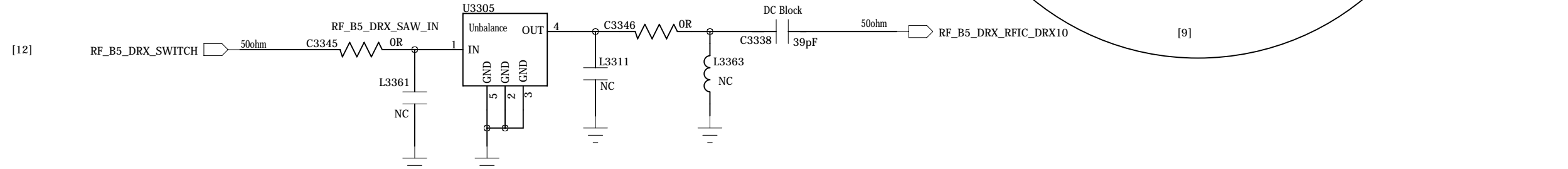
NC_B8_B8



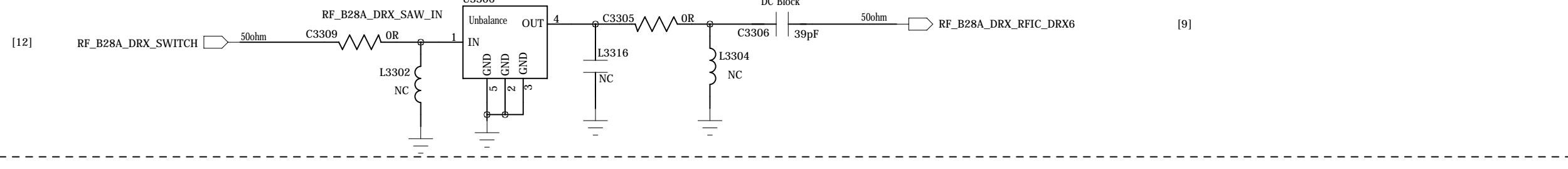
NC_B28A&20/20_B2&20&28



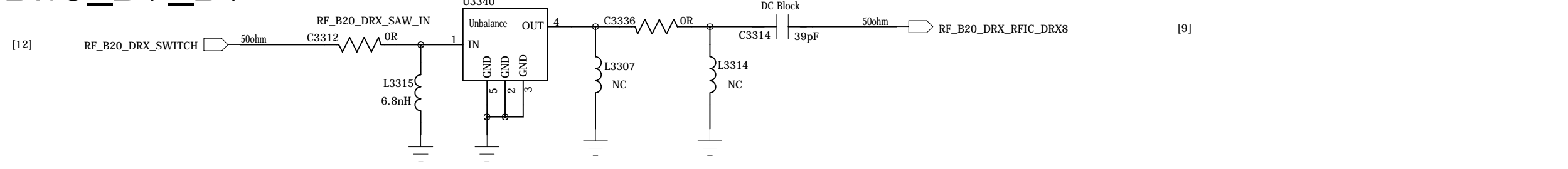
NC_B5_B5



B28A/NC_B40_B40/17



B20 B7 B7

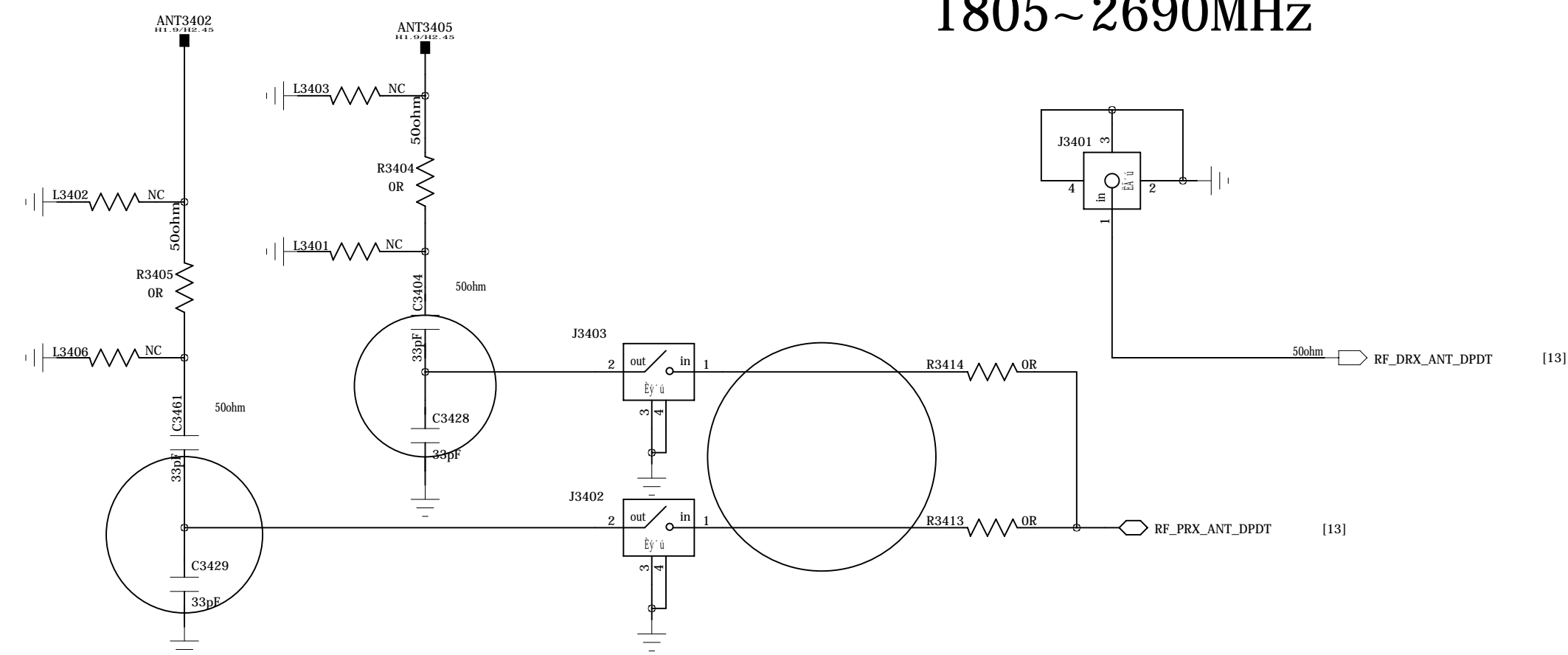


COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 33_RF_MT6177M_RF_DRX		VERSION: V1.0	SHEET: 12 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

RF_MT6177M_RF_ANT

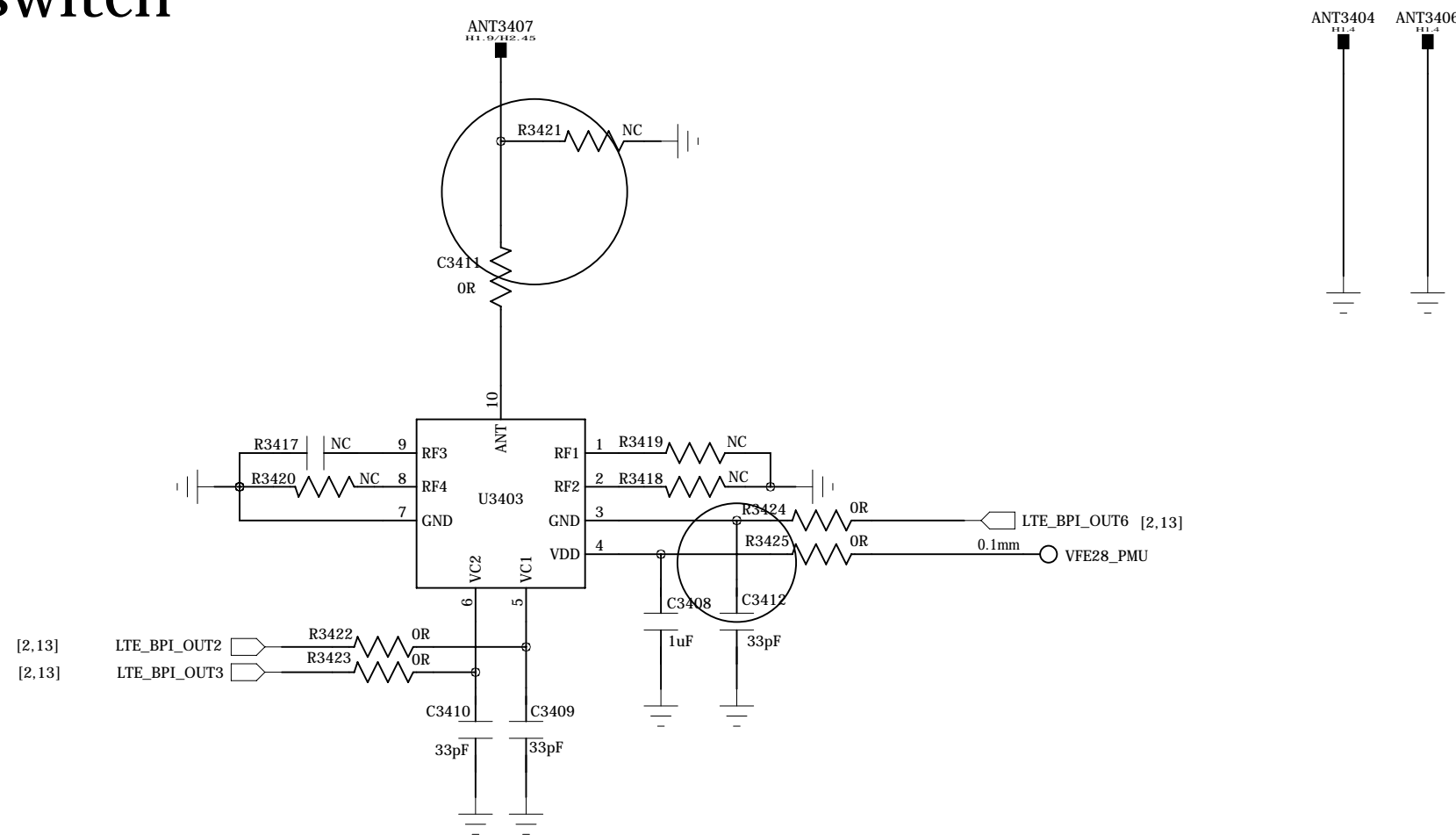
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

ASM_Main
791~960MHz + 1710~2690MHz

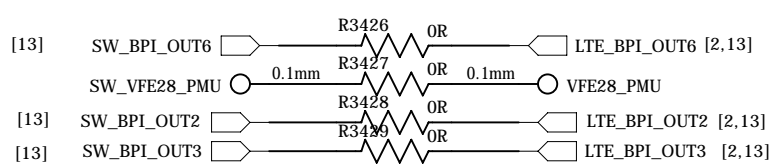


DRX ANT
791~821MHz
1805~2690MHz

ANT switch



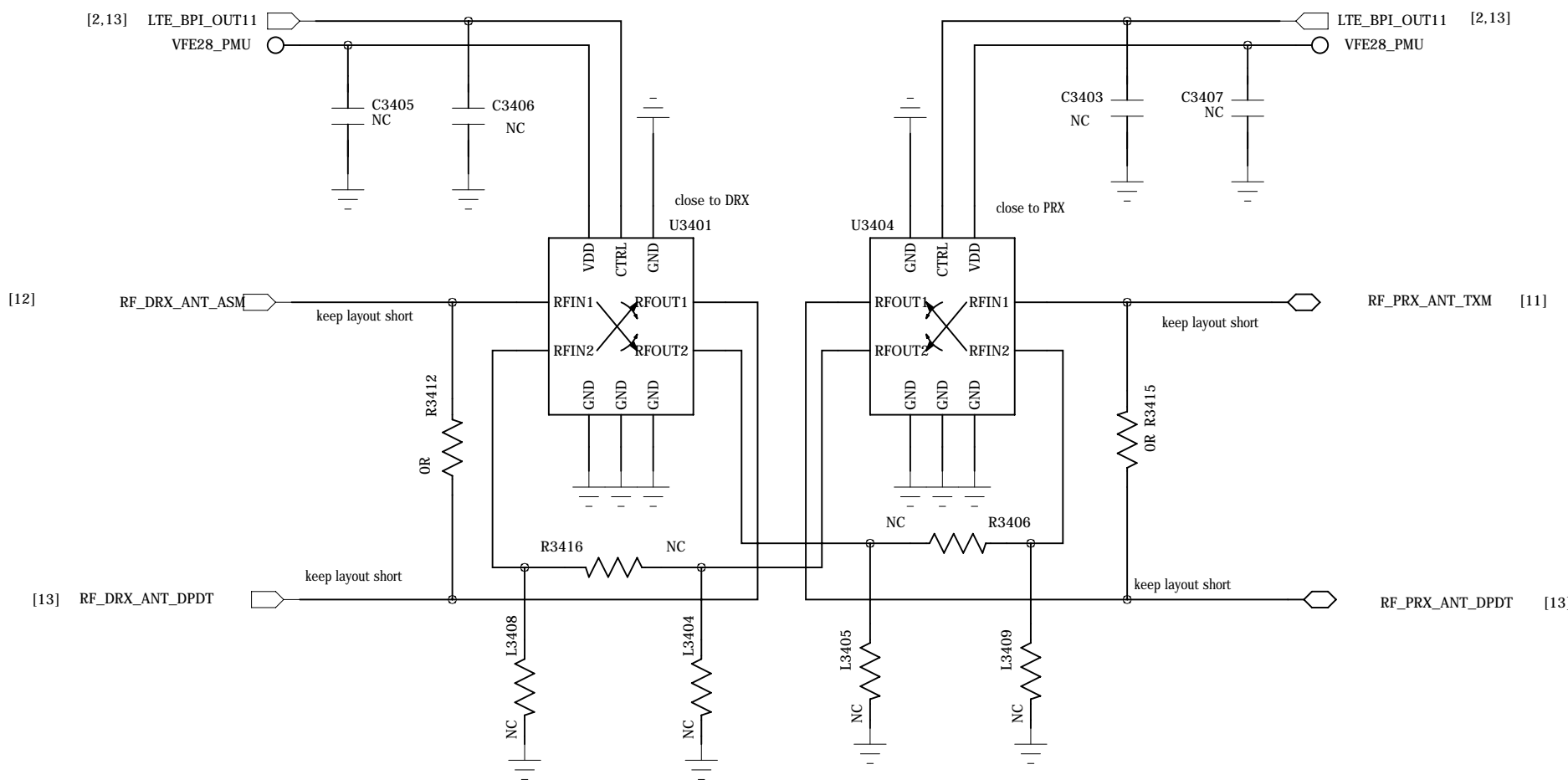
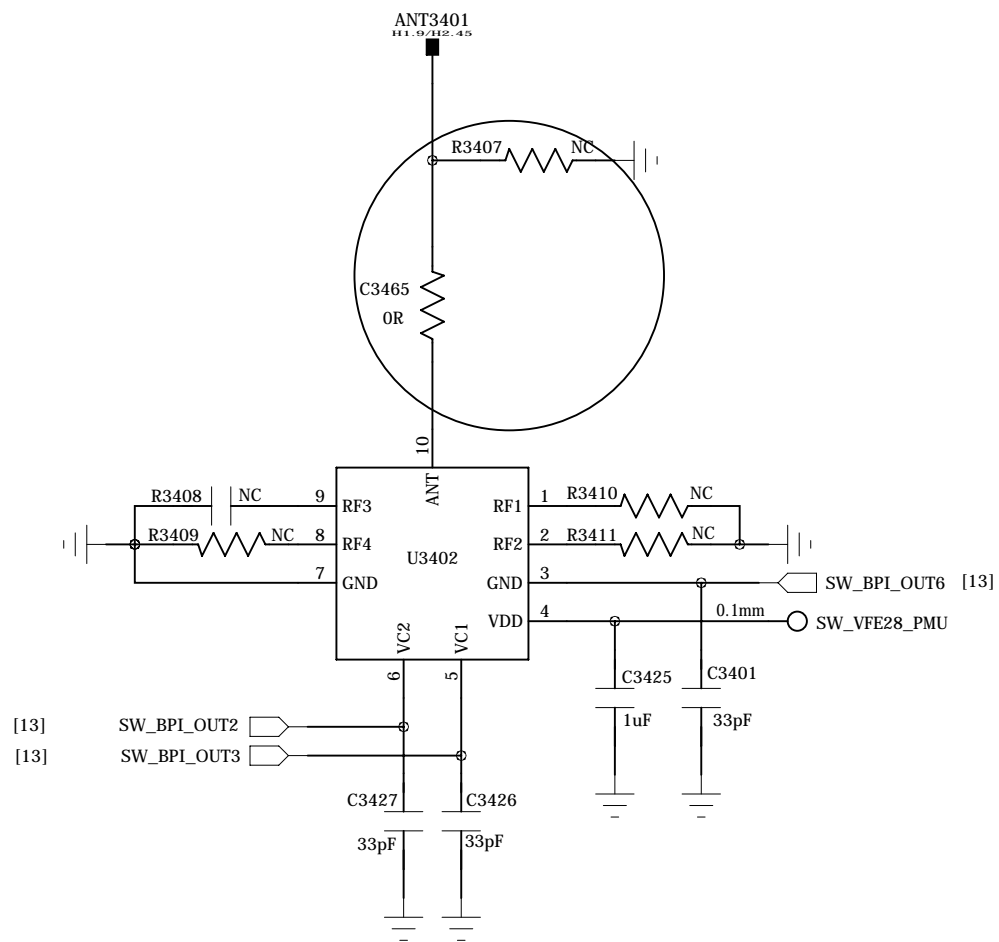
ANT switch



RF119A & RF1694 control logic					
VC1	VC2	RF1	RF2	RF3	RF4
L	L	Y	N	N	N
L	H	N	Y	N	N
H	L	N	N	Y	N
H	H	N	N	N	Y

DPDT

C3405, C3407, C3403, C3406, R3406, R3416, R3417, U3401, U3404			R3412, R3415
Without DPDT			With Material
DPDT			Without Material



COMPANY: TRANSSION HOLDINGS

MODEL: H696

Modified Date: 2020/12/30

DRAWN DJF/TS DATED 2020/08/13

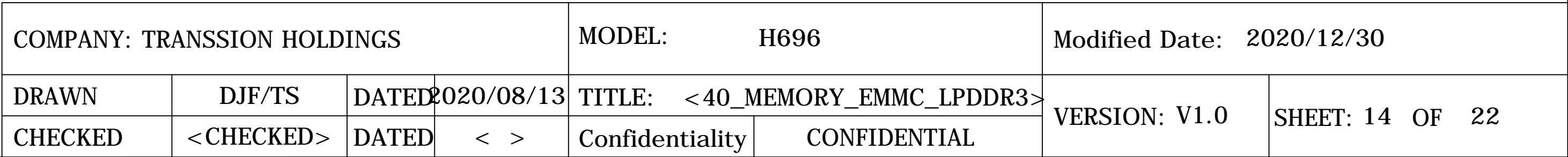
TITLE: 34_RF_ANT_CONTROLLER

VERSION: V1.0 SHEET: 13 OF 22

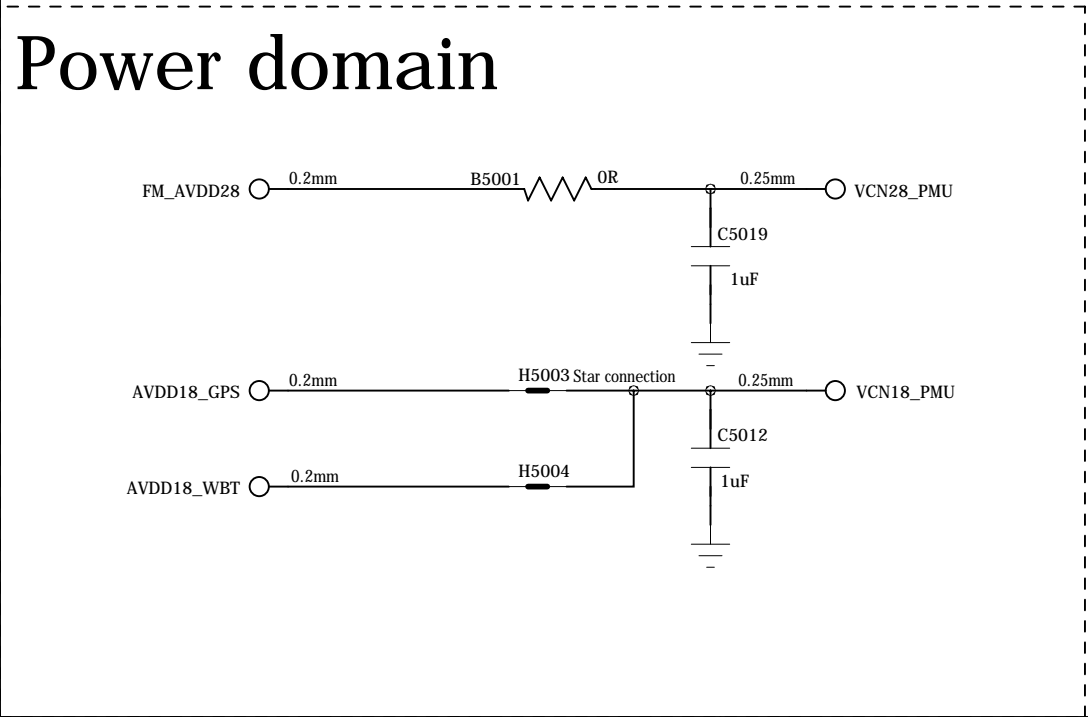
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Confidentiality CONFIDENTIAL

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



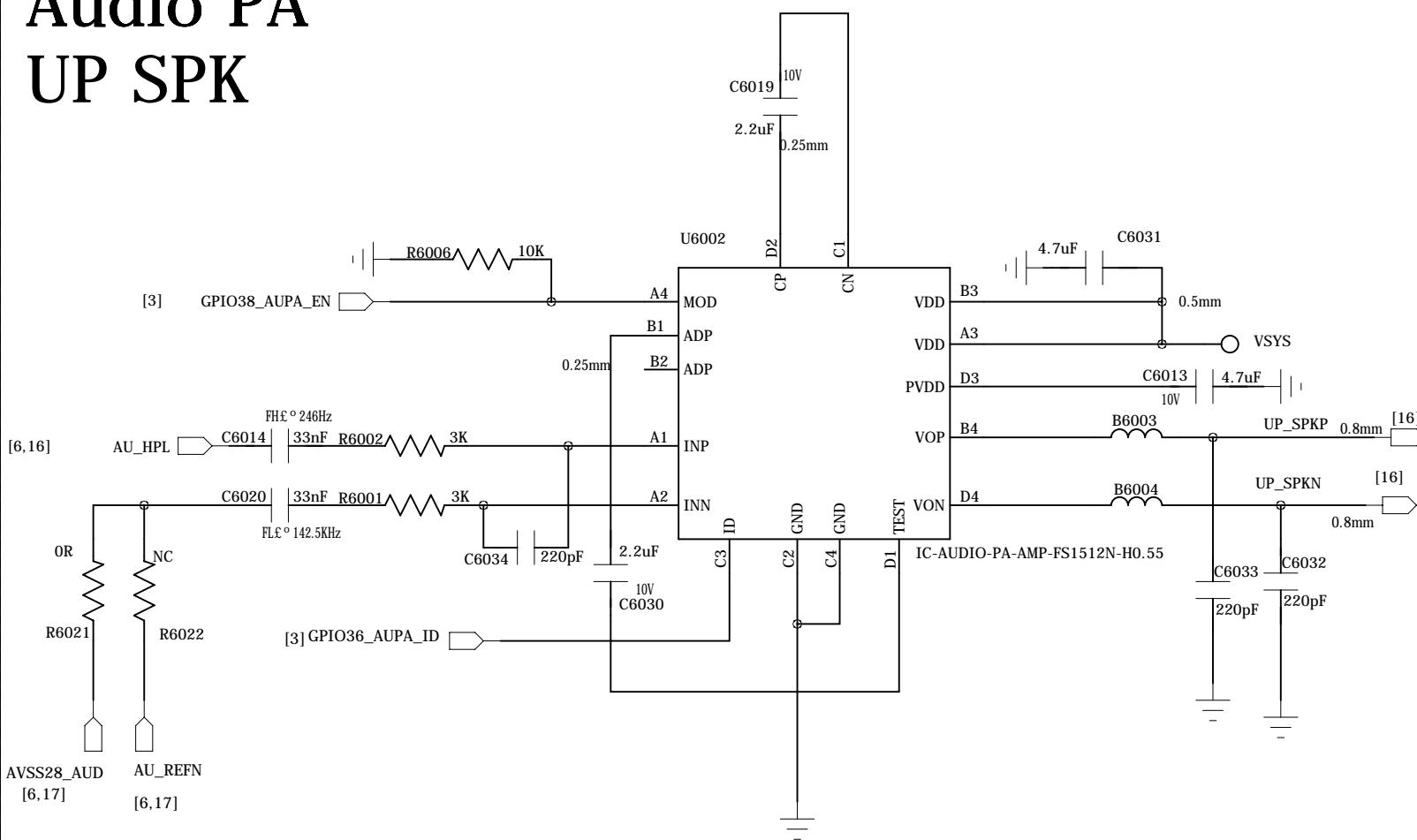
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



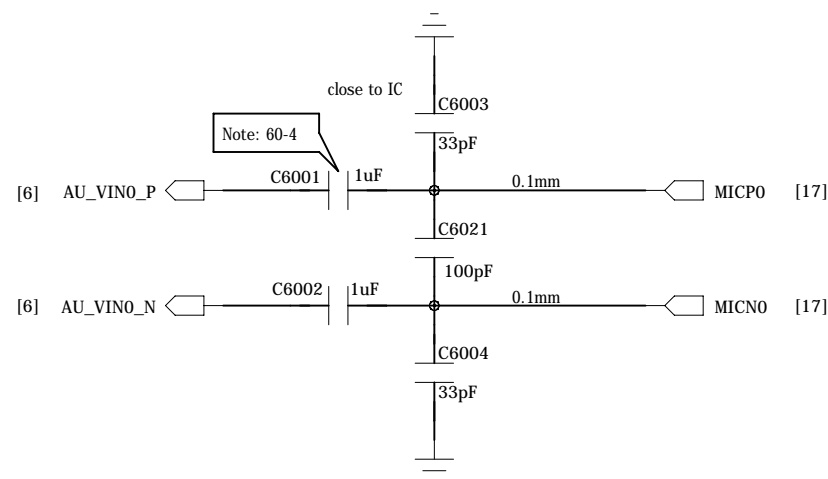
COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 50_CONNECTIVITY_MT6631		VERSION: V1.0	SHEET: 15 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

Audio PA

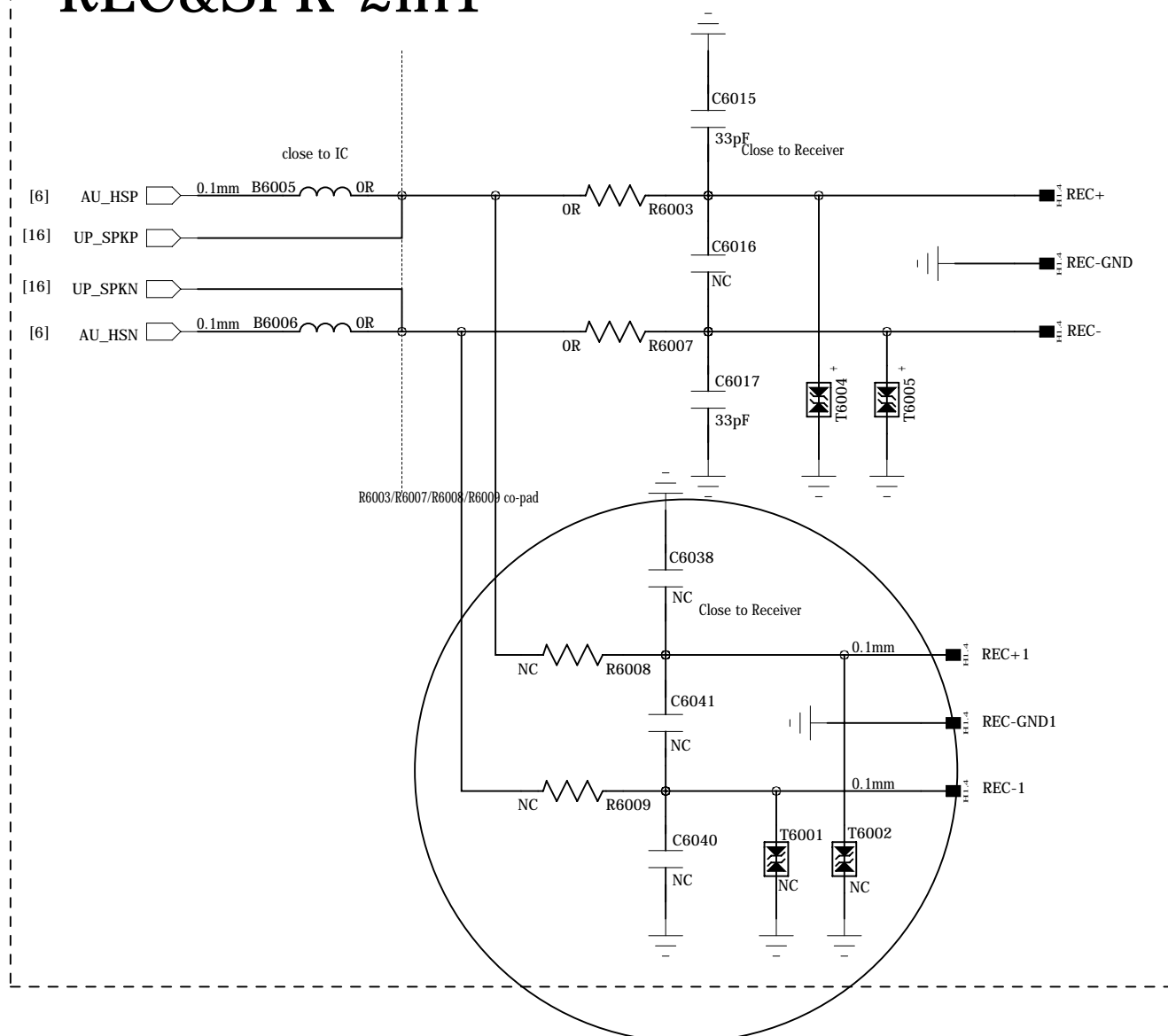
UP SPK



MAIN MIC

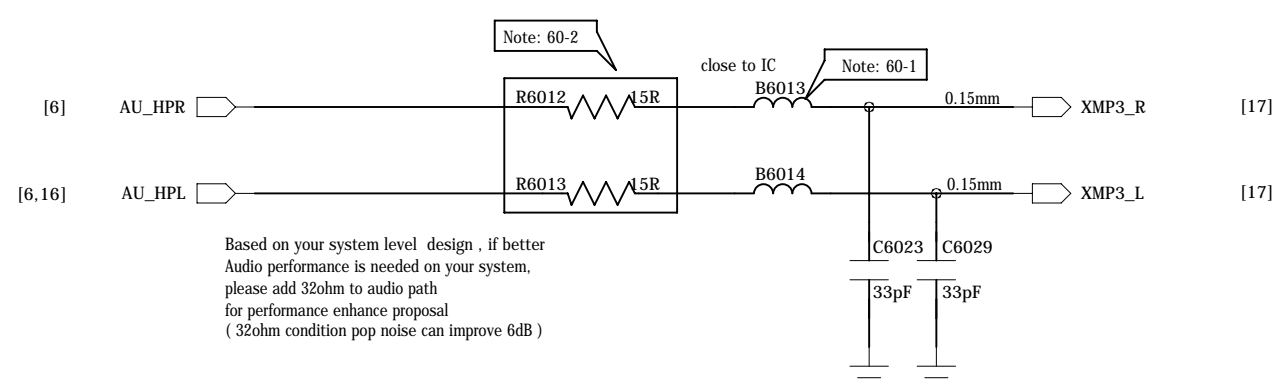


Earphone MICPHONE



Audio PA
DOWN SPK

Earphone Receiver



COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 60_PERI_AUDIO		VERSION: V1.0	SHEET: 16 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

LCD_CTP_INTERFACE

The schematic diagram illustrates the LCD_CTP_INTERFACE, showing the connections between the LCD controller and various peripheral components. The LCD controller is a central component with multiple pins, including P/G, AGND1, AGND2, AGND3, SPK1, SPK2, SPK3, SPK4, SPK5, SPK6, SPK7, SPK8, SPK9, SPK10, SPK11, SPK12, SPK13, SPK14, SPK15, SPK16, SPK17, SPK18, SPK19, SPK20, SPK21, SPK22, SPK23, SPK24, SPK25, SPK26, SPK27, SPK28, SPK29, SPK30, SPK31, SPK32, SPK33, SPK34, SPK35, SPK36, SPK37, SPK38, SPK39, SPK40, SPK41, SPK42, SPK43, SPK44, SPK45, SPK46, SPK47, SPK48, SPK49, SPK50, SPK51, SPK52, SPK53, SPK54, SPK55, SPK56, SPK57, SPK58, SPK59, SPK60, SPK61, SPK62, SPK63, SPK64, SPK65, SPK66, SPK67, SPK68, SPK69, SPK70, SPK71, SPK72, SPK73, SPK74, SPK75, SPK76, SPK77, SPK78, SPK79, SPK80, SPK81, SPK82, SPK83, SPK84, SPK85, SPK86, SPK87, SPK88, SPK89, SPK90, SPK91, SPK92, SPK93, SPK94, SPK95, SPK96, SPK97, SPK98, SPK99, SPK100.

Key components and connections include:

- LEDK_LCM:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- TDP3, TDP0, TCP, TDP1, TDP2:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- LEDK_LCM, VIO28_PMU, LCM_RST, DSI_TE, EINT1_CTP, GPIO0_CTP_RSTB:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- LEDK_LCM, VIO28_PMU, LCM_RST, DSI_TE, EINT1_CTP, GPIO0_CTP_RSTB:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- LEDK_LCM, VIO28_PMU, LCM_RST, DSI_TE, EINT1_CTP, GPIO0_CTP_RSTB:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- LEDK_LCM, VIO28_PMU, LCM_RST, DSI_TE, EINT1_CTP, GPIO0_CTP_RSTB:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- LEDK_LCM, VIO28_PMU, LCM_RST, DSI_TE, EINT1_CTP, GPIO0_CTP_RSTB:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- LEDK_LCM, VIO28_PMU, LCM_RST, DSI_TE, EINT1_CTP, GPIO0_CTP_RSTB:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- LEDK_LCM, VIO28_PMU, LCM_RST, DSI_TE, EINT1_CTP, GPIO0_CTP_RSTB:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- LEDK_LCM, VIO28_PMU, LCM_RST, DSI_TE, EINT1_CTP, GPIO0_CTP_RSTB:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- LEDK_LCM, VIO28_PMU, LCM_RST, DSI_TE, EINT1_CTP, GPIO0_CTP_RSTB:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- LEDK_LCM, VIO28_PMU, LCM_RST, DSI_TE, EINT1_CTP, GPIO0_CTP_RSTB:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- LEDK_LCM, VIO28_PMU, LCM_RST, DSI_TE, EINT1_CTP, GPIO0_CTP_RSTB:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- LEDK_LCM, VIO28_PMU, LCM_RST, DSI_TE, EINT1_CTP, GPIO0_CTP_RSTB:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52.
- LEDK_LCM, VIO28_PMU, LCM_RST, DSI_TE, EINT1_CTP, GPIO0_CTP_RSTB:** Connected to P/G pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16,

	R6105	R6106	R6111	R6124	R6115	R6116	R6123	R6104	R6109	R6103	R6117	R6119	F6101	R6112
W/I Flash 60Hz LCM	ØR	ØR	ØR	ØR	ØR	ØR	NC	NC	NC	NC	NC	NC	NC	NC
W/O Flash 60Hz New LCM	ØR	ØR	ØR	NC	NC	NC	ØR	ØR	NC	ØR	ØR	ØR	NC	NC
W/O Flash 90Hz New LCM	NC	NC	NC	NC	NC	NC	ØR	ØR	NC	ØR	ØR	ØR	EMI	NC
W/O Flash 60Hz Old LCM	ØR	ØR	ØR	ØR	NC	NC	ØR	NC	NC	NC	ØR	ØR	NC	ØR

Diagram illustrating the J6102 module connections:

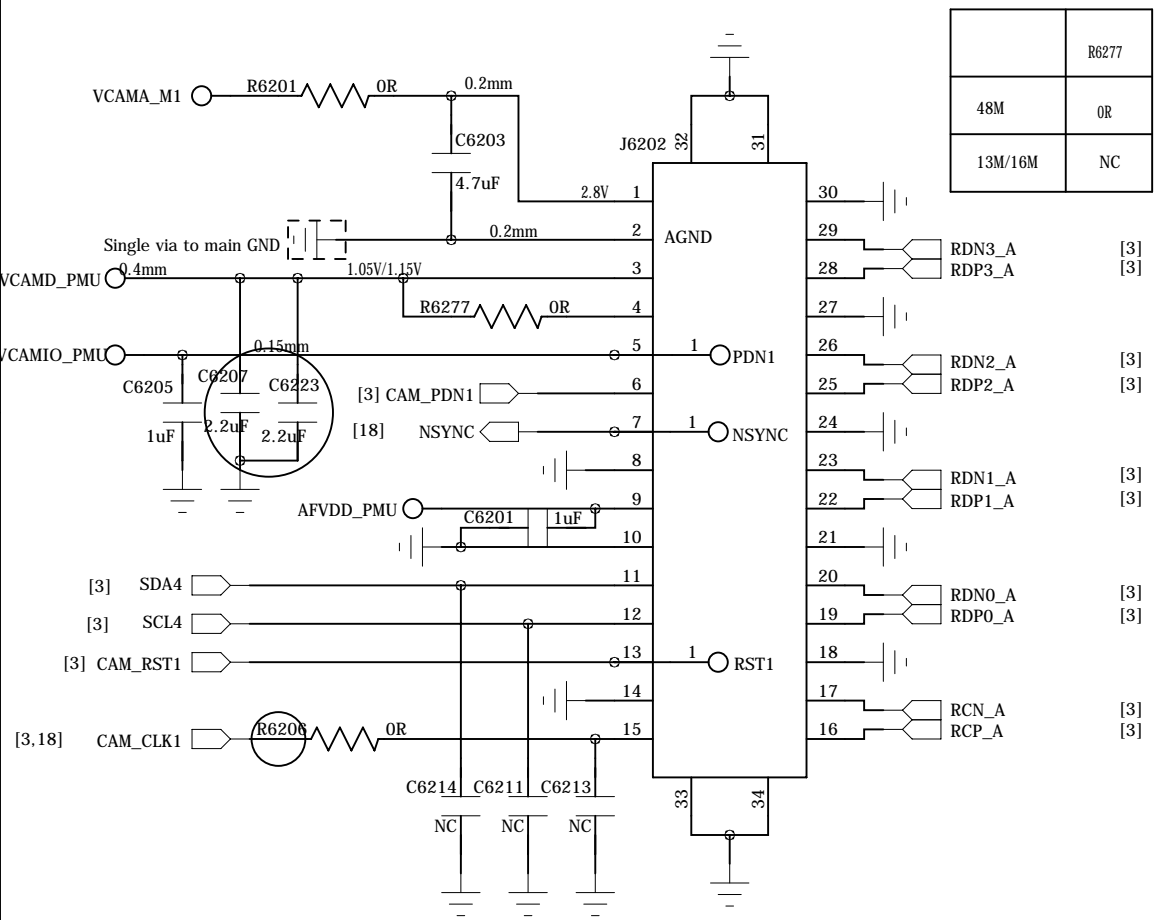
- GPIO6_FP_RST** (Signal) is connected to pin 2 (VDD).
- VIO28_PMU** (Signal) is connected to pin 3 (INT1).
- 0.2mm** (Pin Header) is connected to pin 4 (MI).
- 1uF** (Capacitor) is connected to pin 5 (CK).
- 1uF** (Capacitor) is connected to pin 6 (MO).
- 1uF** (Capacitor) is connected to pin 7 (RST).
- 1uF** (Capacitor) is connected to pin 8 (CK).
- 1uF** (Capacitor) is connected to pin 9 (MI).
- 1uF** (Capacitor) is connected to pin 10 (INT1).
- 1uF** (Capacitor) is connected to pin 11 (VDD).
- 1uF** (Capacitor) is connected to pin 12 (CS).
- 1uF** (Capacitor) is connected to pin 13 (NC).
- 1uF** (Capacitor) is connected to pin 14 (NC).

[illegible]

COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 61_PERI_LCM_CTP_FP		VERSION: V1.0	SHEET: 17 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

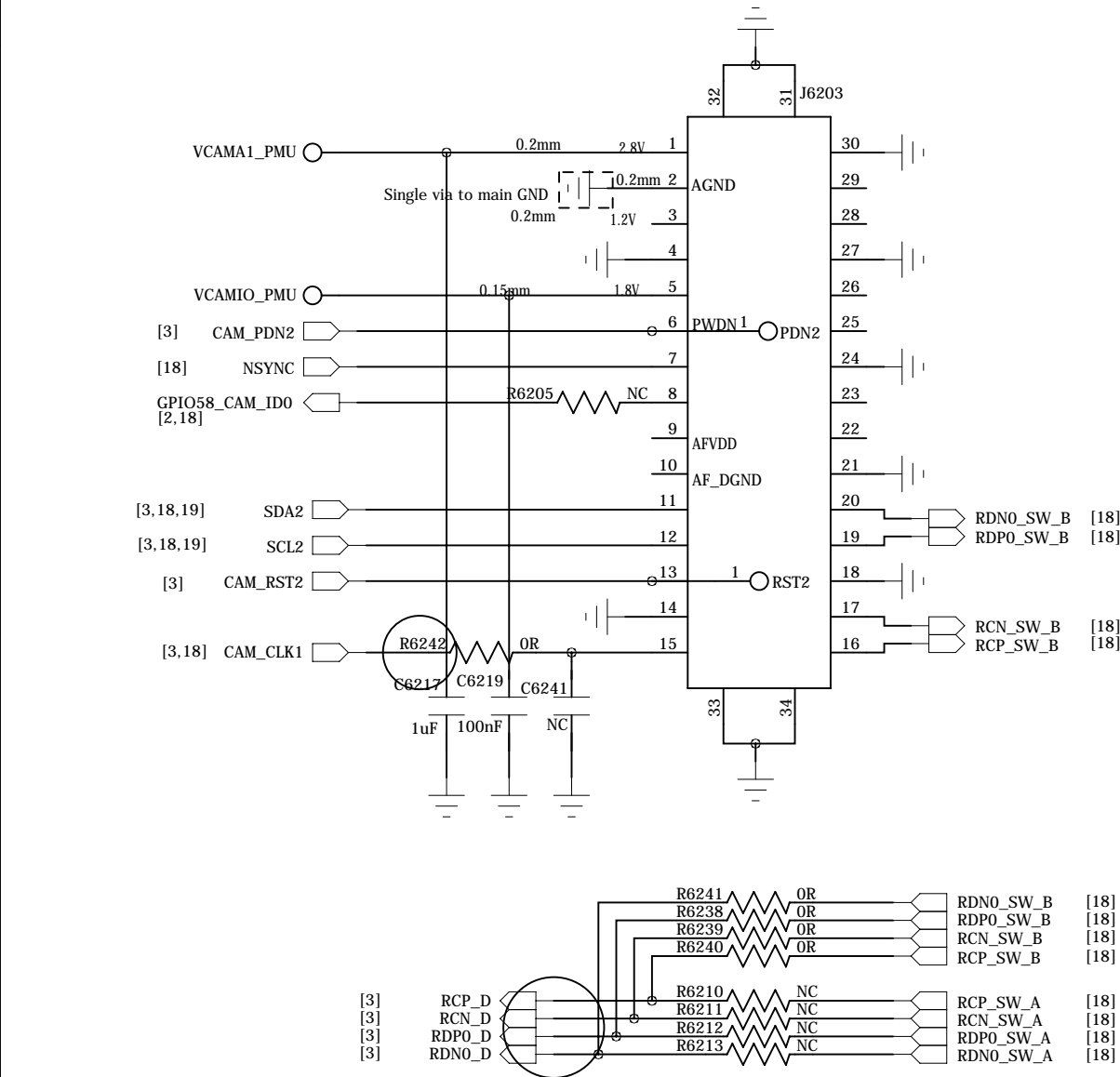
PERI_CAMERA_I

REAR CAMERA_I (Max 48M+AF)



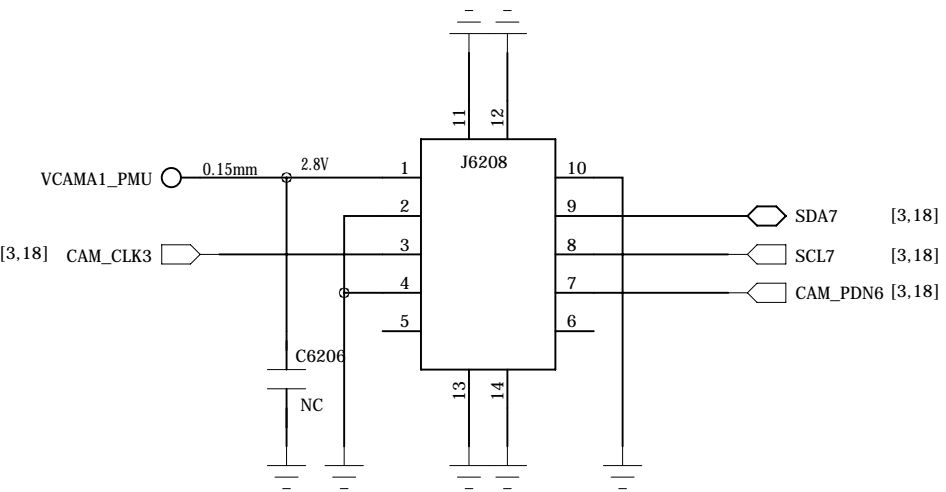
REAR CAMERA_II(2M) Depth/Macro

SENSOR	VCAMA	VCAMD	VCAMIO	I2C Address
OV02B1B	2.8V 40mA	NC	1.8V 70mA	Write:0x7A Read:0x7B
GC02M1B-C24Y0	2.8V 40mA	NC	1.8V 70mA	Write:0x20 Read:0x21
OV02B10-A25A-001A	2.8V 35mA	NC	1.8V 45mA	Write:0x78 Read:0x79
GC02M1-C24YA	2.8V 40mA	NC	1.8V 70mA	Write:0x20 Read:0x21



REAR CAMERA_VI

VGA



REAR CAMERA_IV

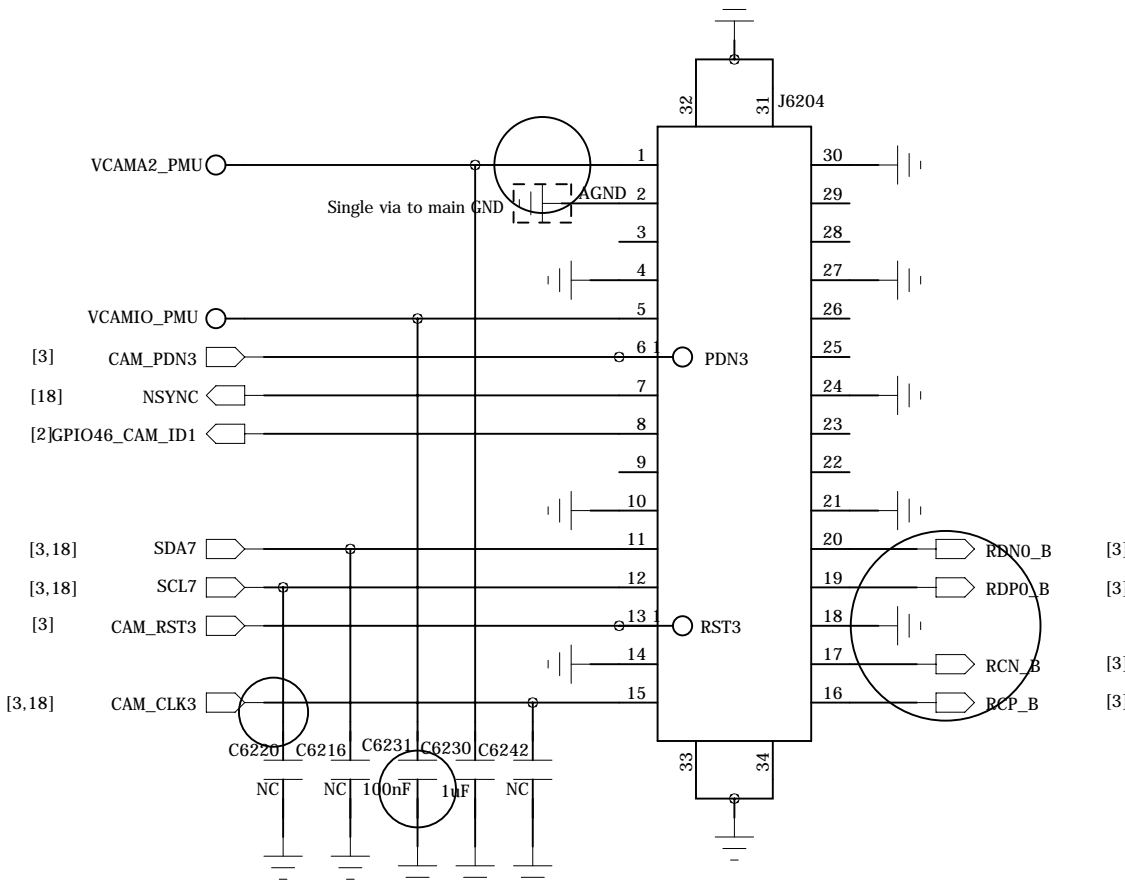
Macro

SENSOR	VCAMA	VCAMD	VCAMIO	I2C Address
OV02B10-A25A-001A	2.8V 35mA	NC	1.8V 45mA	Write:0x78 Read:0x79
GC02M1-C24YA	2.8V 40mA	NC	1.8V 70mA	Write:0x20 Read:0x21

REAR CAMERA_III

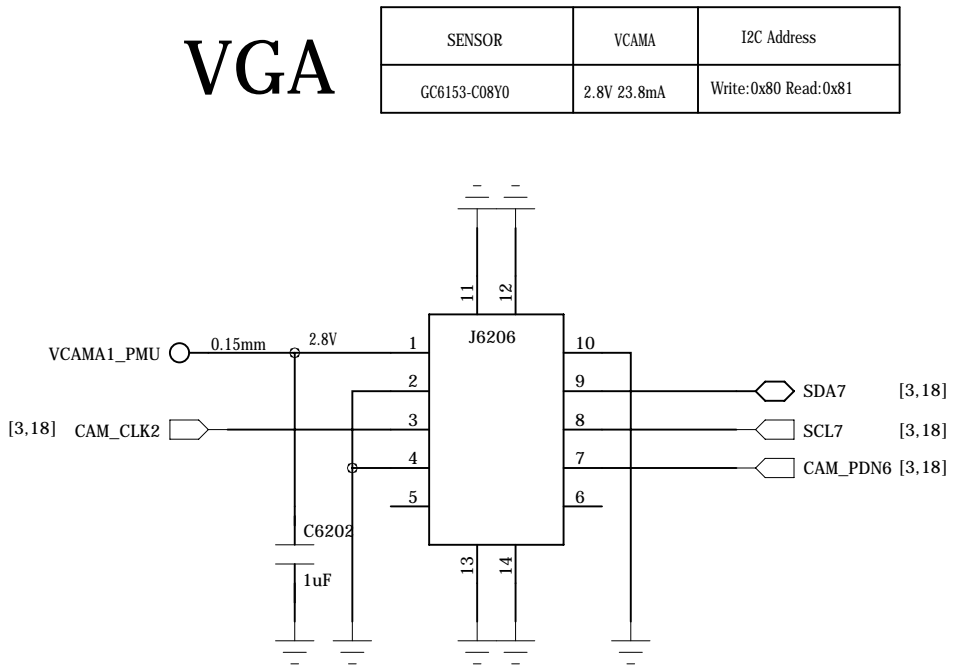
Depth/Macro

SENSOR	VCAMA	VCAMD	VCAMIO	I2C Address
OV02B1B	2.8V 40mA	NC	1.8V 70mA	Write:0x7A Read:0x7B
GC02M1B-C24Y0	2.8V 40mA	NC	1.8V 70mA	Write:0x20 Read:0x21
OV02B10-A25A-001A	2.8V 35mA	NC	1.8V 45mA	Write:0x78 Read:0x79
GC02M1-C24YA	2.8V 40mA	NC	1.8V 70mA	Write:0x20 Read:0x21



REAR CAMERA_V

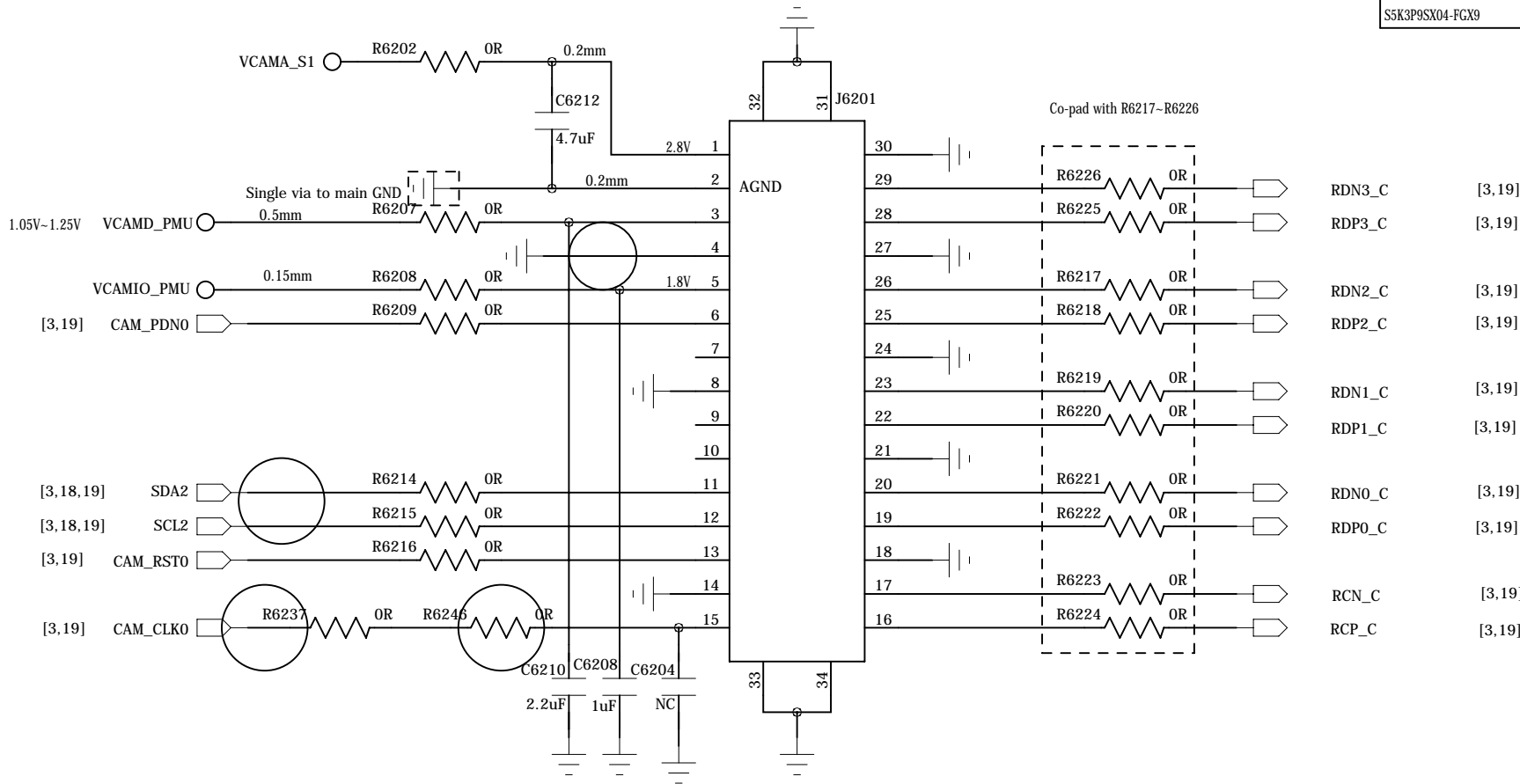
VGA



PERI_CAMERA_II

FRONT CAMERA_I (8M~16M)

For Drop

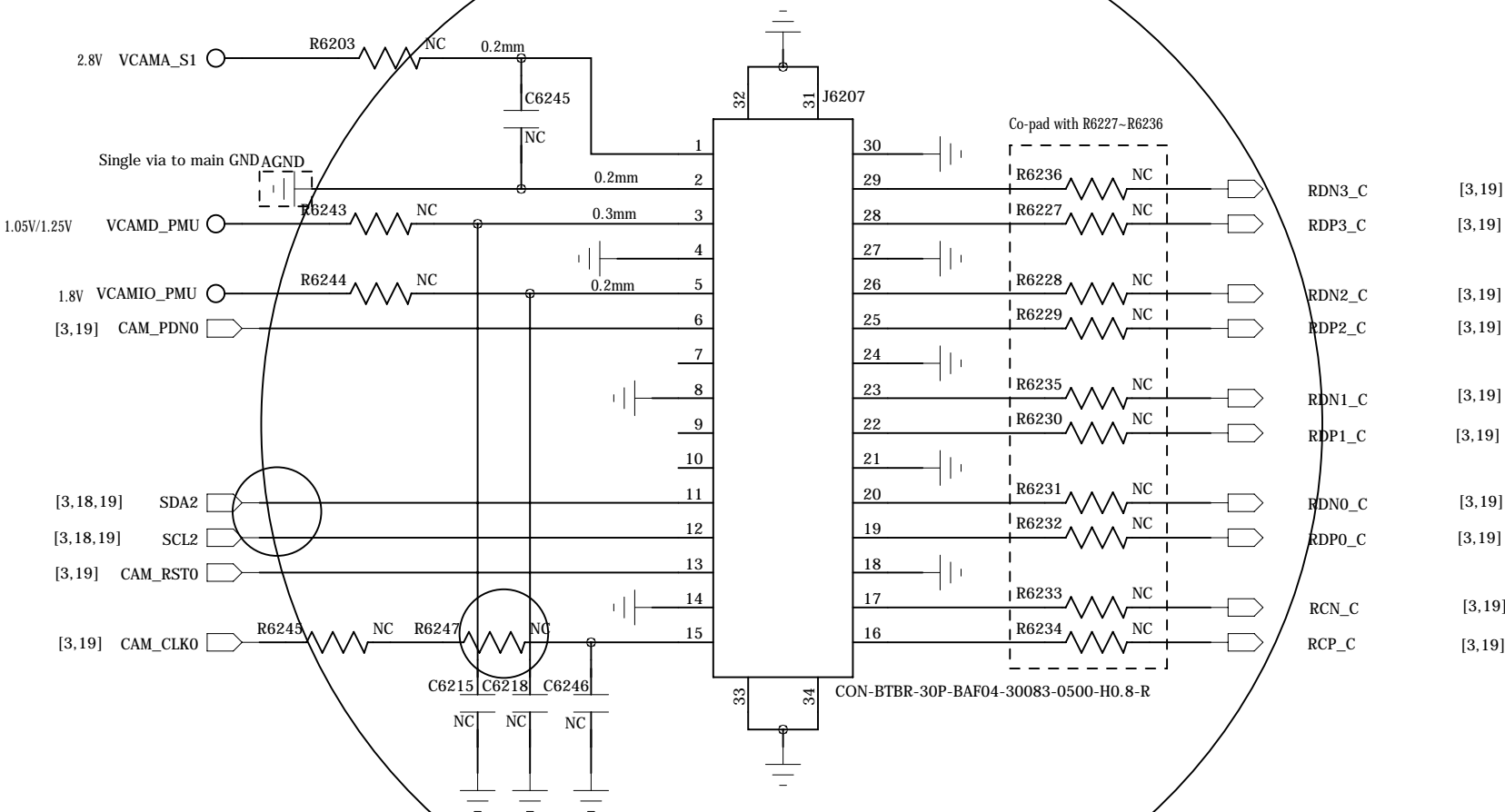


	SENSOR	I2C Address
5M	GC5035-MCHD0	Write:0x6E Read:0x6F
8M	OV08856-GA4A-2A	Write:0x6C Read:0x6D
16M	GC8034-WC1X0	Write:0x6E Read:0x6F
	SSK3P9SX04-FGX9	Write:0x5A Read:0x5B
	P24C64E-C4H-MIR	Write:0xA2 Read:0xA3
	P24S64E-C4H-MIR	Write:0xA2 Read:0xA3

SENSOR	VCAMA	VCAMD	VCAMIO
GC5035-MCHD0	2.8V 35mA	1.2V 80mA	1.8V 3mA
OV08856-GA4A-2A	2.8V 28mA	1.2V 95mA	1.8V 2.7mA
GC8034-WC1X0	2.8V 35mA	1.25V 140mA	1.8V 10mA
SSK3P9SX04-FGX9	2.8V 57.5mA	1.05V 159.9mA	1.8V 0.5mA

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

For Blind Hole



COMPANY: TRANSSION HOLDINGS

MODEL: H696

Modified Date: 2020/12/30

DRAWN

DJF/TS

DATED2020/08/13

TITLE: 62_PERI_CAMERA_II

VERSION: V1.0

SHEET: 19 OF 22

CHECKED

<CHECKED>

DATED

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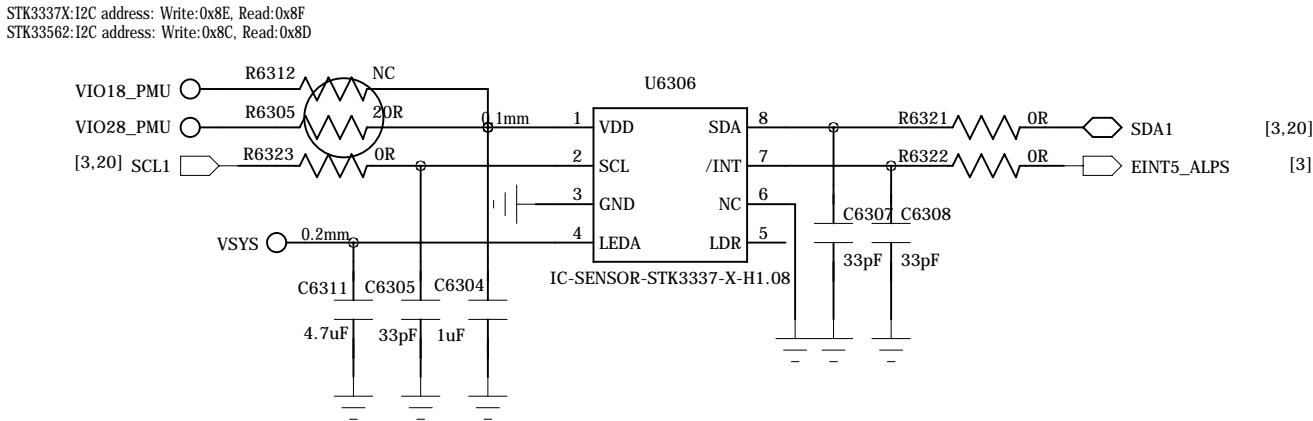
Confidentiality

CONFIDENTIAL

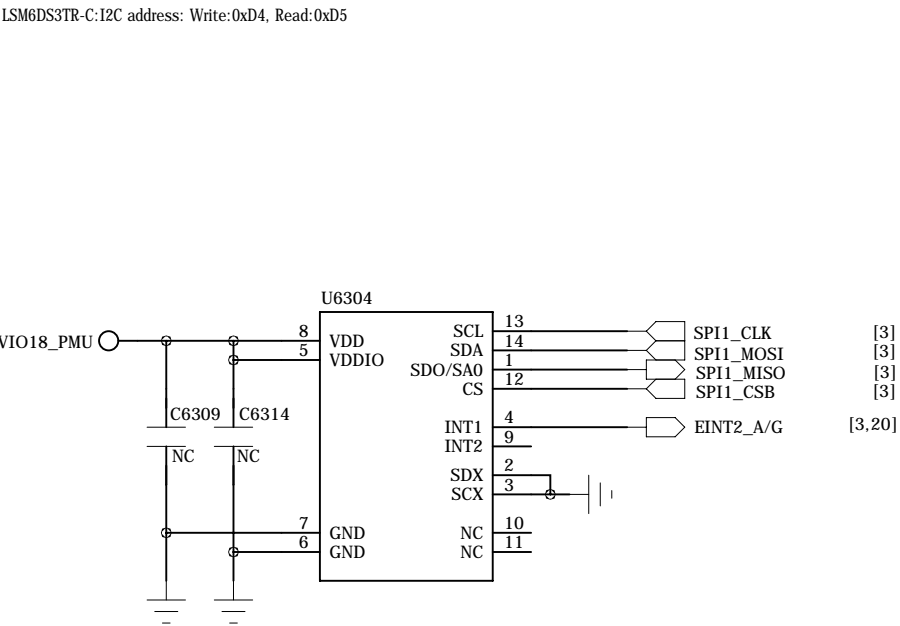
PERI_SENSORS

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

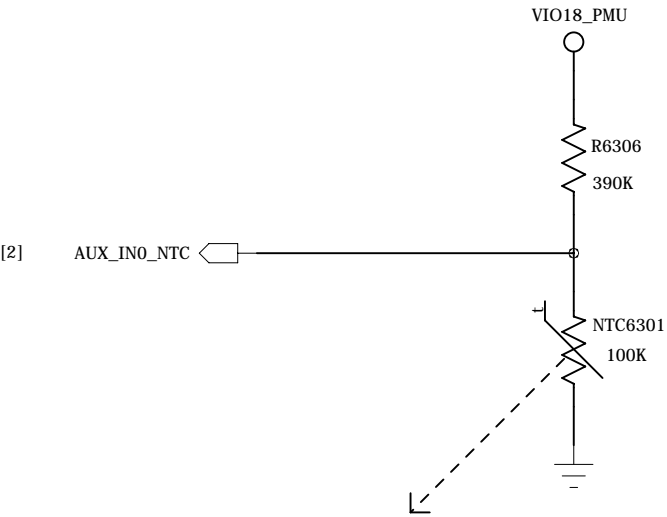
AL& PS Sensor



G-Sensor + Gyro Sensor

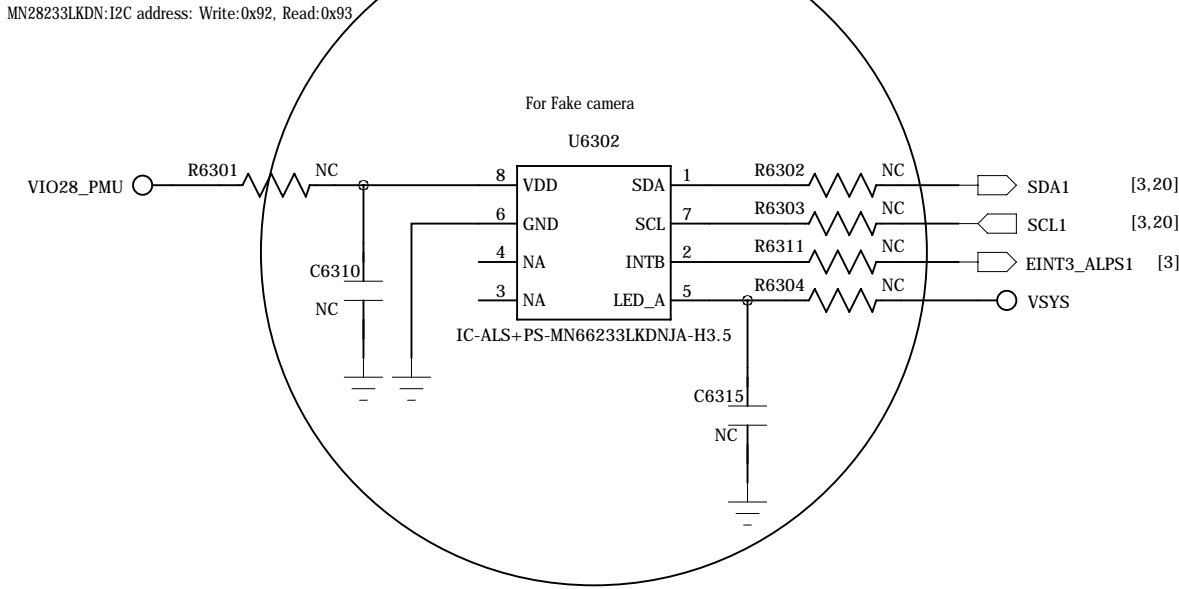


Thermistor to sense AP temperature



1. NTC6301 must keep a distance about 6-8 mm away from BB and far from other heat sources 10 mm at least.
2. The distance is the shortest distance from package edge to edge.

AL& PS Sensor

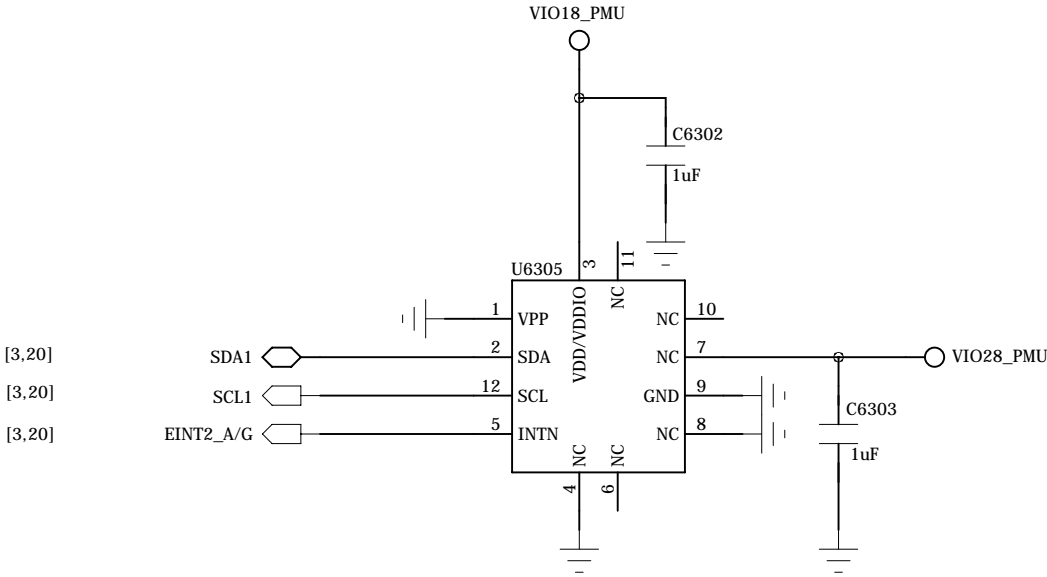


M-Sensor (COMPASS)

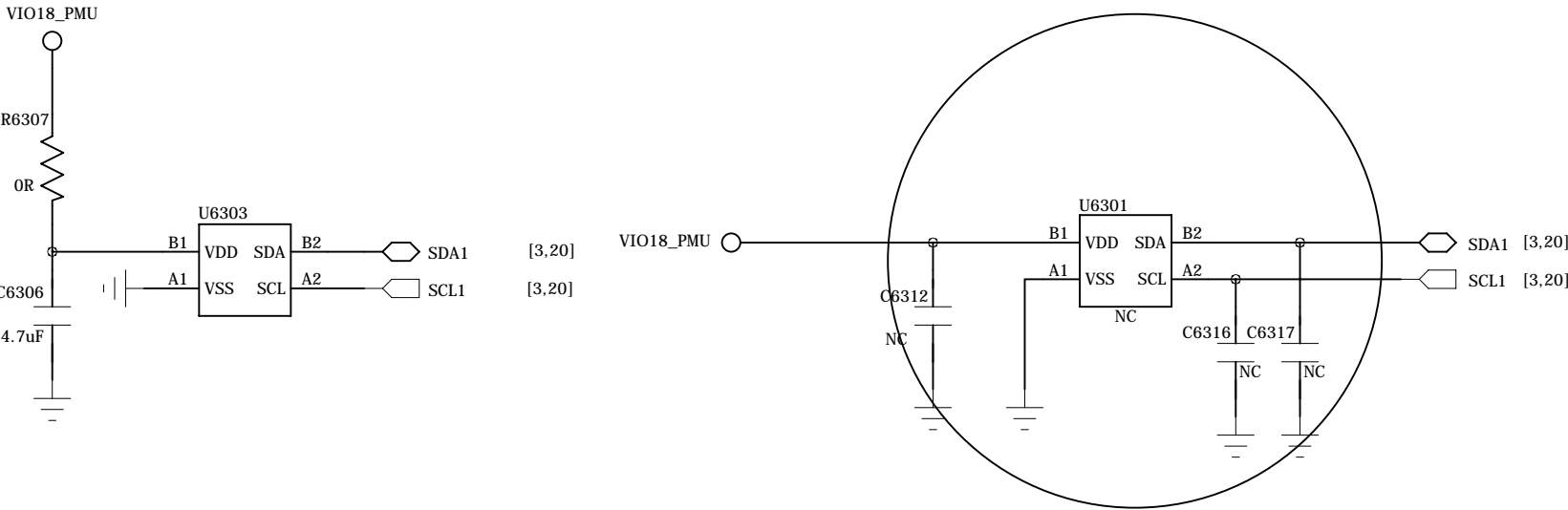
MMC5603: I2C ADDRESS: 0x60 (Write) / 0x61 (Read)
QMC6308: I2C ADDRESS: 0x38 (Write) / 0x39 (Read)

G-Sensor

MXC6655XA: ADDRESS: 0x2A (Write) / 0x2B (Read) default
STK8BA50-S/SC7A20: ADDRESS: 0x30 (Write) / 0x31 (Read) Reserve



component	MXC6655XA	SC7A20	STK8BA50-S
C6302	NC	100nF	1uF
C6303	1uF	100nF	1uF



COMPANY: TRANSSION HOLDINGS

MODEL: H696

Modified Date: 2020/12/30

DRAWN

DJF/TS

DATED 2020/08/13

TITLE: 63_PERI_SENSORS

VERSION: V1.0

SHEET: 20 OF 22

CHECKED

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DATED

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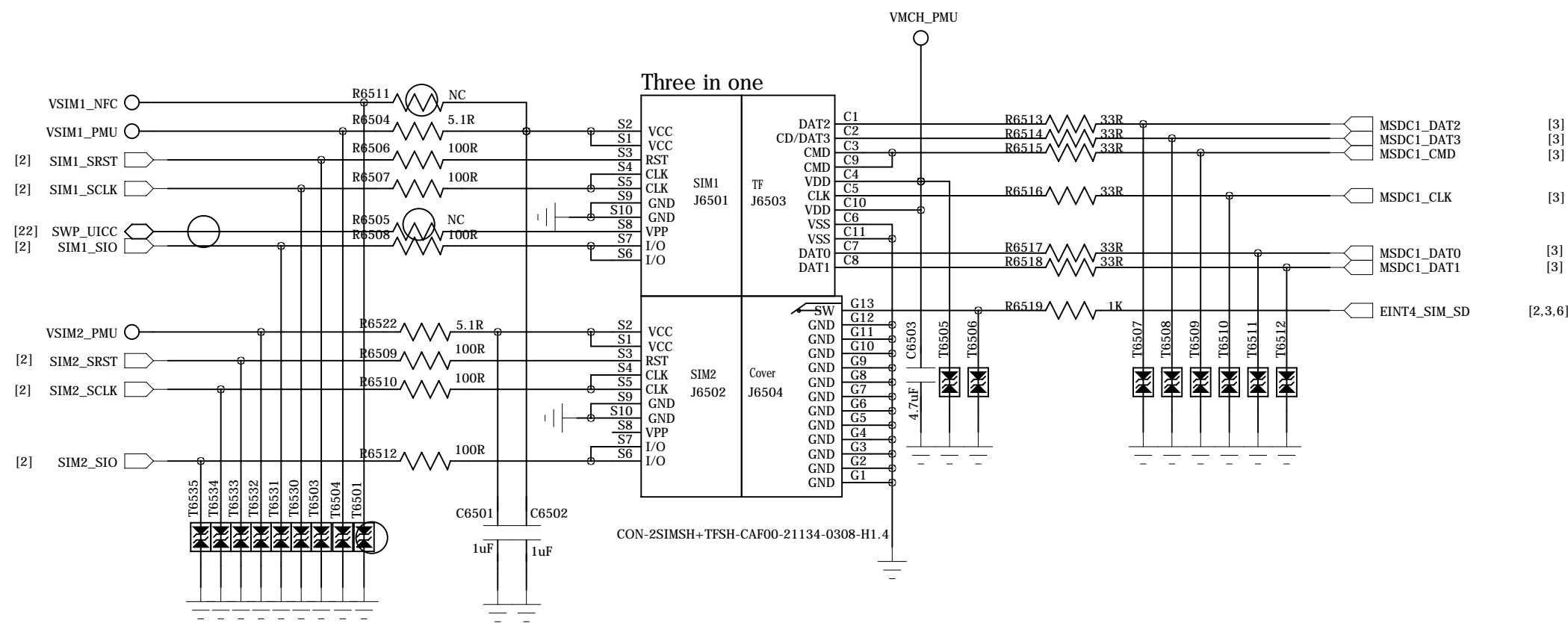
Confidentiality

CONFIDENTIAL

PERI_SIM_SD_KEYPAD

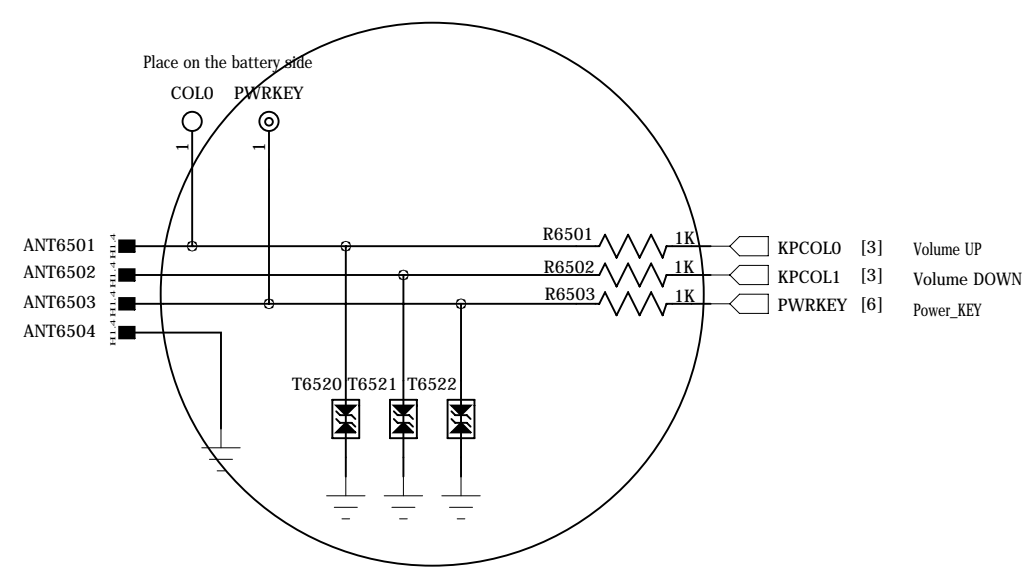
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

SIM1/2-SD-CARD



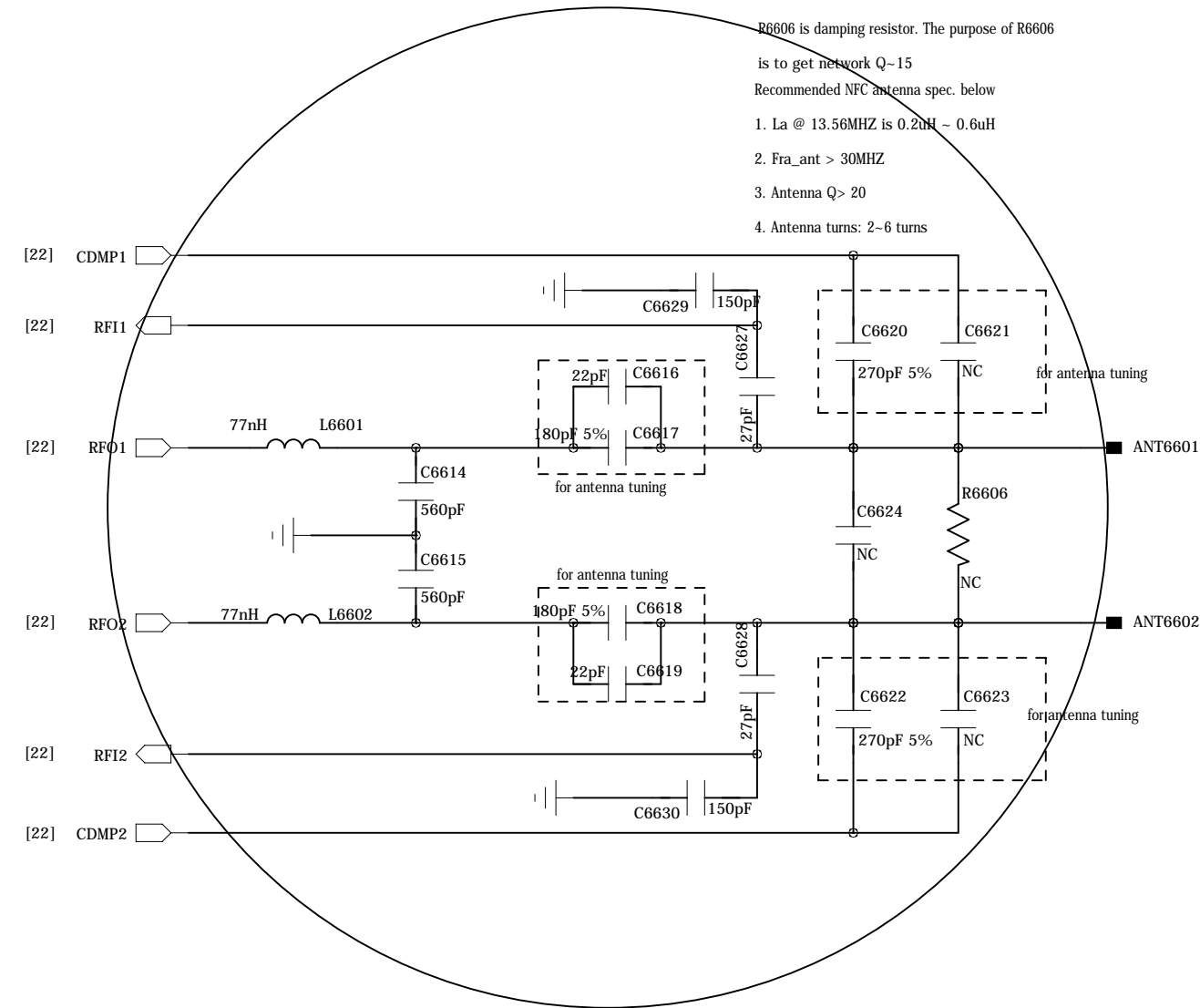
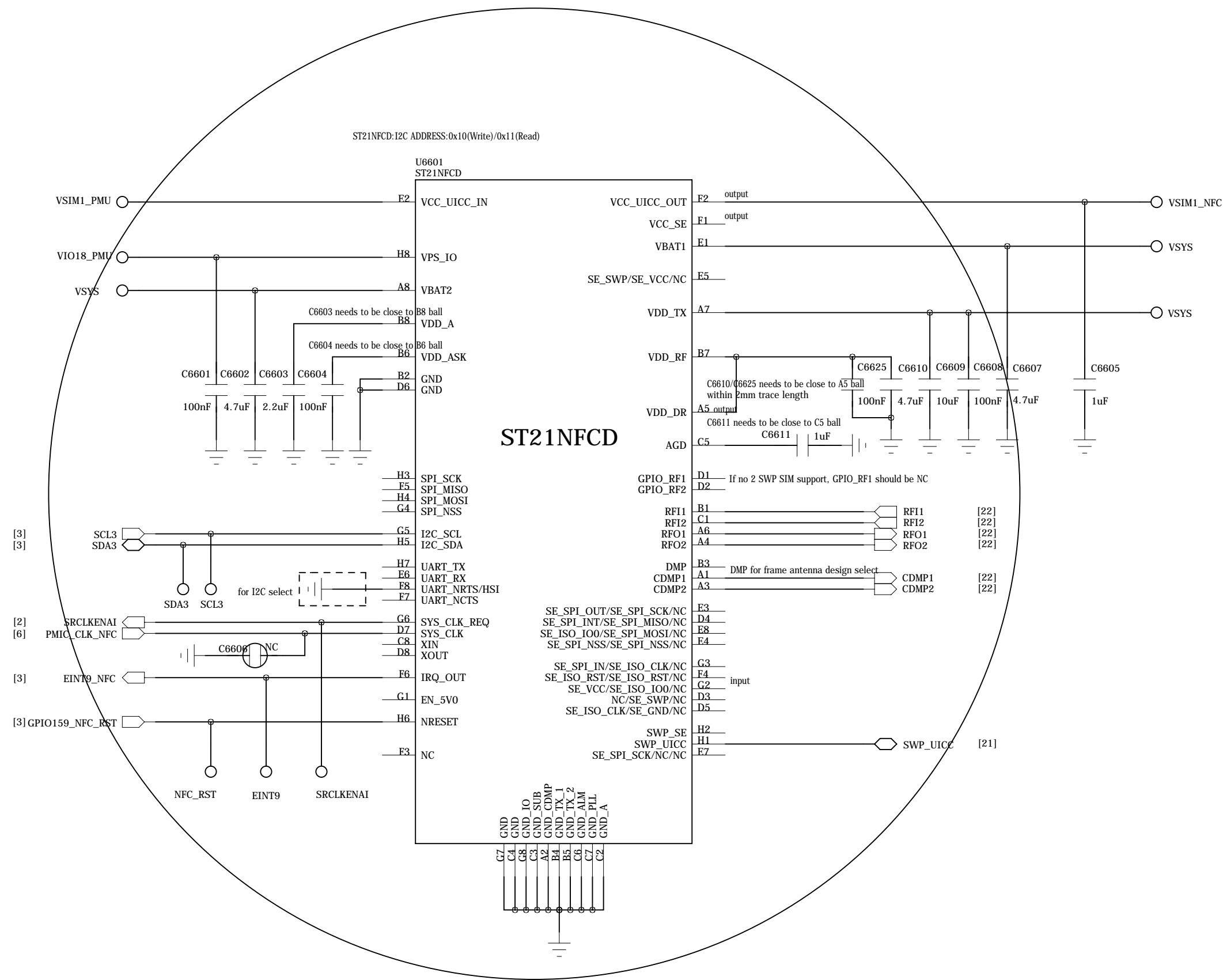
SD POWER CONTROL

SIDEKEY



COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 65_PERI_SIM_SD_KEYPAD		VERSION: V1.0	SHEET: 21 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: TRANSSION HOLDINGS				MODEL: H696		Modified Date: 2020/12/30	
DRAWN	DJF/TS	DATED	2020/08/13	TITLE: 66_PERI_NFC		VERSION: V1.0	SHEET: 22 OF 22
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		