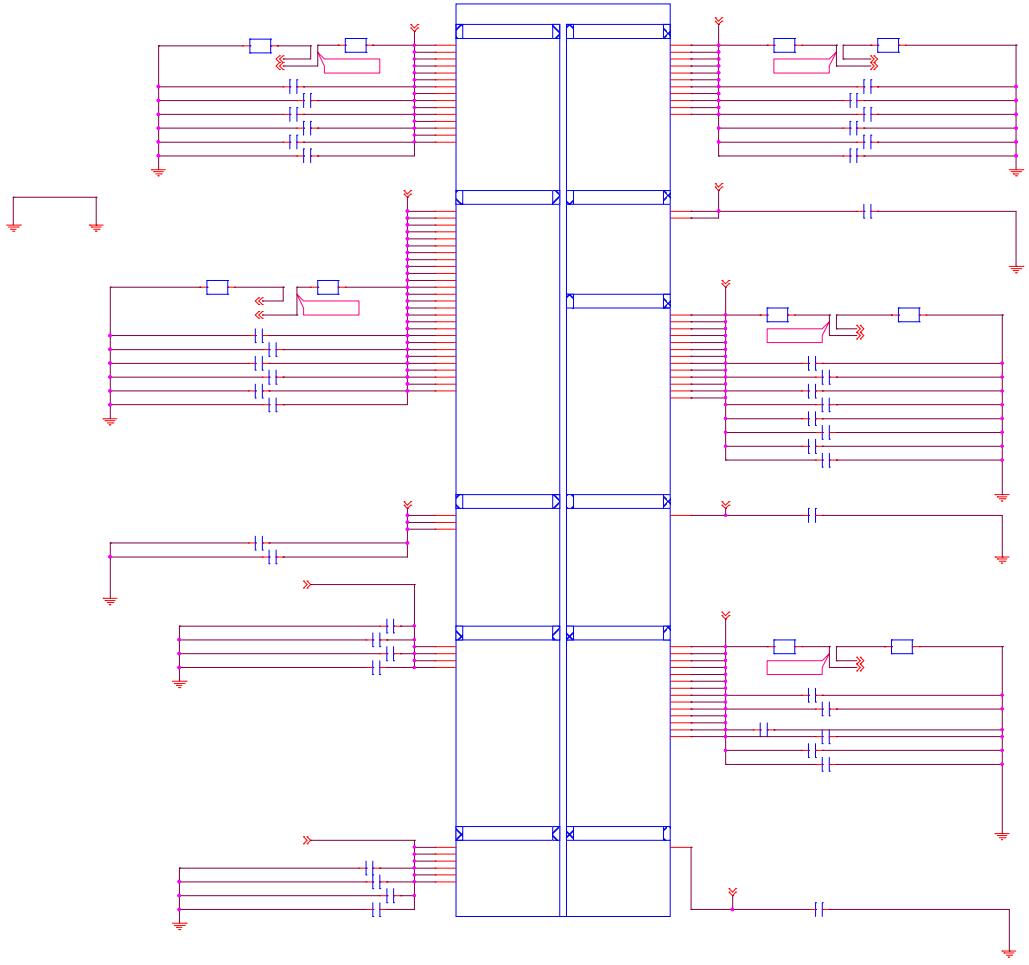

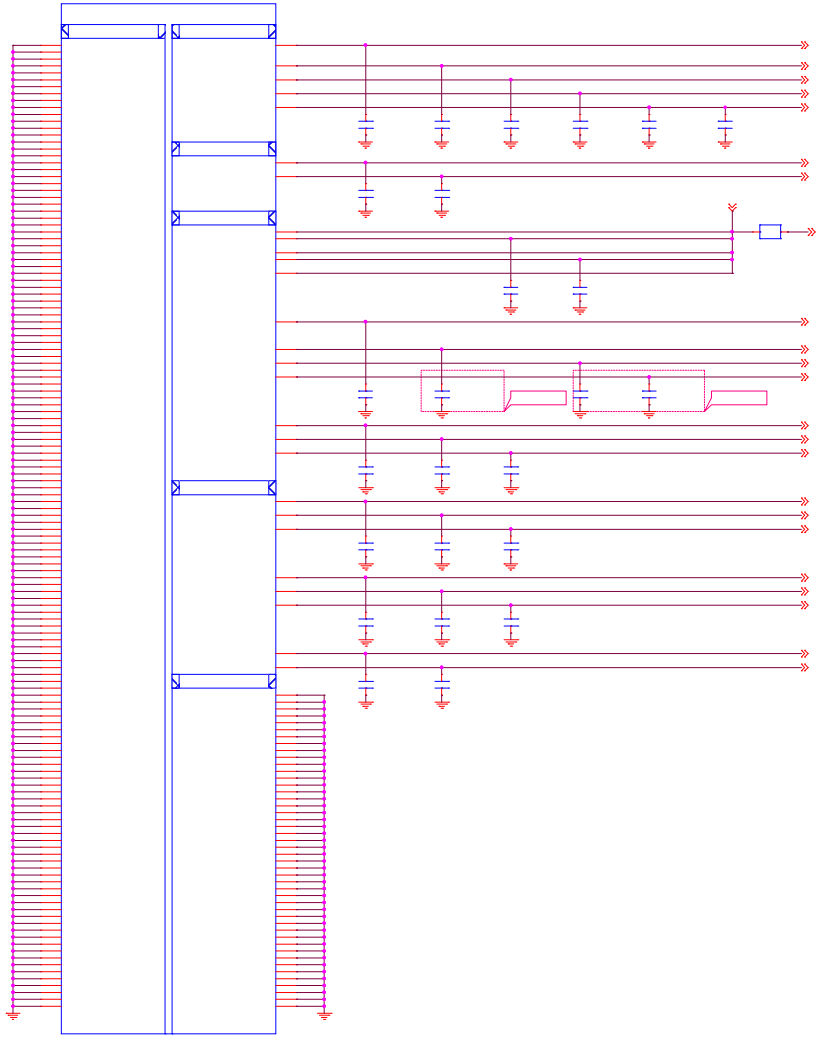

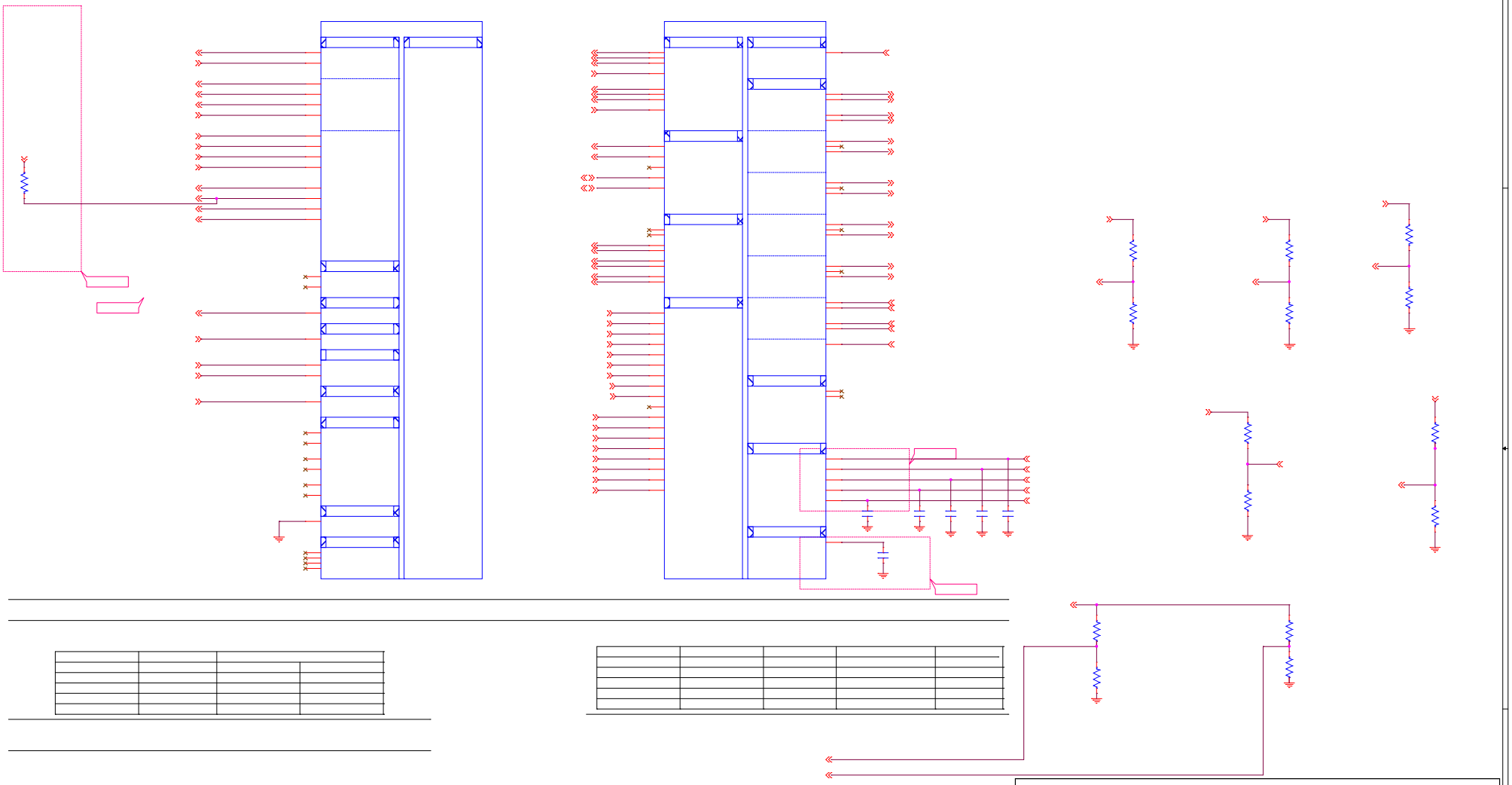

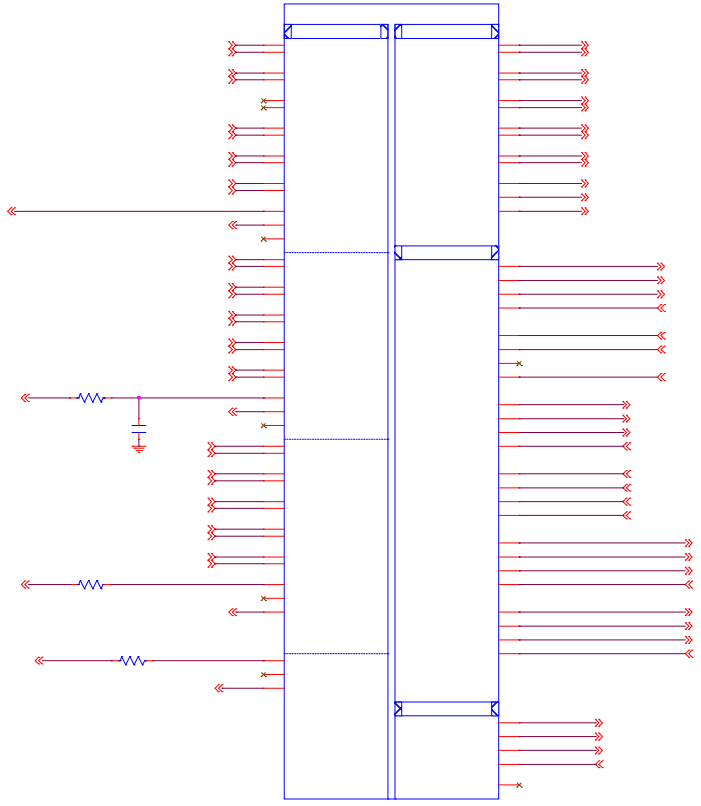

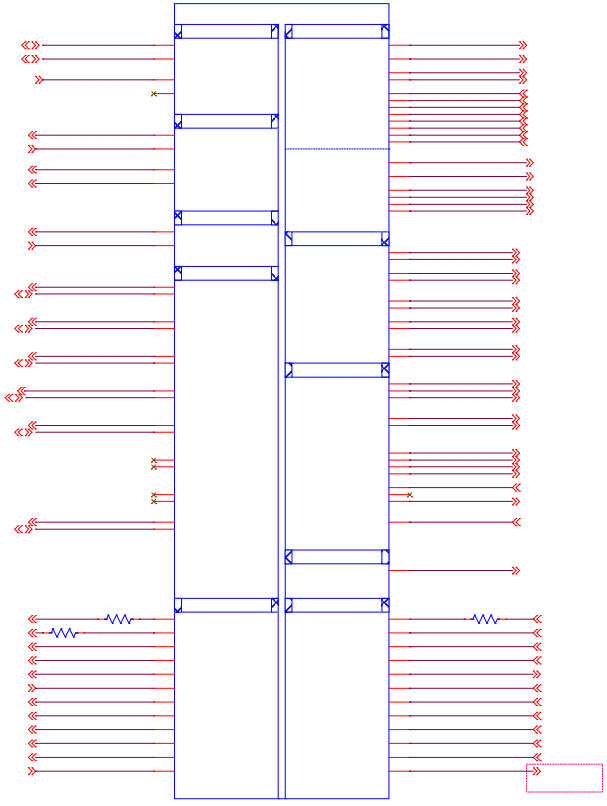

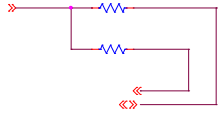

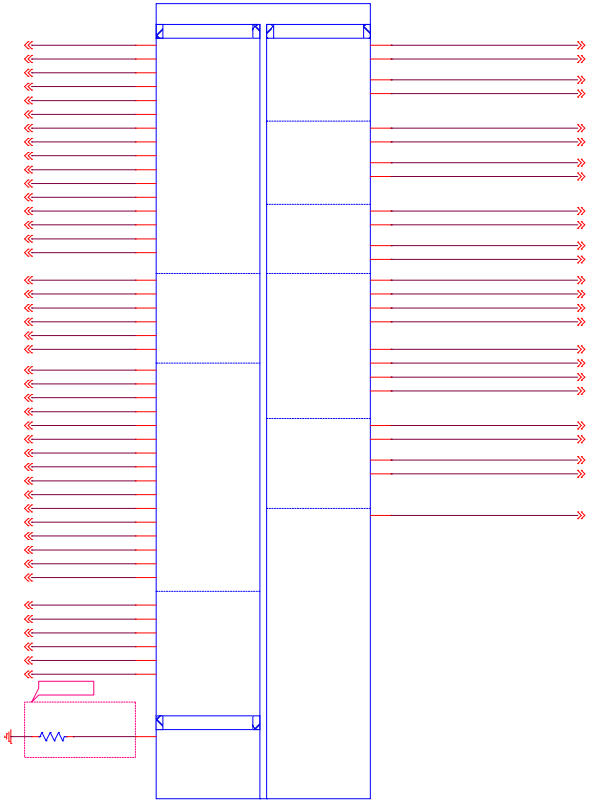

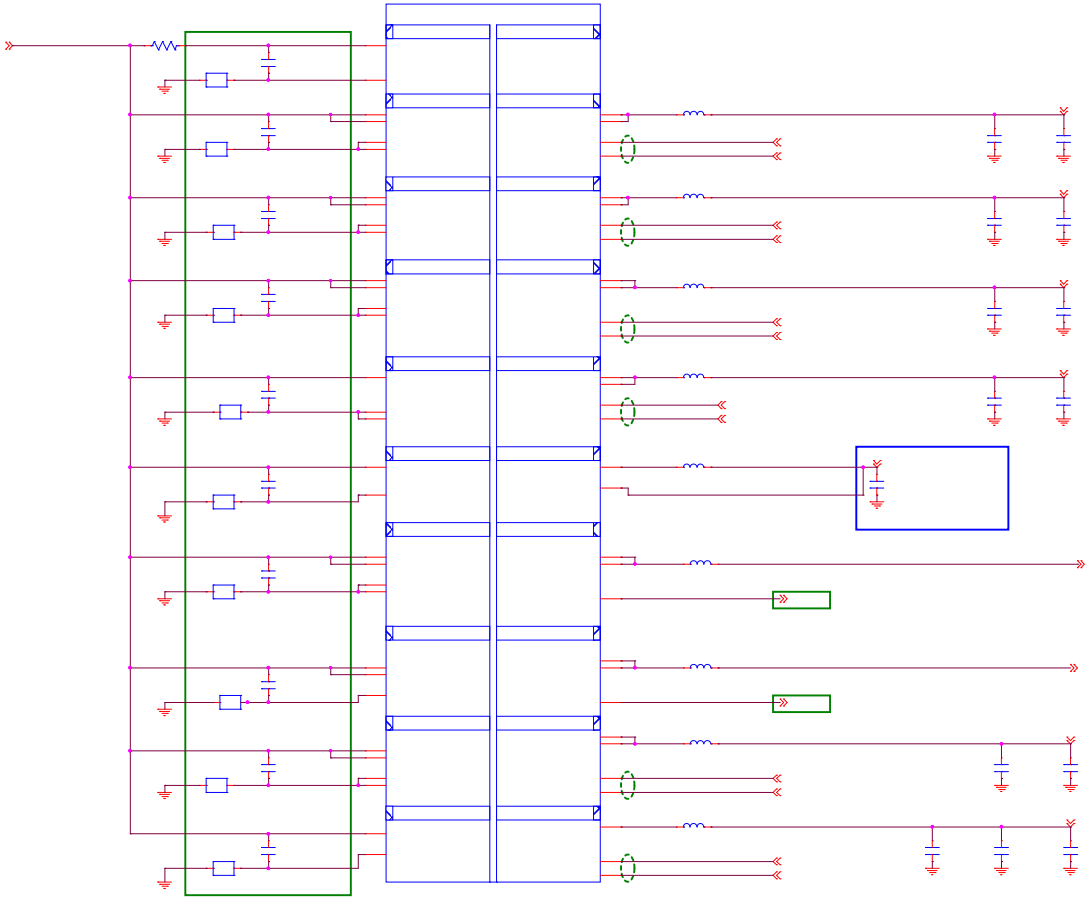

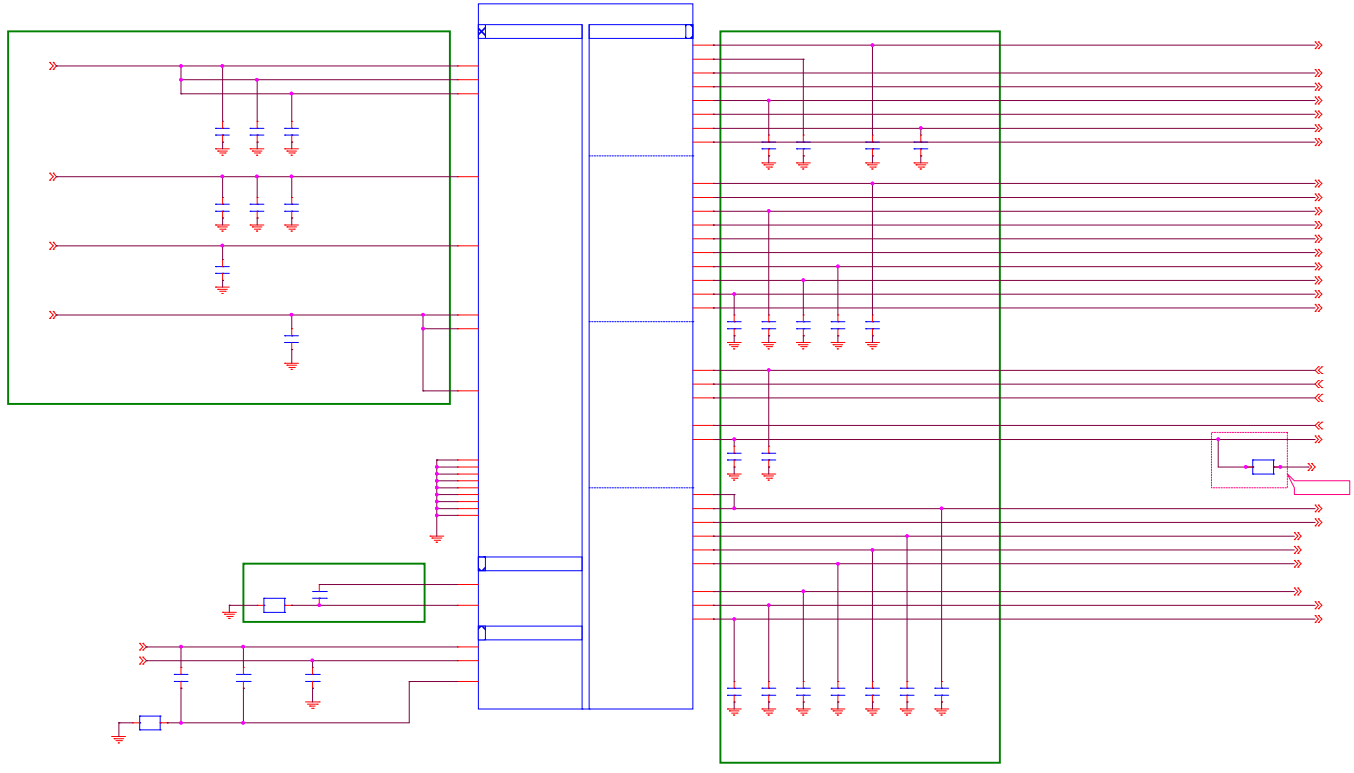

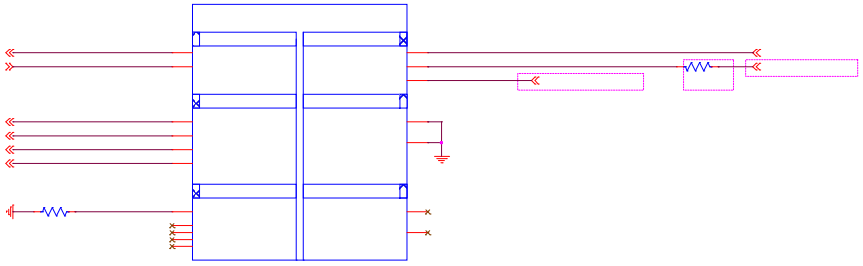

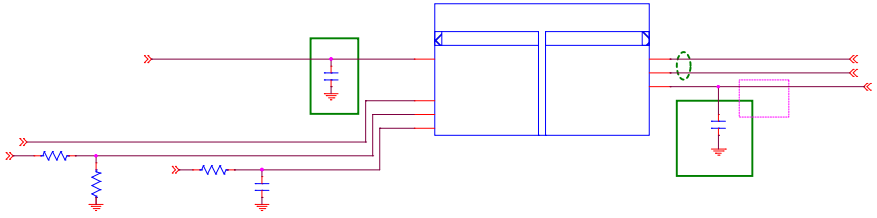

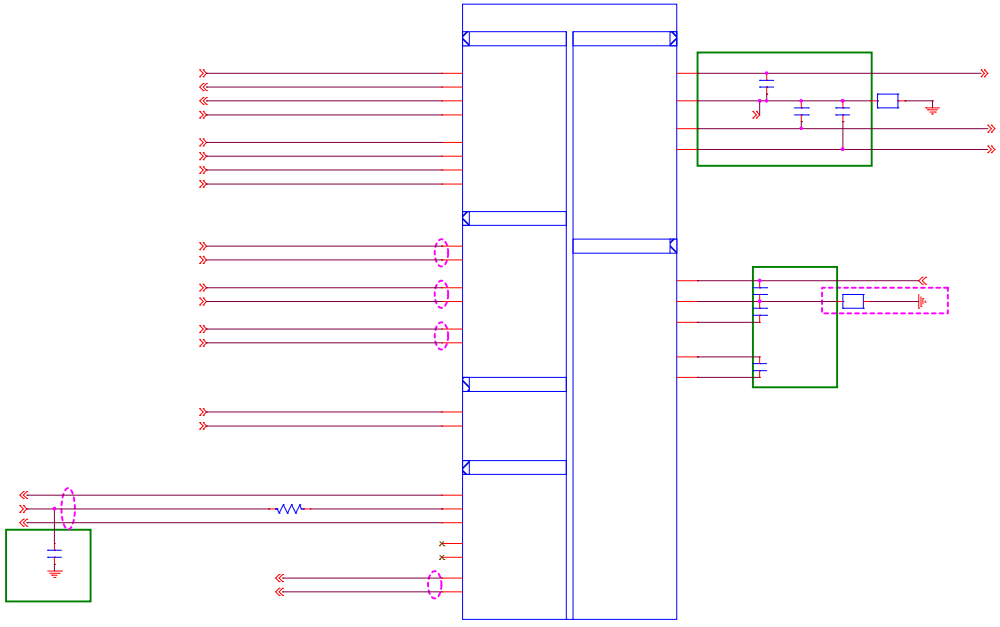

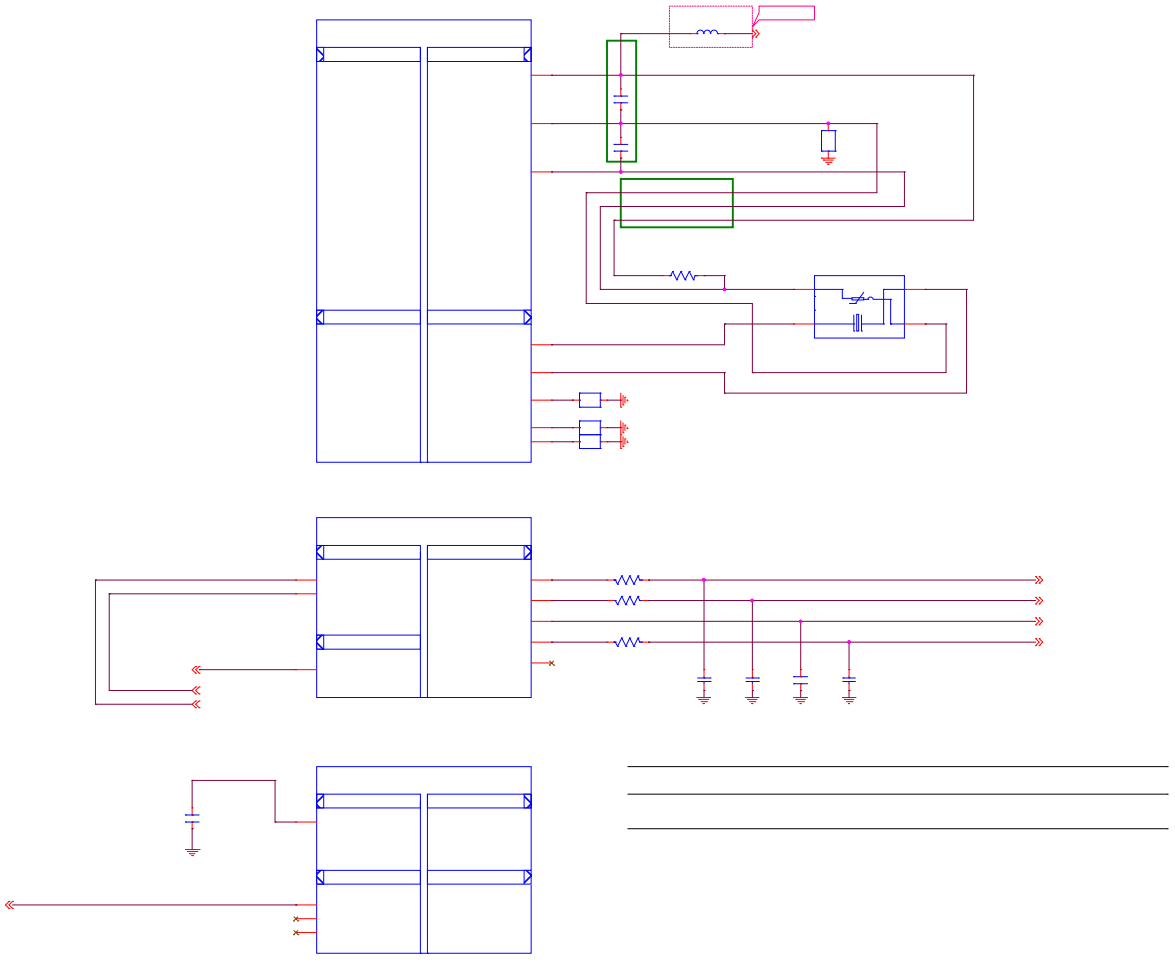

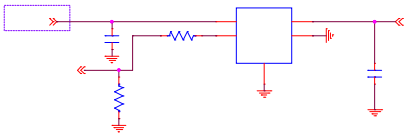

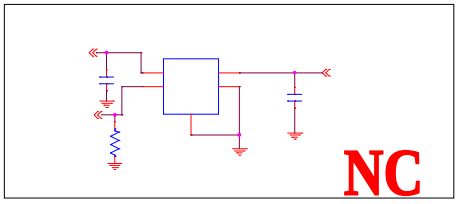
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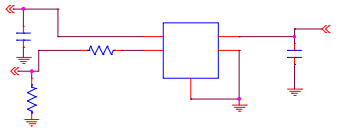

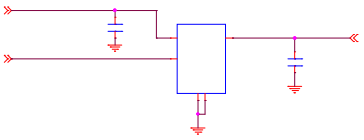

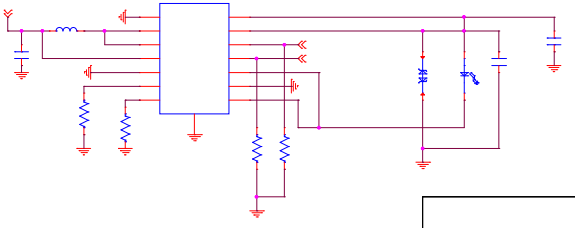

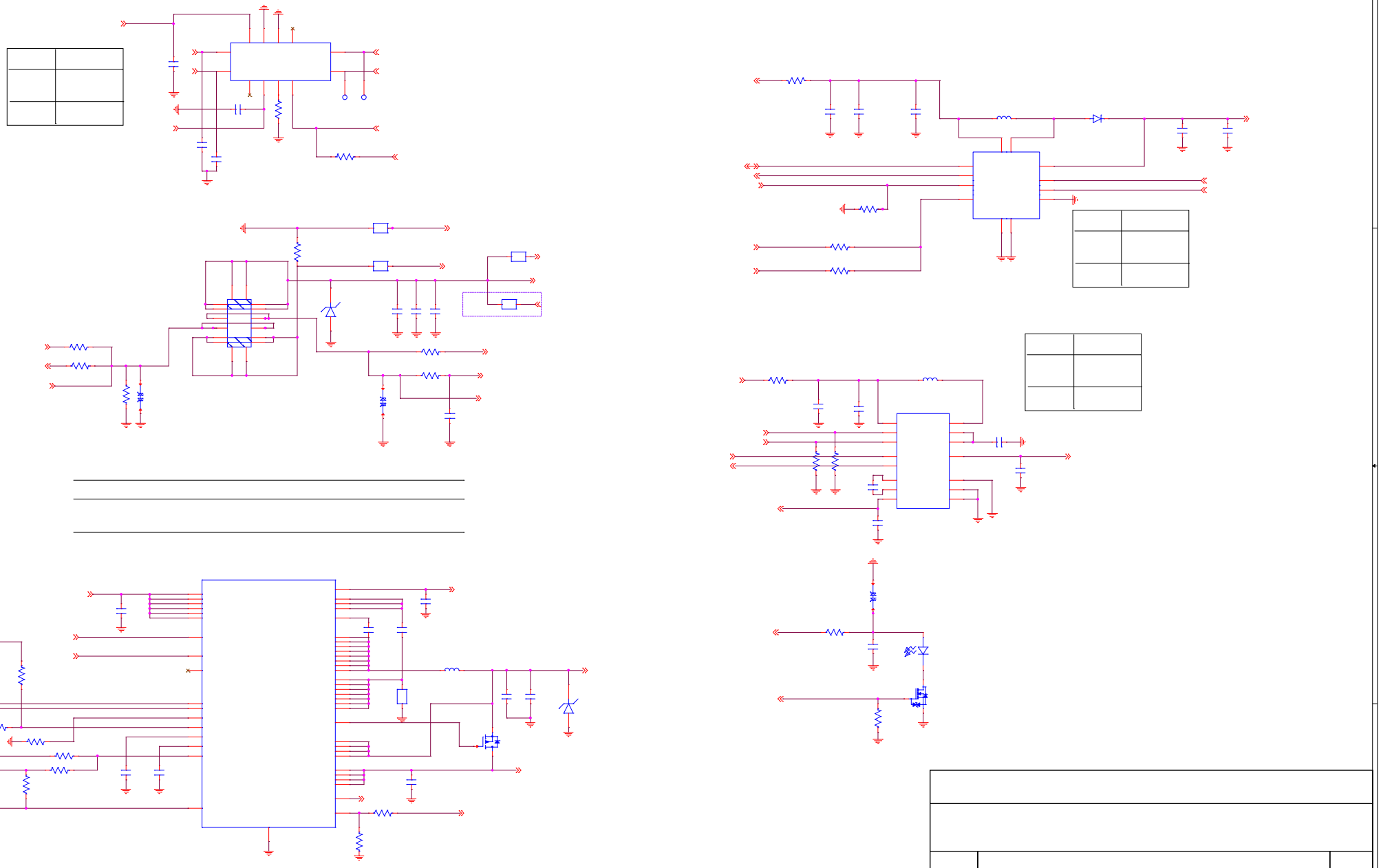

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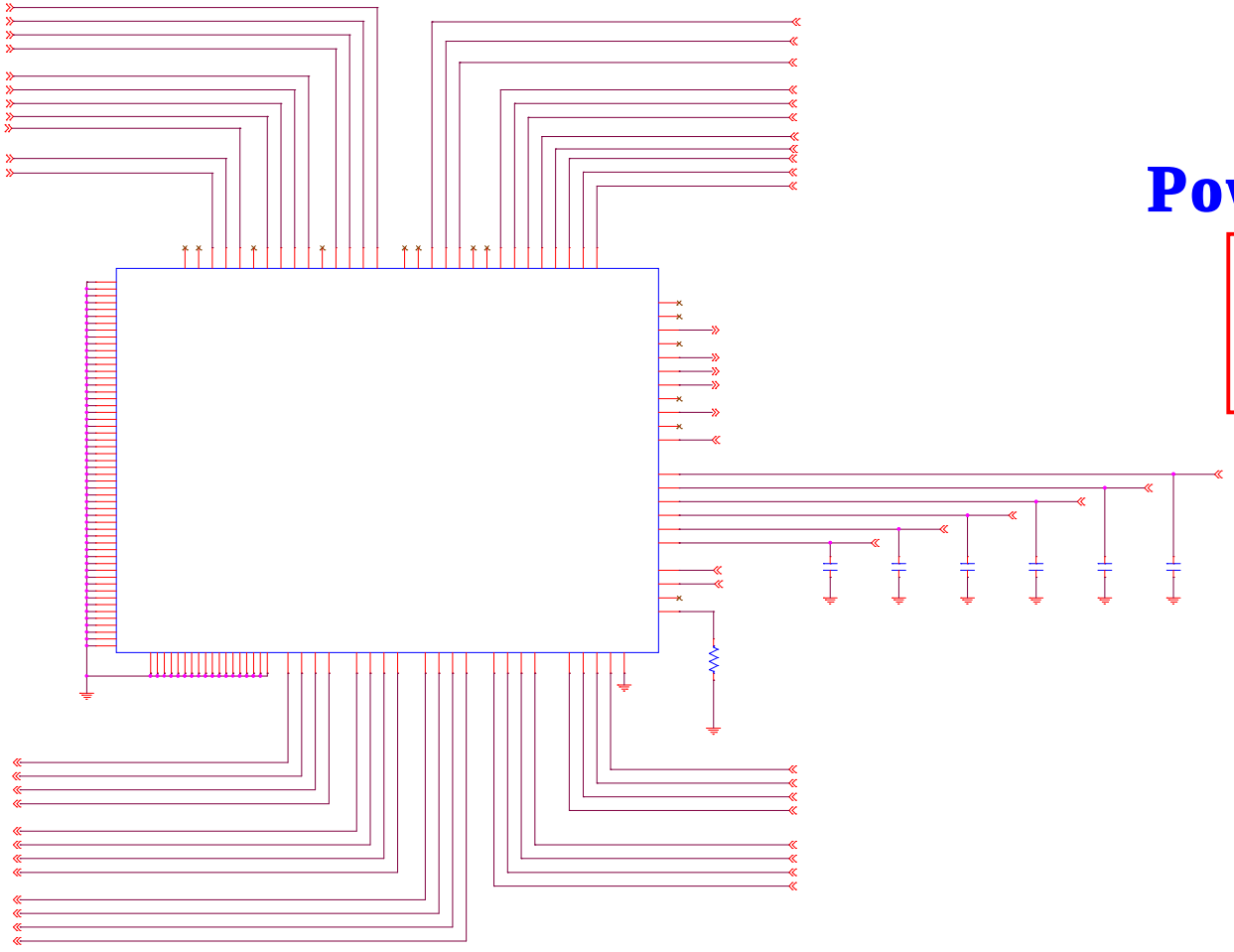
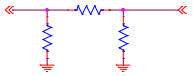
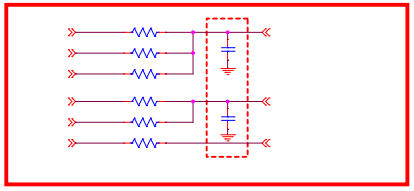


Attenuator for Detector



Power For MT6177



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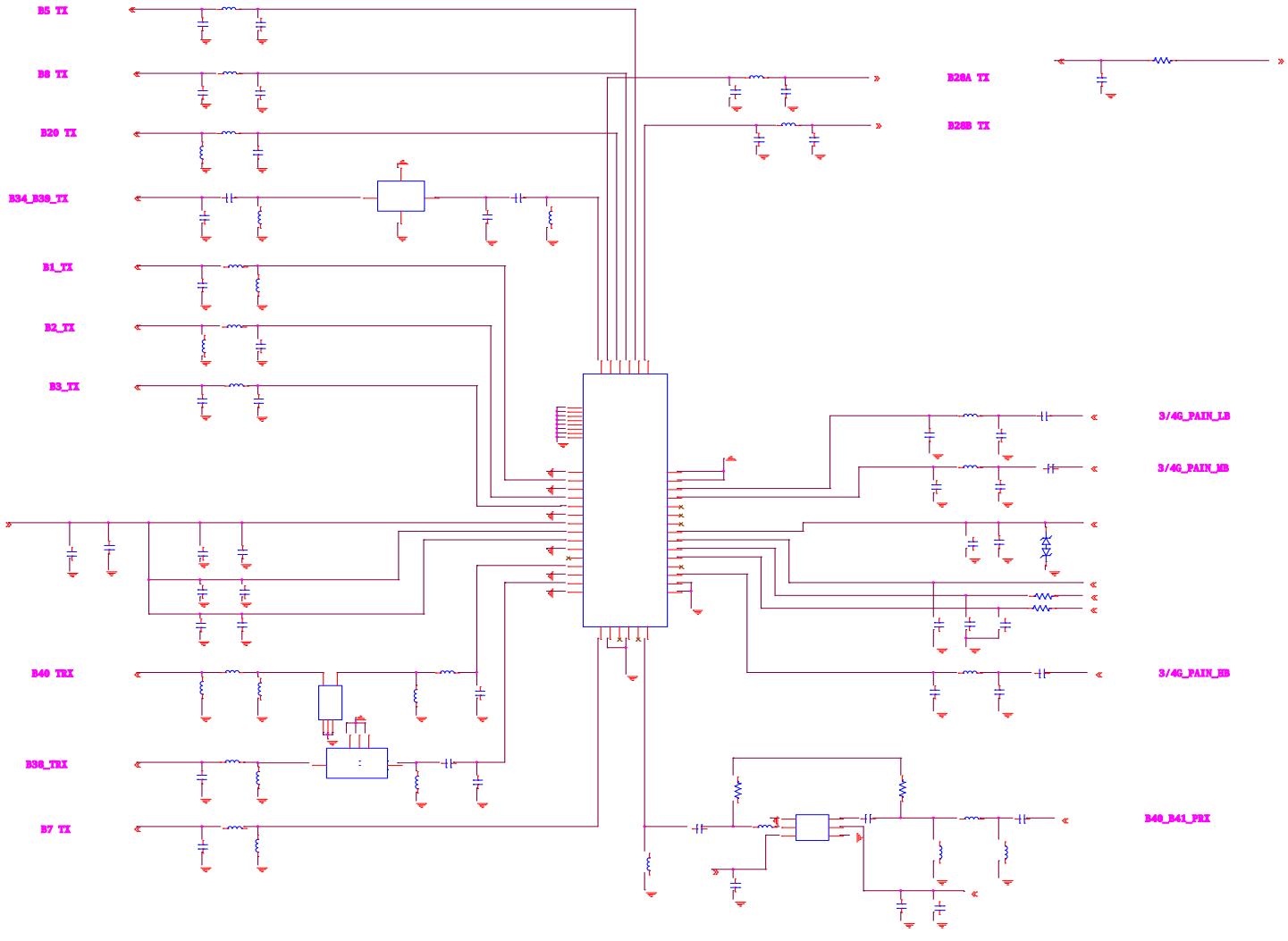
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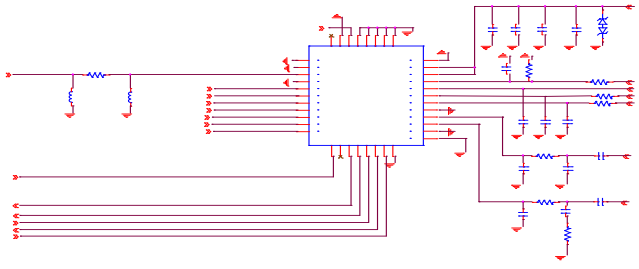

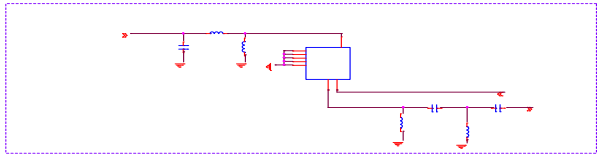

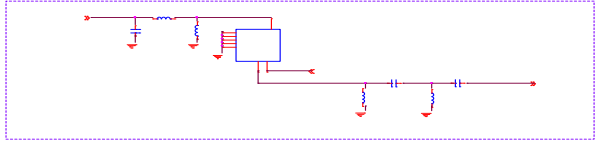

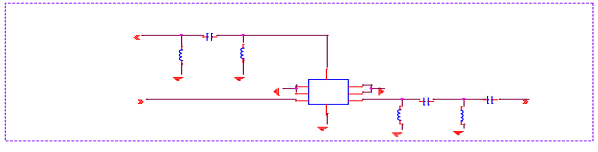

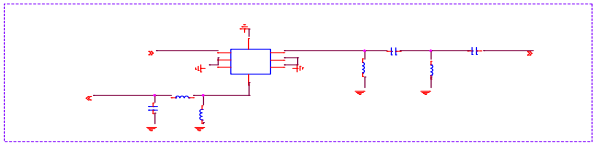

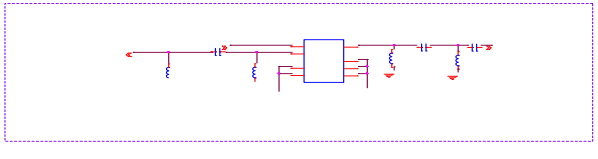

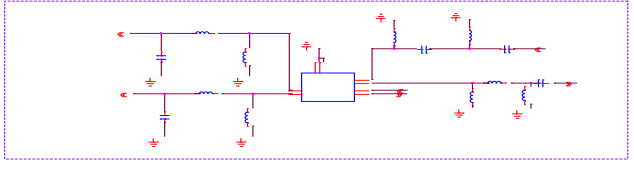

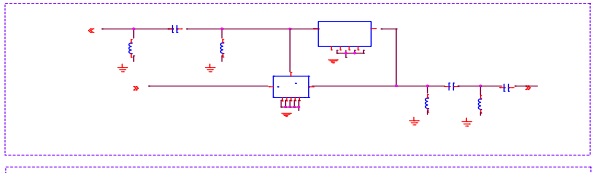

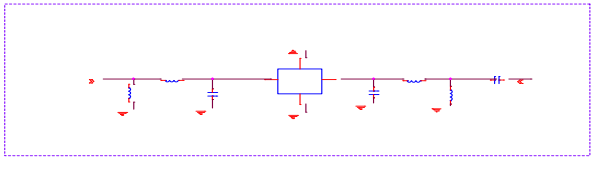

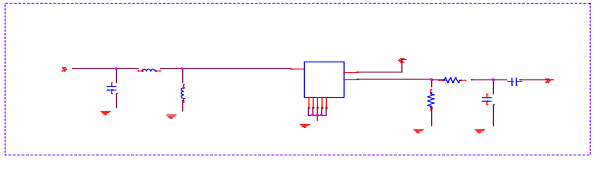

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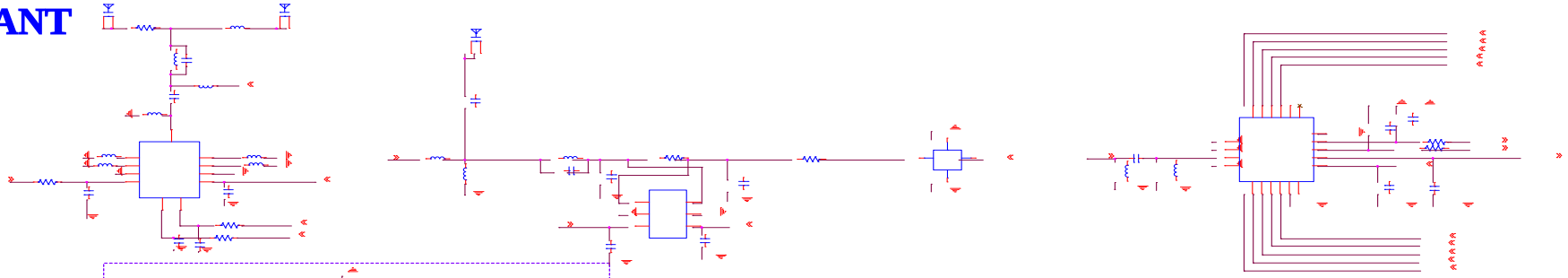

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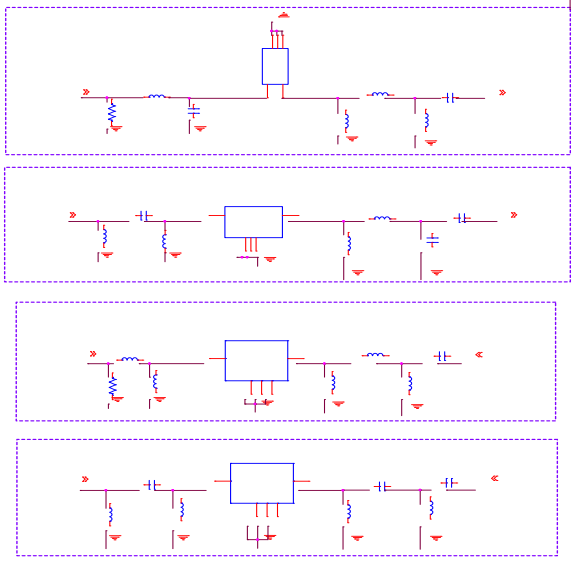

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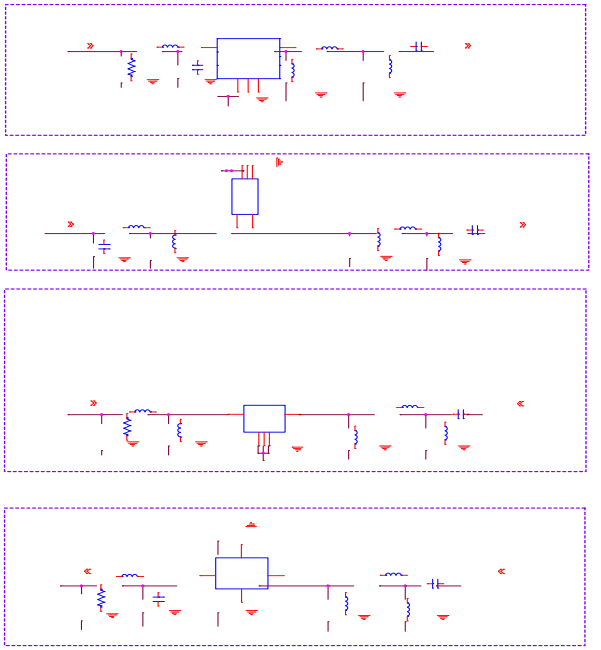

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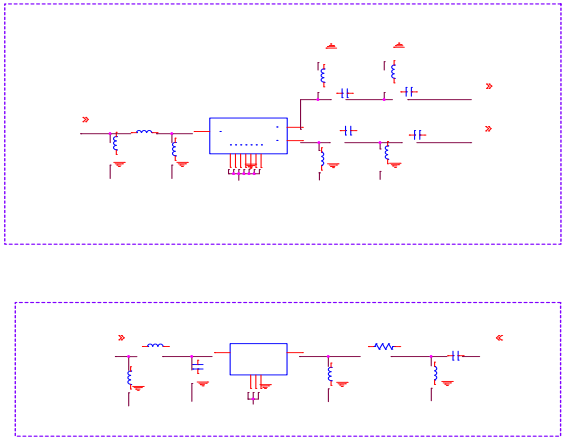

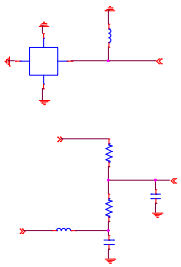

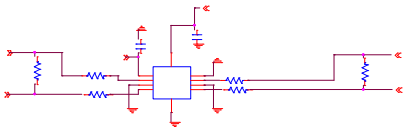

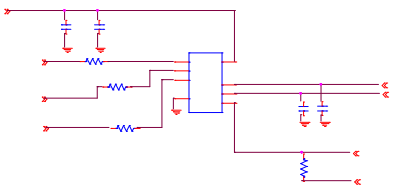

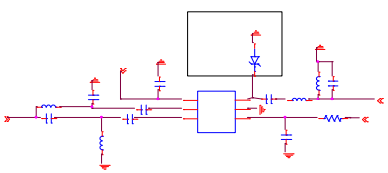

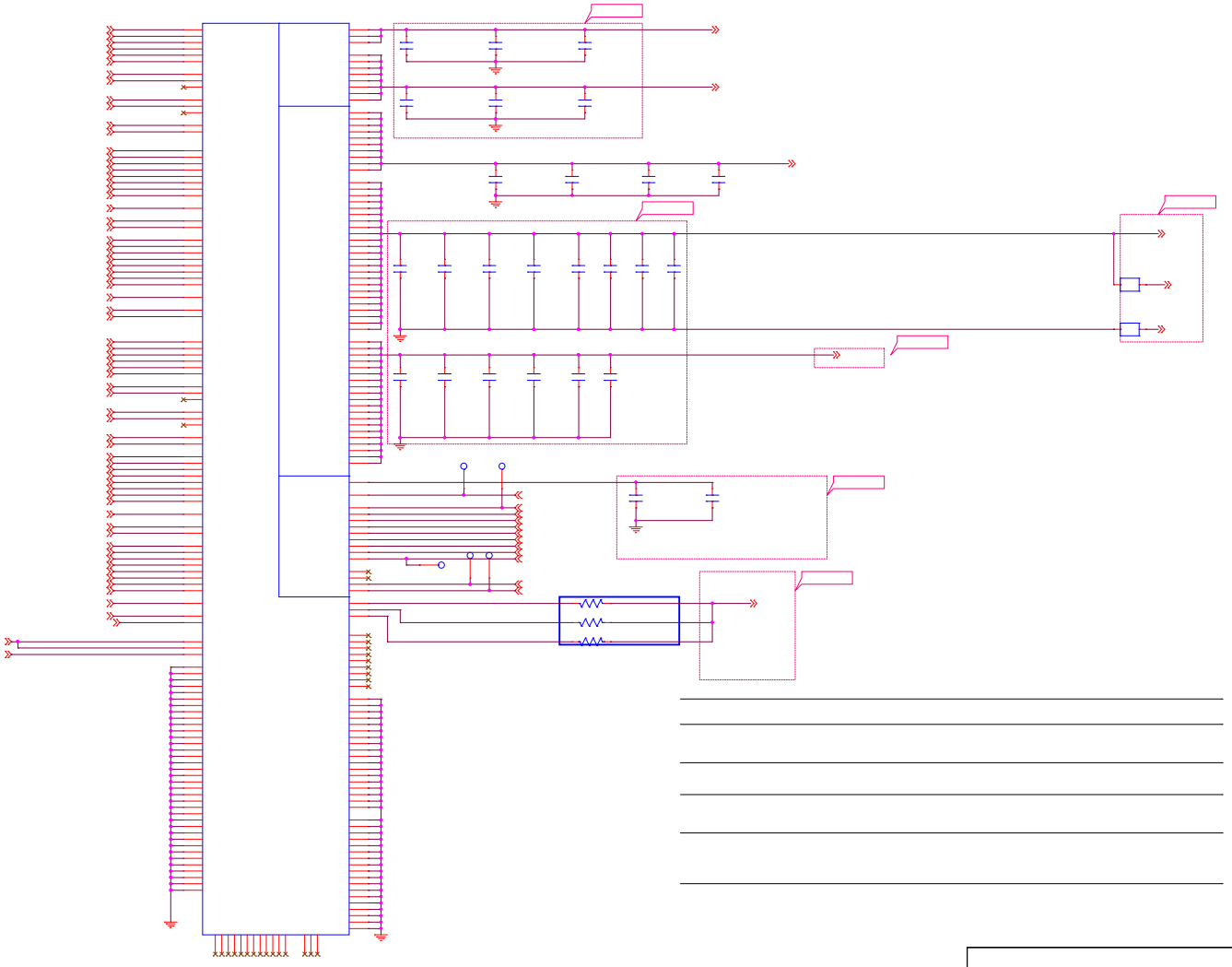

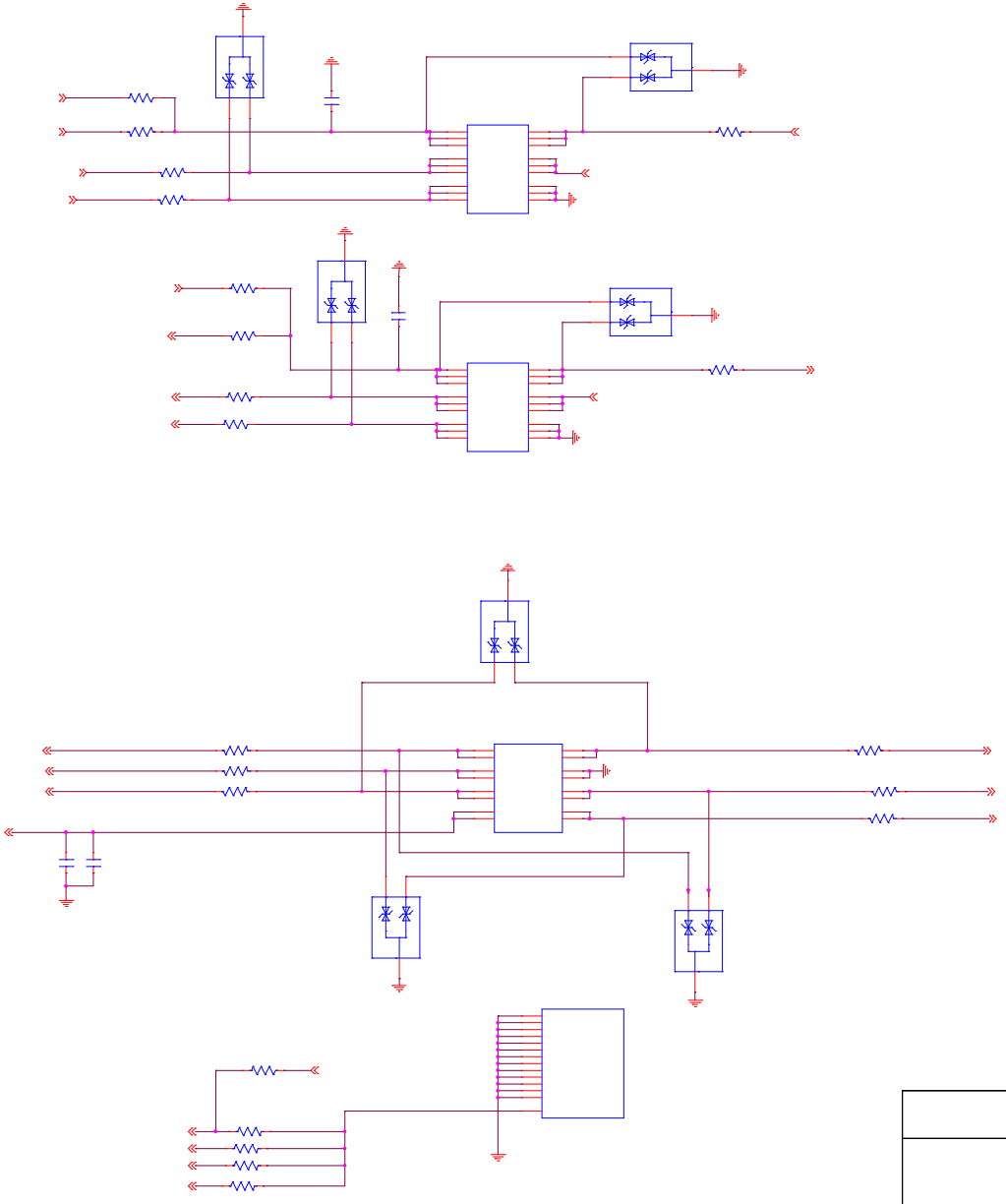

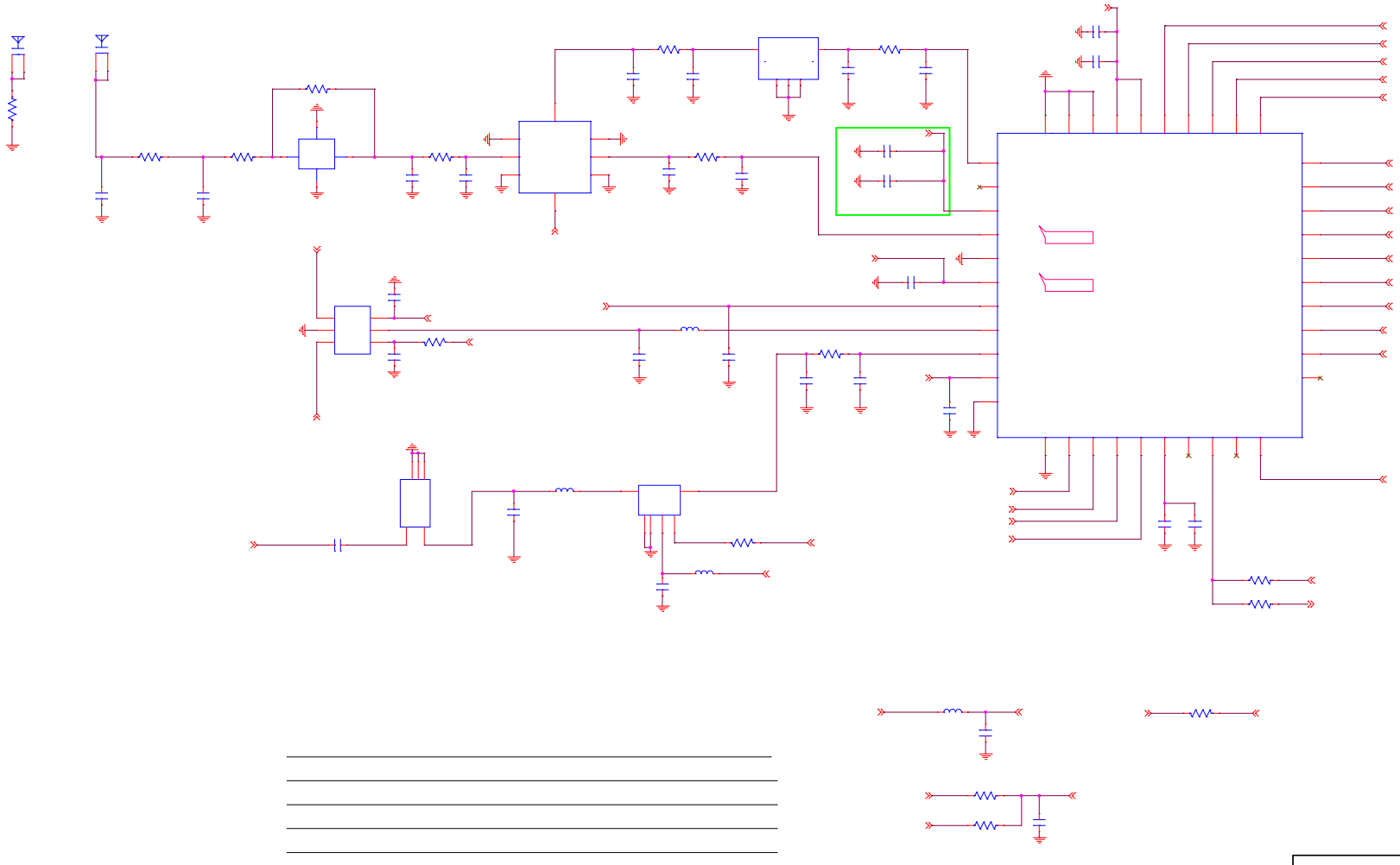

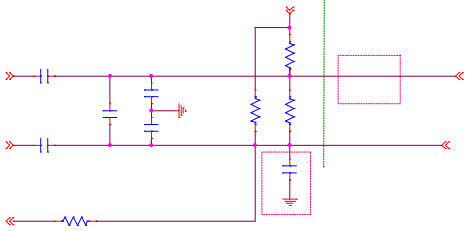

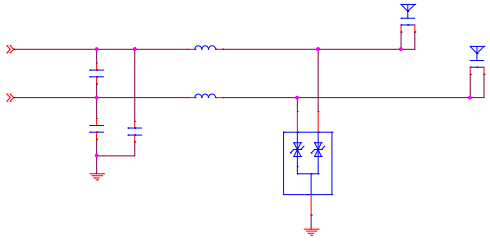

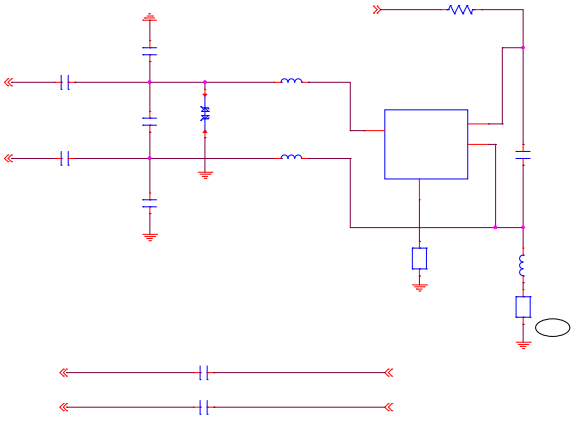

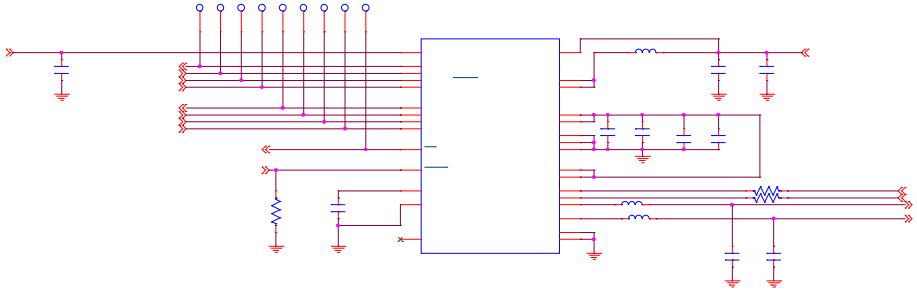

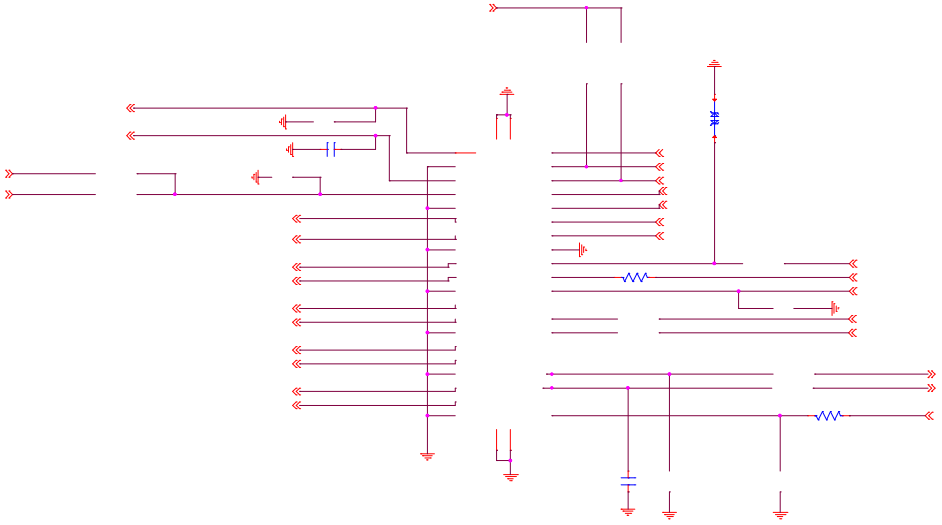

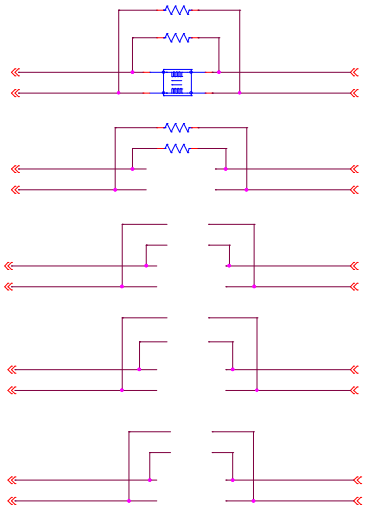

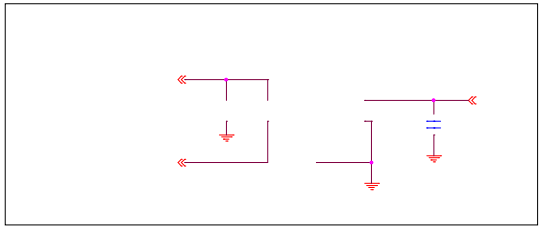

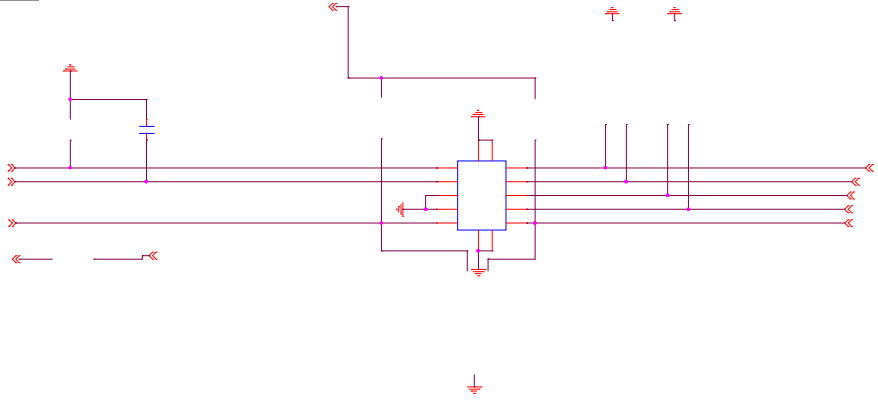

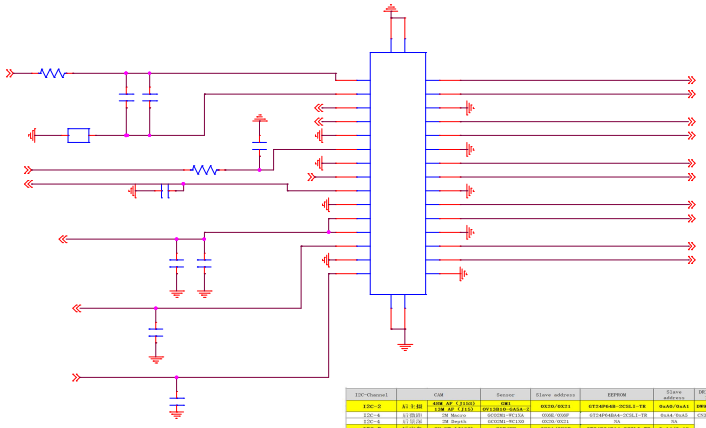

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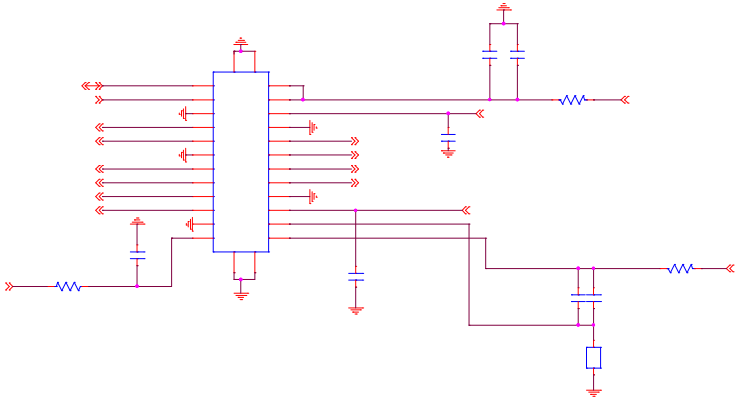
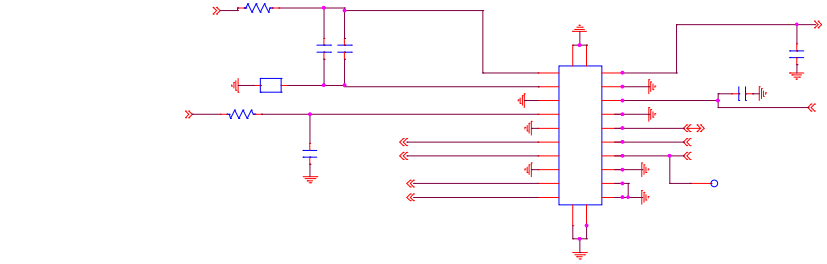
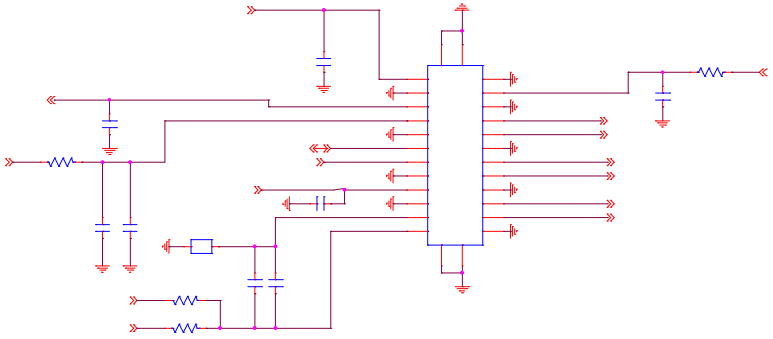
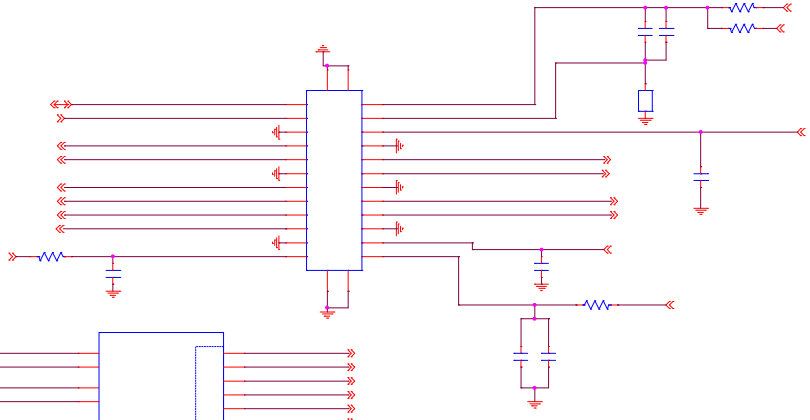
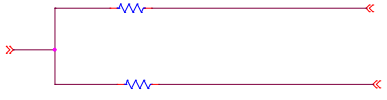
I2C-4	A1 GLIO	2M Macro	OV02B1B-GA8A-2	0X50/0X51	GT24P64B4-2CSL1-TR	0xA4/0xA5	CN3927	0X1D/0X1E
I2C-4	A1 GLIO	2M Depth	0X20/0X21	GT24P64B4-2CSL1-TR	NA	NA		
I2C-2	A1/7.8Z	8M UW_IMX503	0X6A/0X6B	GT24P64B4-2CSL1-TR	0xA4/0xA5	NA	NA	
I2C-4	前IR	5M_FF_IMX50	0X3D3C-GA4A-2A	0X50/0X51	GT24P64B4-2CSL1-TR	0xA2/0xA3		
		13M_FF_IMX50	0V13B10-GA8A-2					

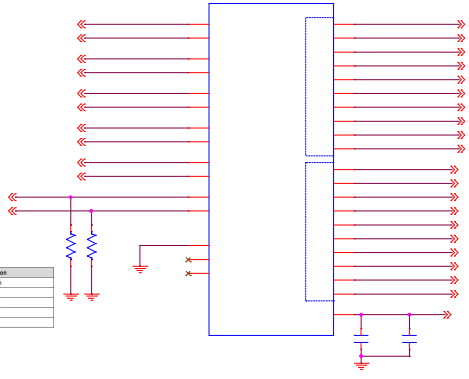








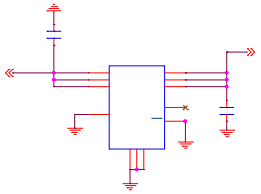




INPUTS			OUTPUTS	
CLK_REG1	CLK_REG2	MCLR_IN	CLK_OUT1	CLK_OUT2
L	L	X	L	L
H	H	CLK	CLK	CLK
L	L	CLK	CLK	L
H	H	CLK	CLK	CLK

Table 1. Function Table

OE	SEL	Function
H	X	I/O pins High-Impedance
L	L	CLK0(P/N) = CLK4(P/N)
		Dn(P/N) = Dbs(P/N)
L	H	CLK0(P/N) = CLK5(P/N)
		Dn(P/N) = Dbs(P/N)



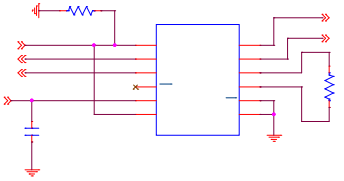
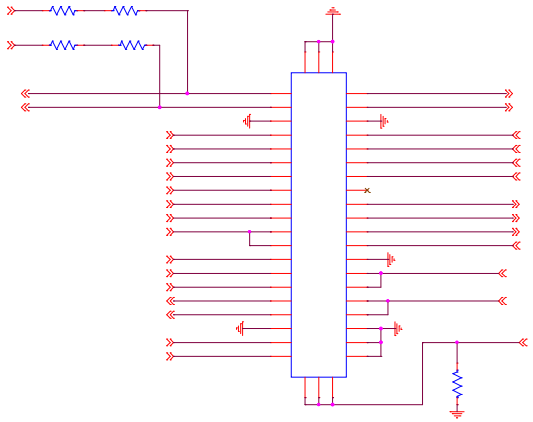
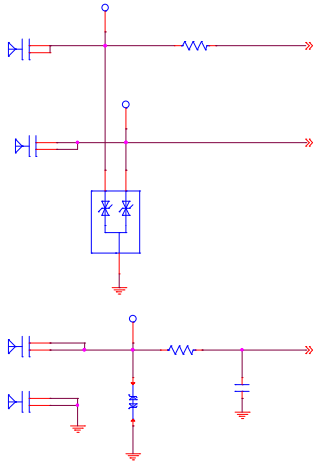
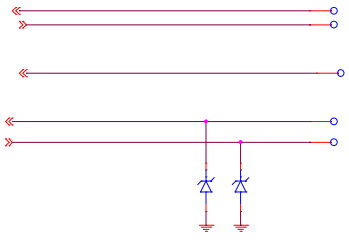
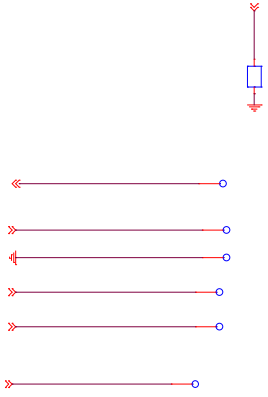
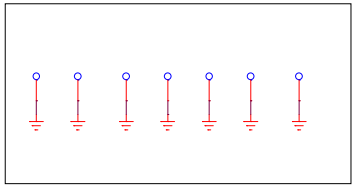
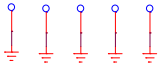
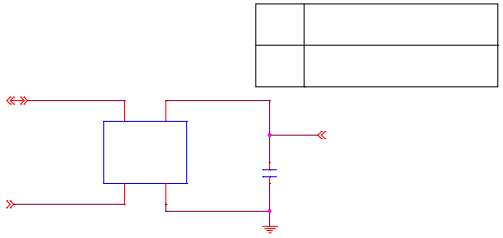
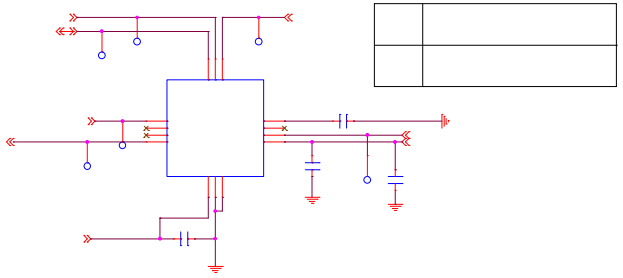
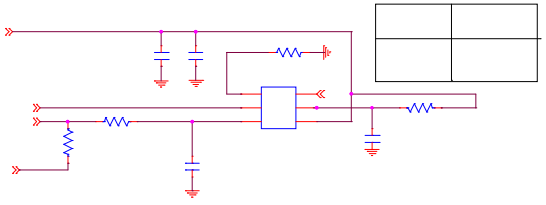
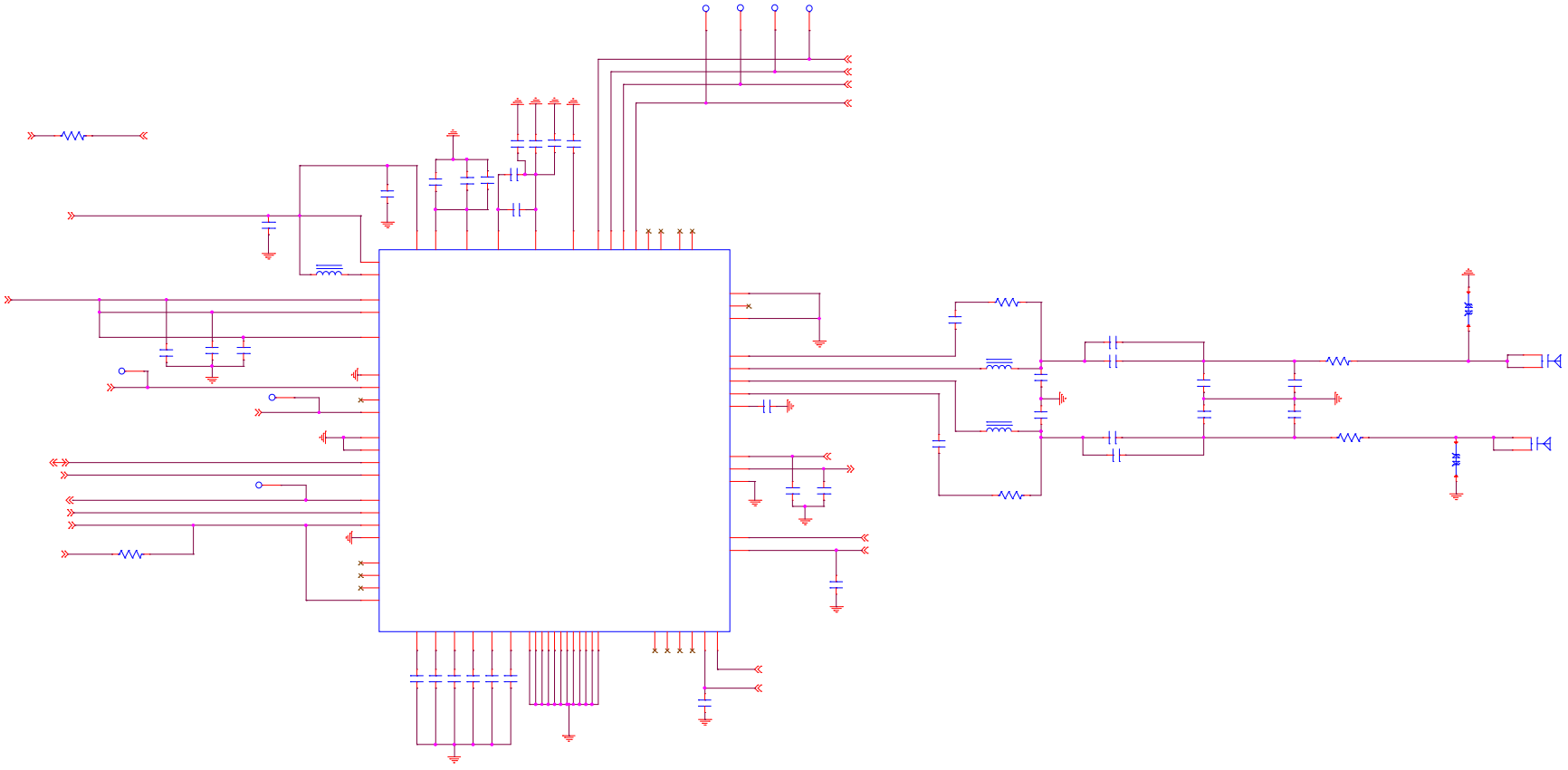


Table 1. Function Table

$\overline{OE}$	SEL1	SEL2	D- Connection	D+ Connection
H	X	X	High-Z	High-Z
L	L	L	D- to D1-	D+ to D1+
L	L	H	D- to D1-	D+ to D2+
L	H	L	D- to D2-	D+ to D1+
L	H	H	D- to D2-	D+ to D2+