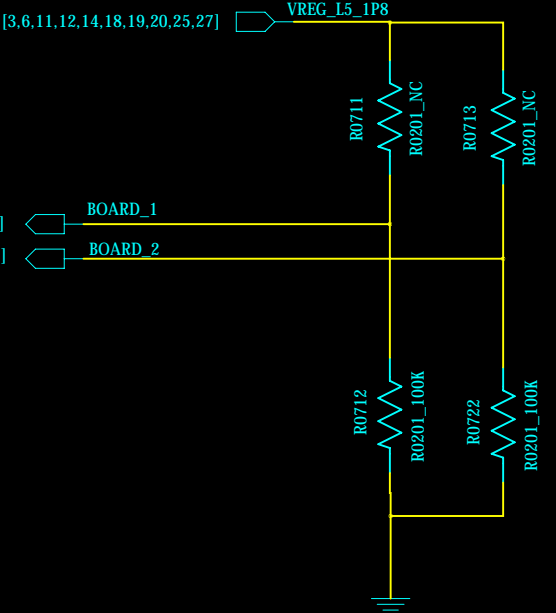
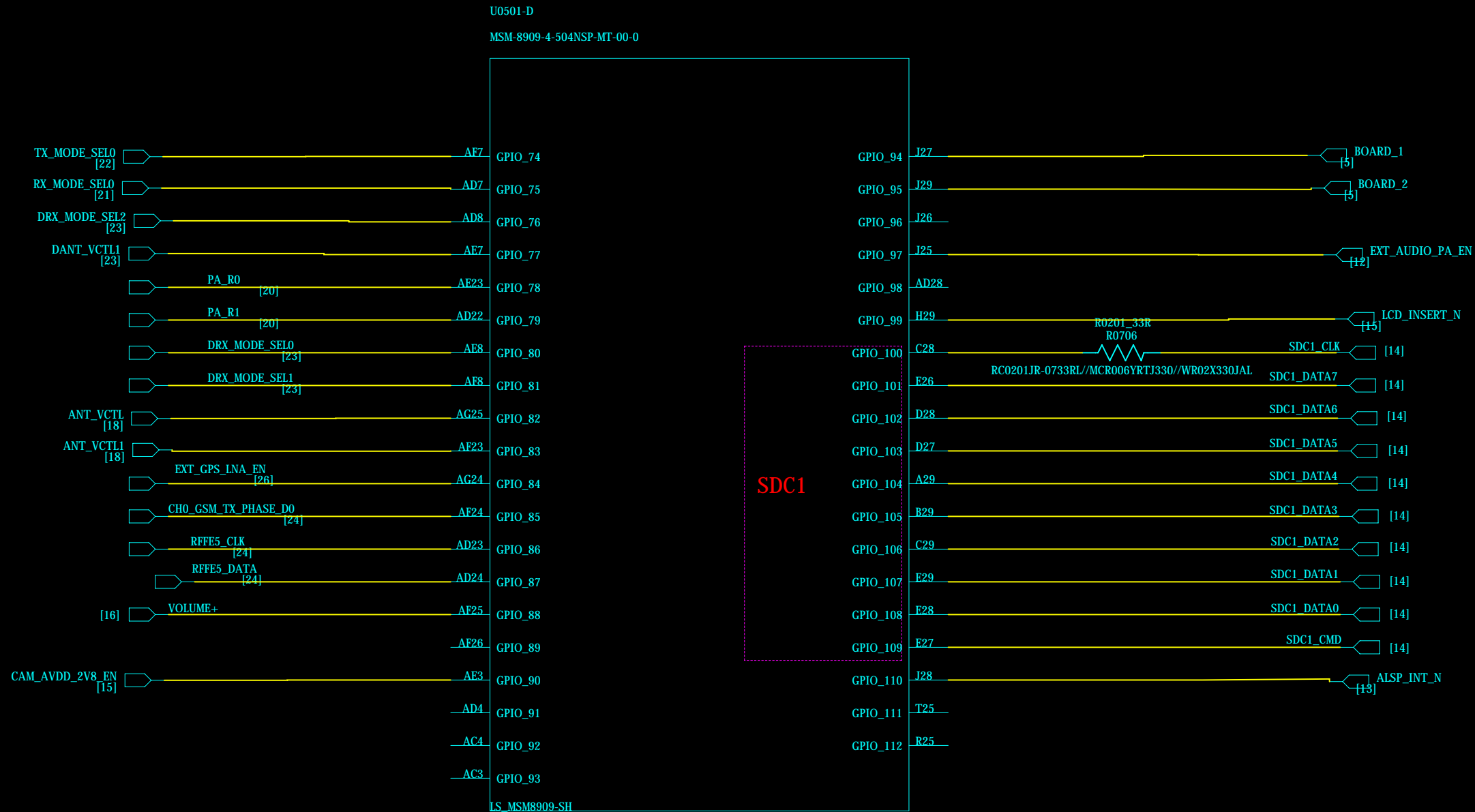
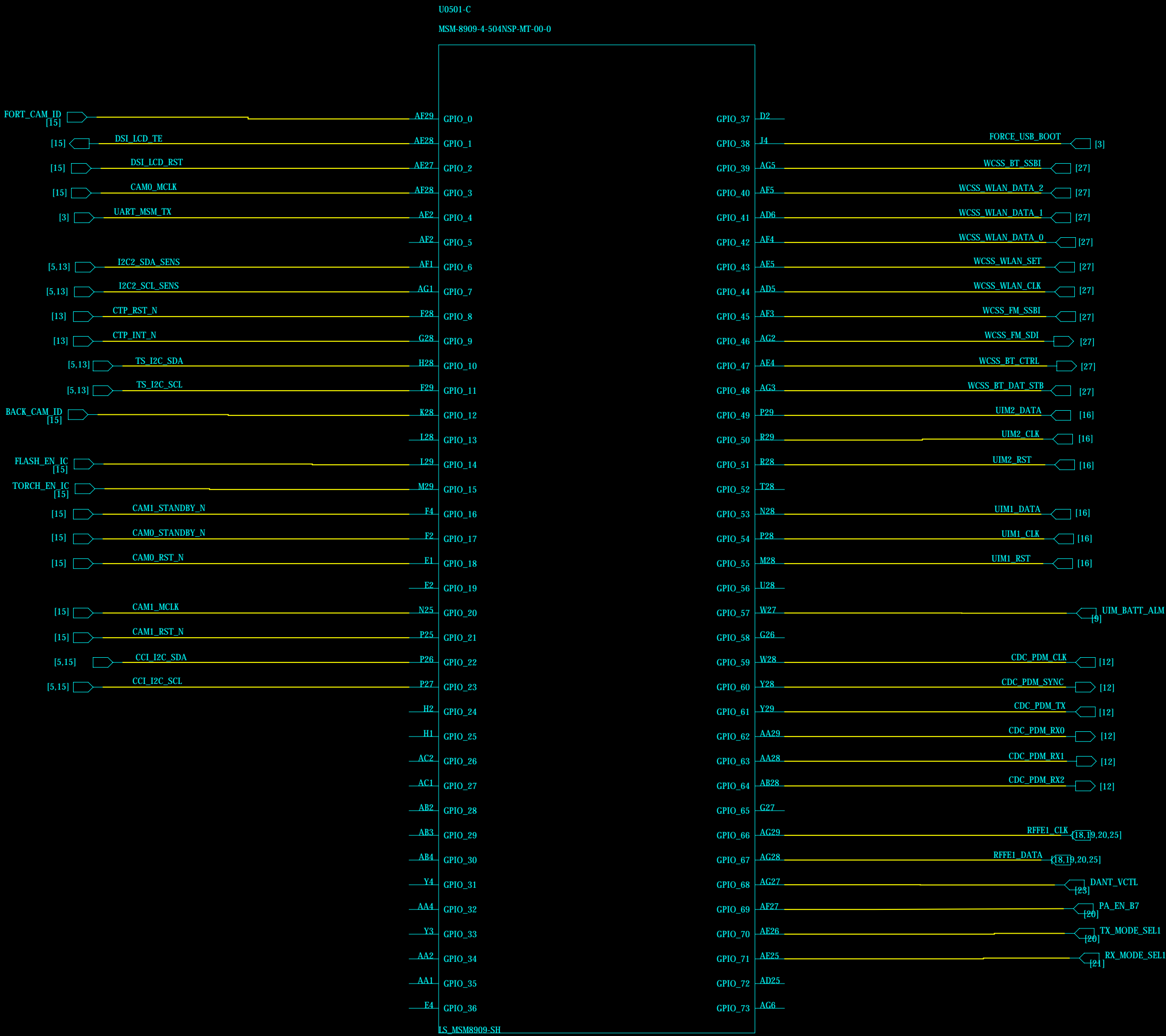


MSM8909_GPIO

BOARD IDÅaÖÄ±i						
	R0720	R0721	R0722	R0723	GPIO68	GPIO69
Å·ÖpμΨζ~f~L01f°	NC	SMT	NC	SMT	0	0
Å·ÐÄμΨζ~f~L02f°	NC	SMT	SMT	NC	0	1
Å·ÄÄμΨζ~f~L03f°	SMT	NC	NC	SMT	1	0
Å·ÄÄÊ<ζ~f~L23f°	SMT	NC	SMT	NC	1	1

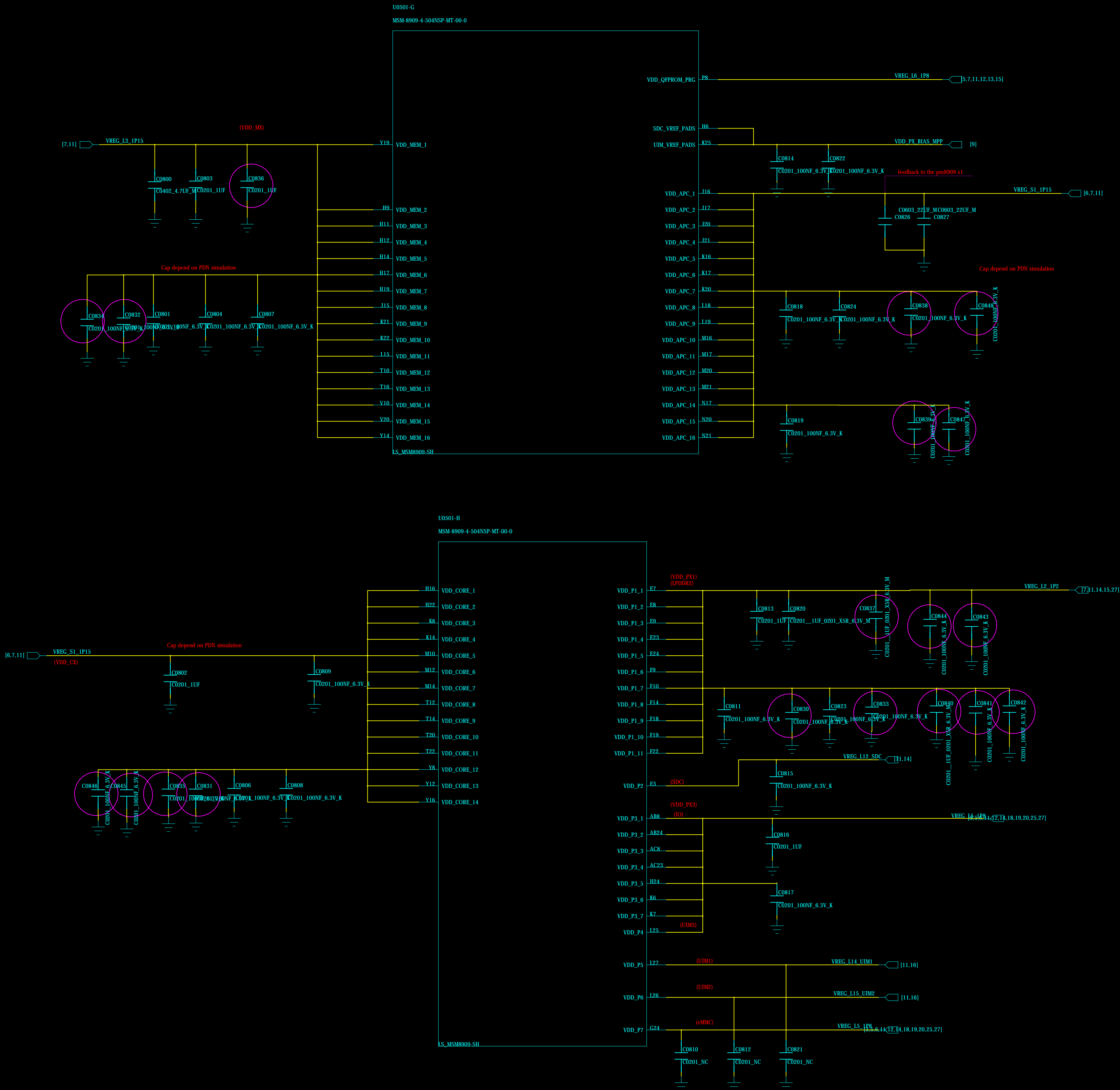


GPIOs	BOOT_CONFIG	BOOT_CONFIG[3:1]	BOOT_CONFIG
GPIO_76	BOOT_CONFIG[0], WDOG	0b000	Nand -> SD @ SD2 -> USB2.0
GPIO_77	BOOT_CONFIG[1]	0b001	SD @ SD2 -> USB2.0
GPIO_78	BOOT_CONFIG[2]	0b010	EMMC
GPIO_79	BOOT_CONFIG[3]	0b011	USB2.0

Configuration GPIO Table			BOOT_SEL	BOOT MODE
GPIO_80	WDOG_DISABLE	BOOT_CONFIG_0	0b0000 (Default)	eMMC->SDC2->USB
GPIO_81	BOOT_SEL_1	BOOT_CONFIG_1	0b0001	SDC2->eMMC->USB
GPIO_82	BOOT_SEL_2	BOOT_CONFIG_2	0b0010	eMMC->USB
GPIO_83	BOOT_SEL_3	BOOT_CONFIG_3	0b0011	USB-HS only
GPIO_84		BOOT_CONFIG_4		
GPIO_86		BOOT_CONFIG_5		
GPIO_87		BOOT_CONFIG_6		
GPIO_88		BOOT_CONFIG_7		
GPIO_90		BOOT_CONFIG_8		
GPIO_37	FORCE_USB_BOOT			

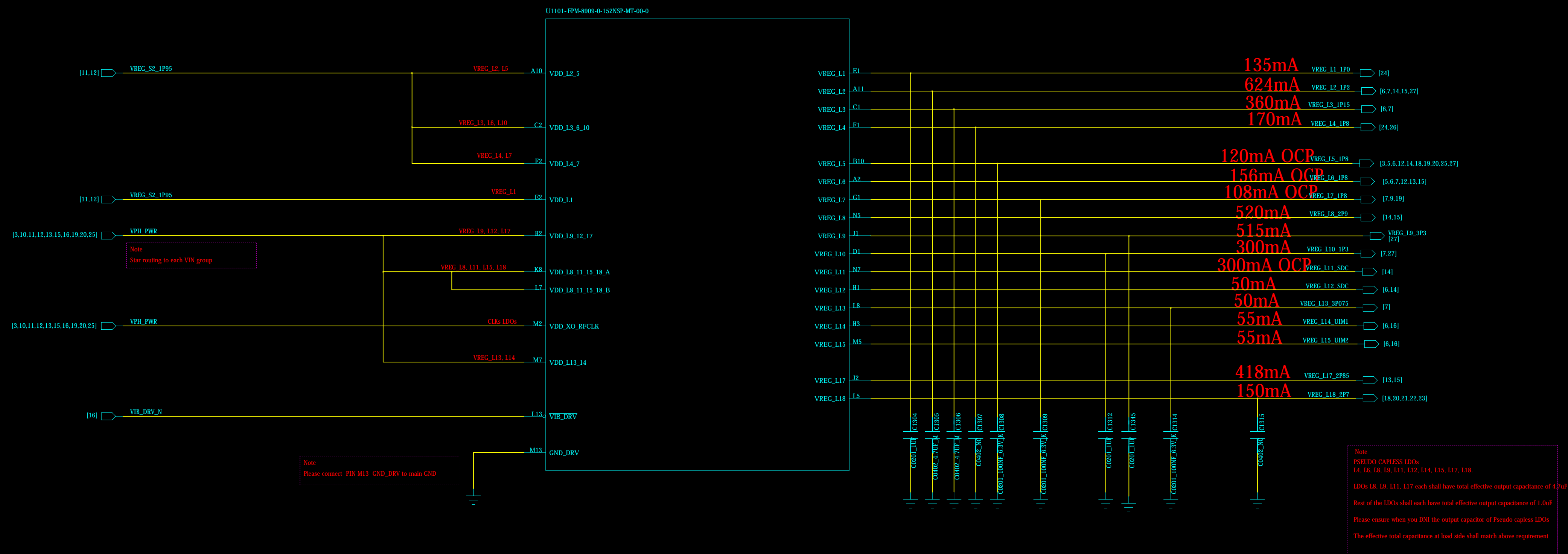
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MSM8909_POWER_1



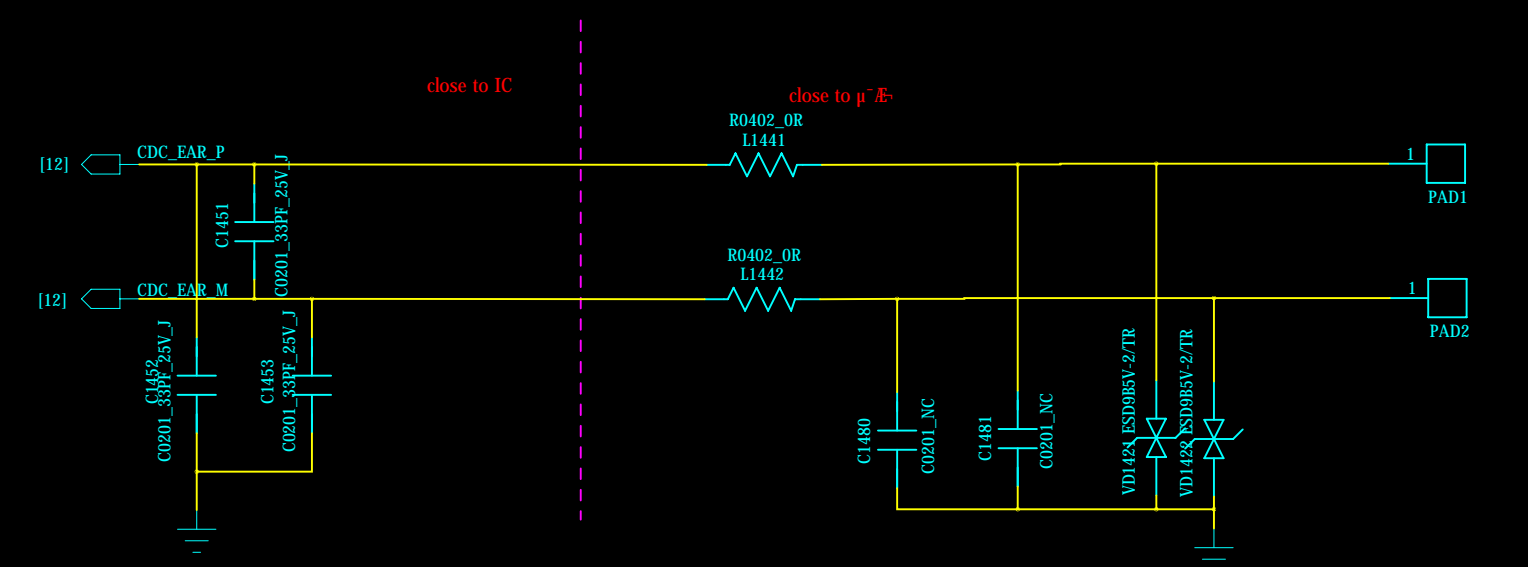
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PM8909_POWER



AUDIO

SPK PA

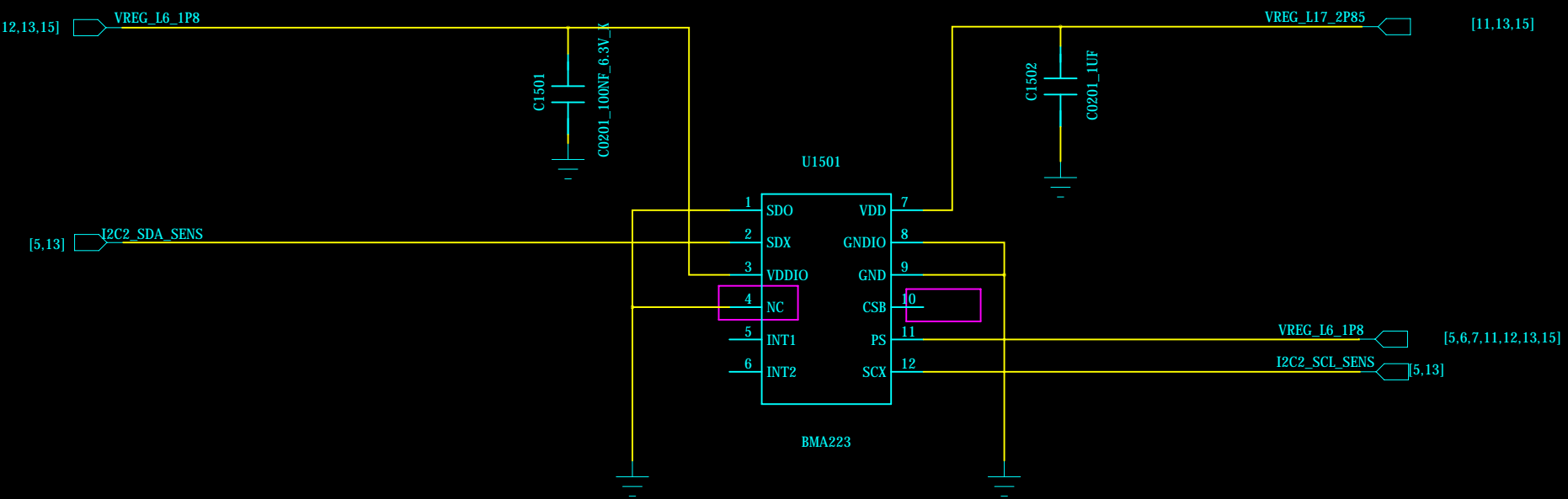


RECEIVER

MAIL MIC

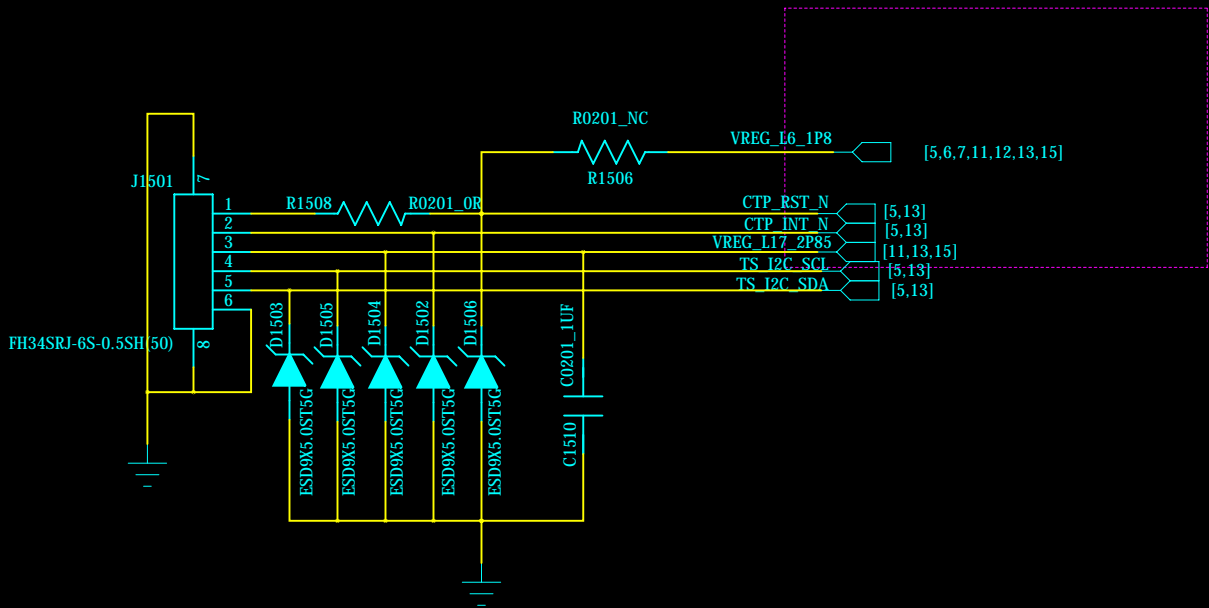
SECONED MIC

SENSORS



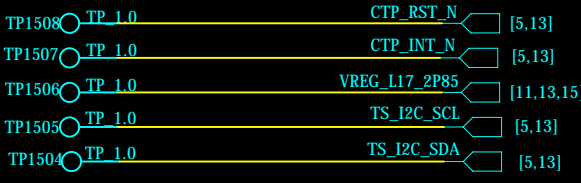
G-sensor

TP

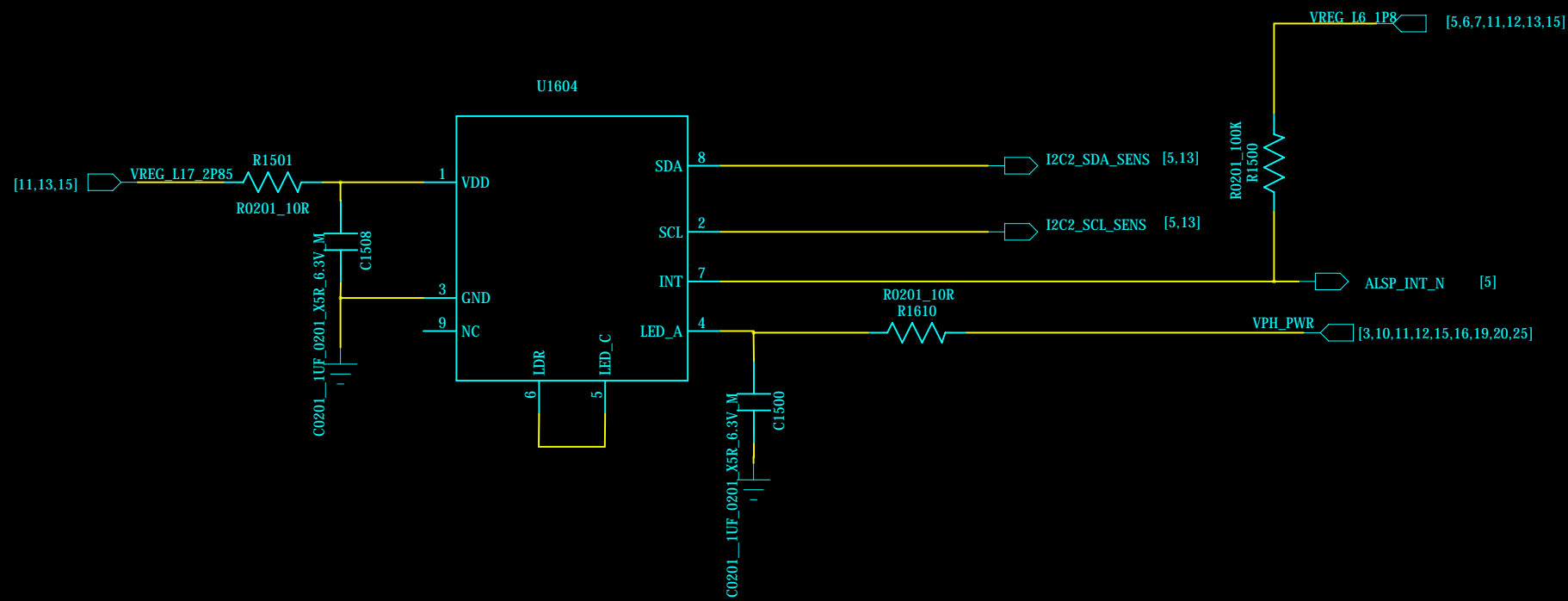


Delete 2.8/1.8 transition IO

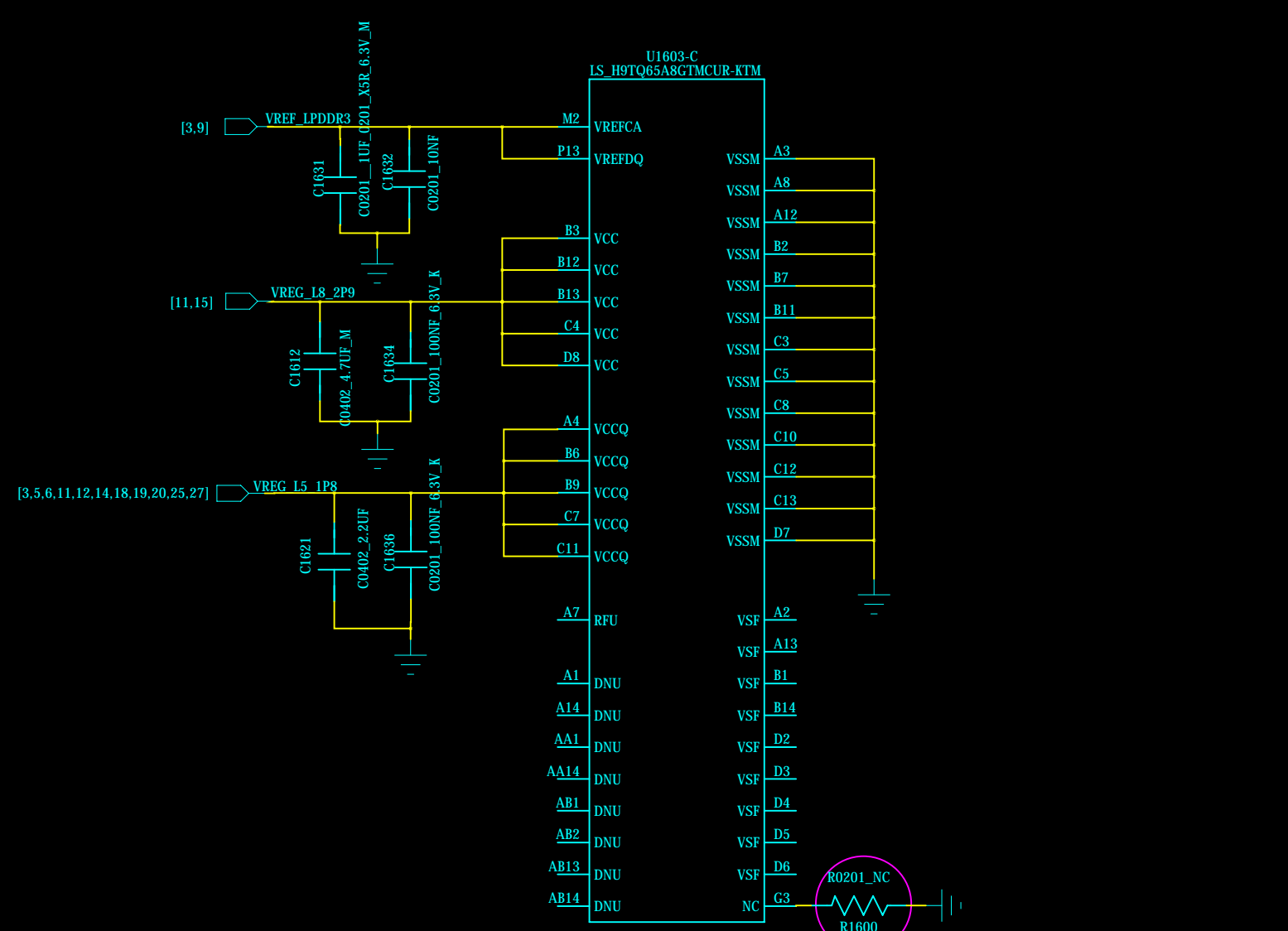
For MSG2033 or Cypress



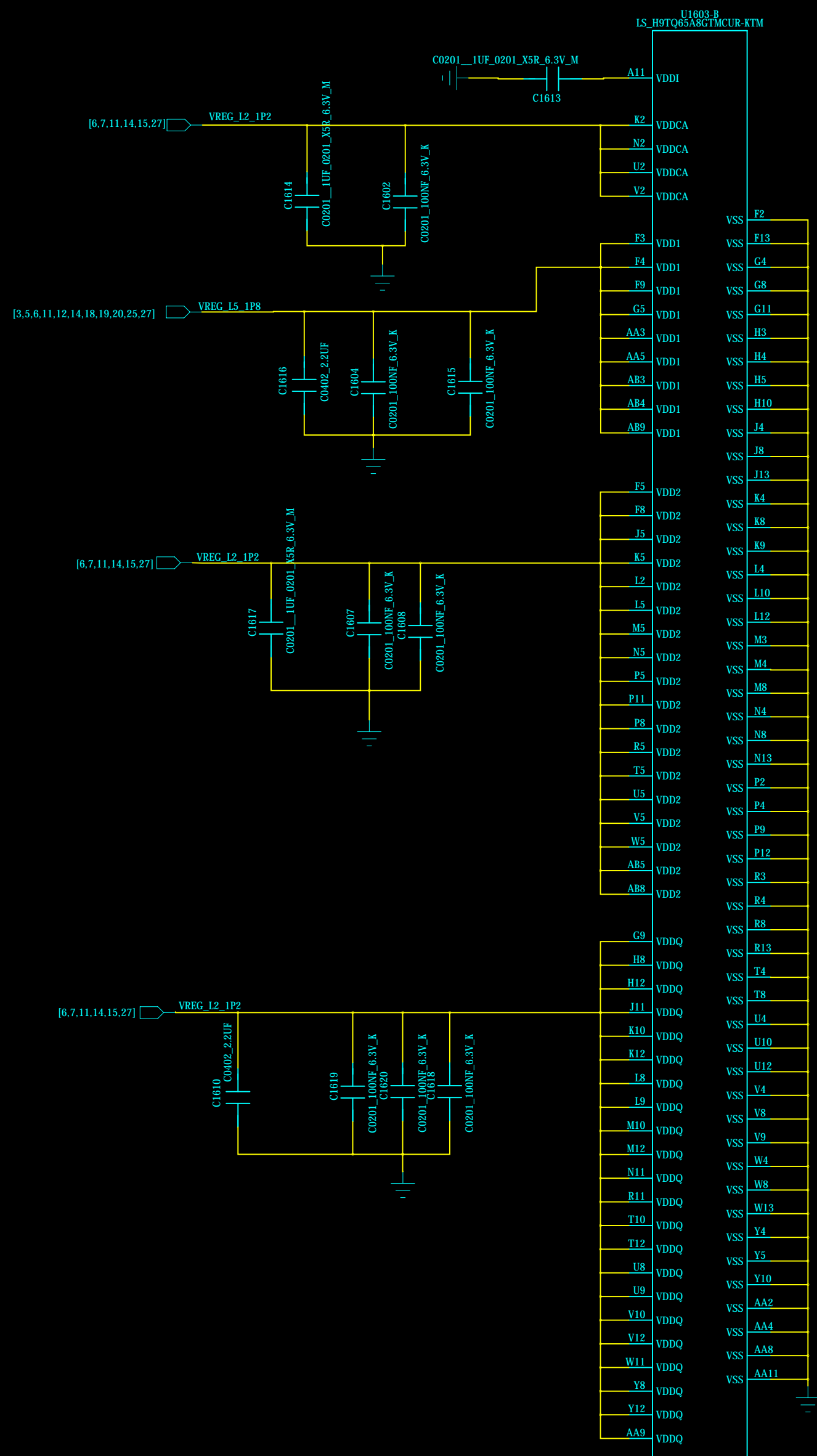
ALS+PS



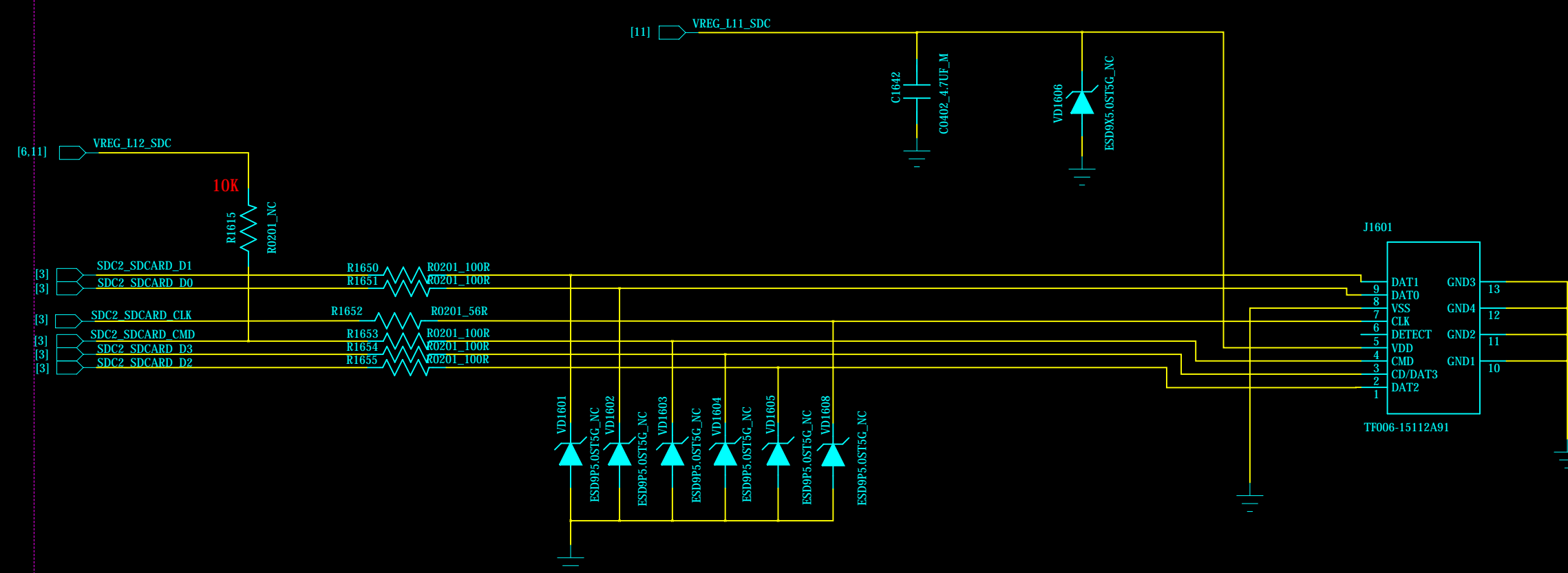
MEMORY



DDR3 check pass

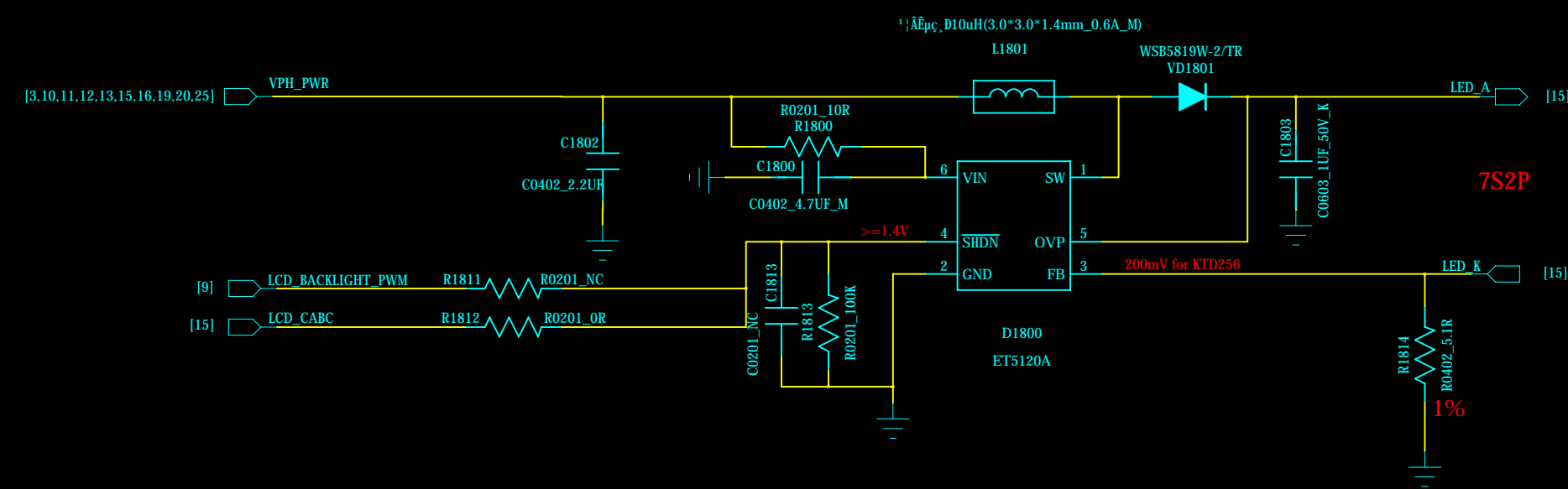


SD

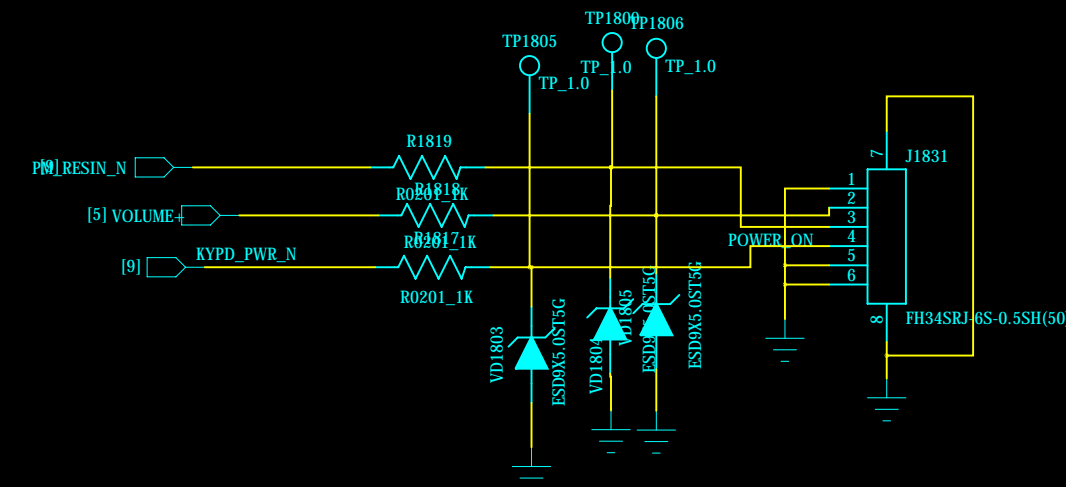


INTERFACE

LCD BACKLIGHT_DRIVER

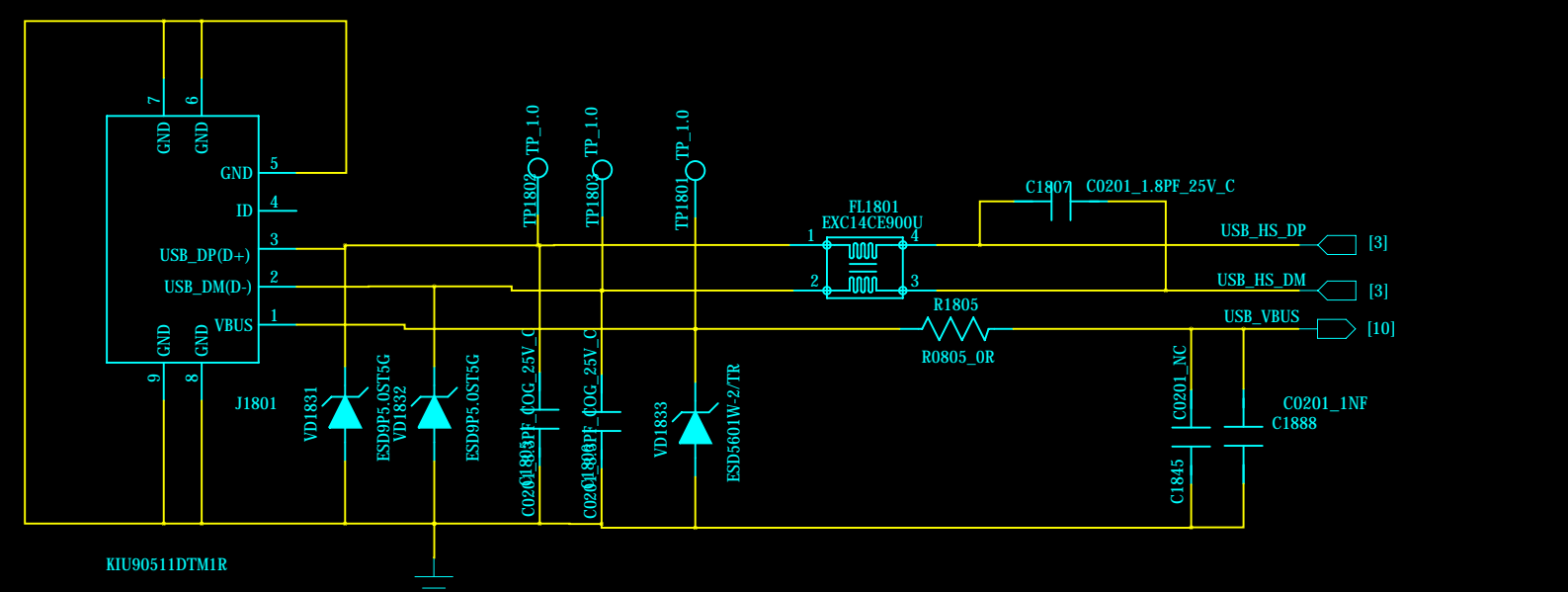


SIDE KEYS

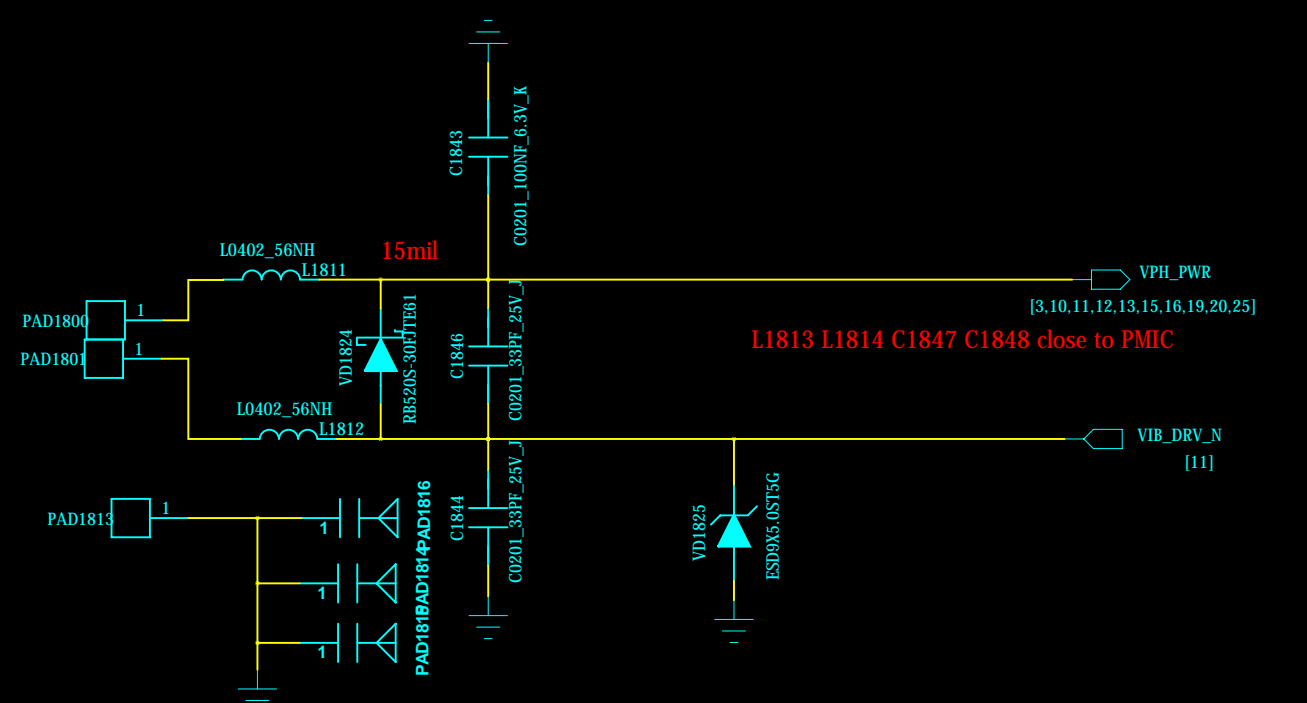


BAT CON

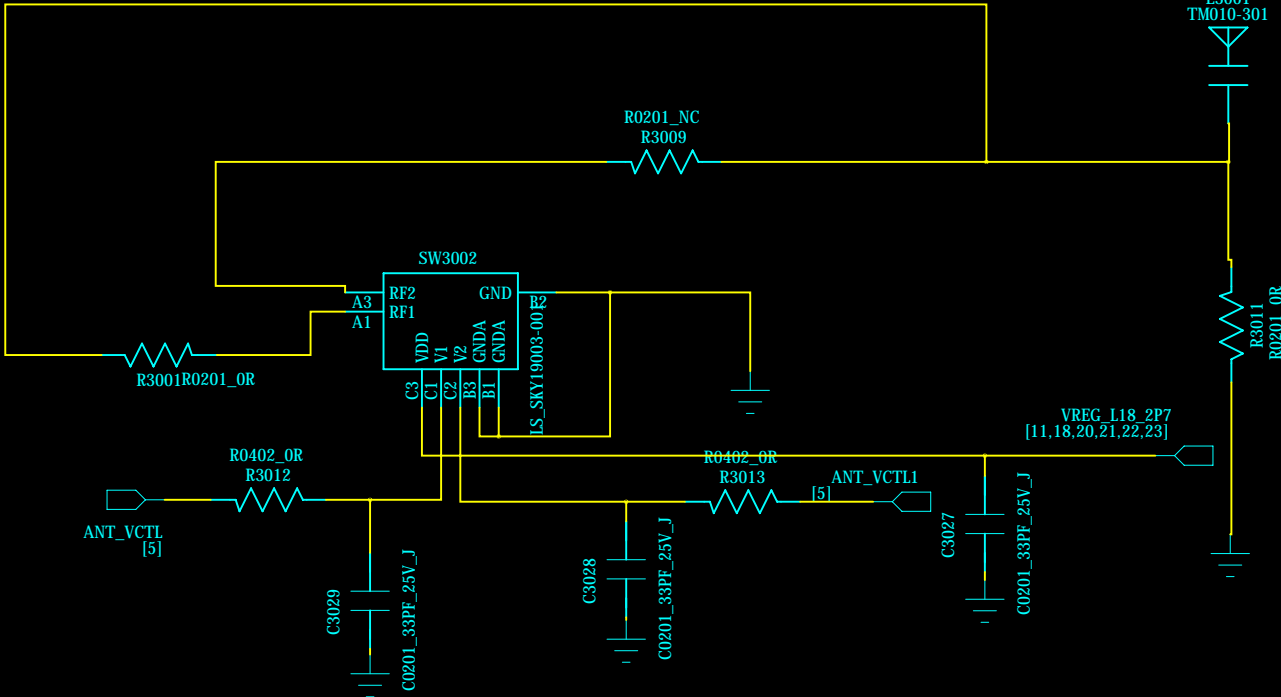
USB



MOTOR



PRIMARY_ANTENNA_SWITCH





PA_THERMAL

LTE B7 B28

B7

B28A

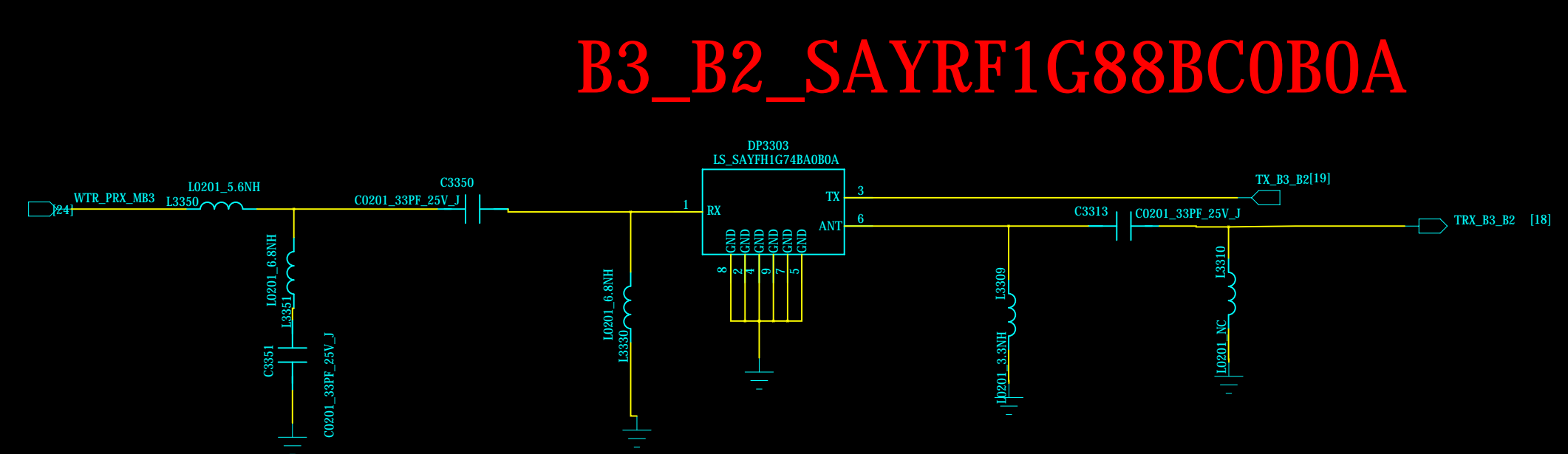
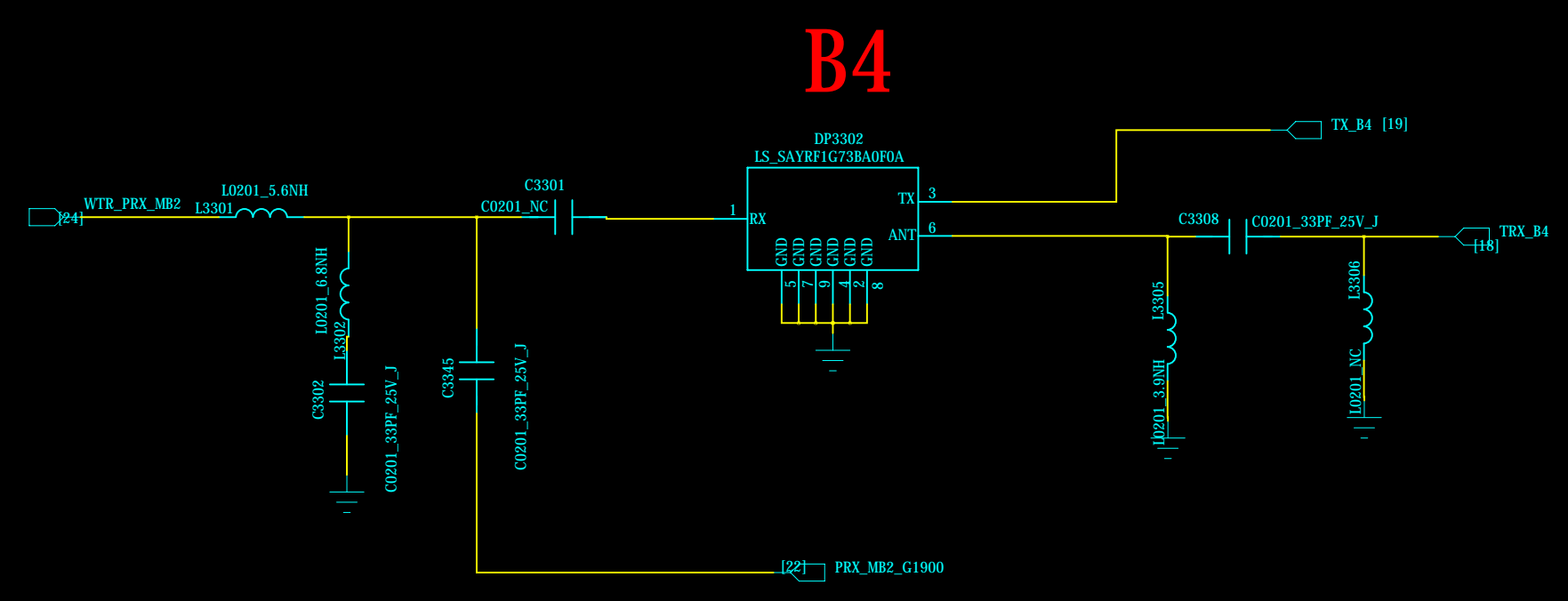
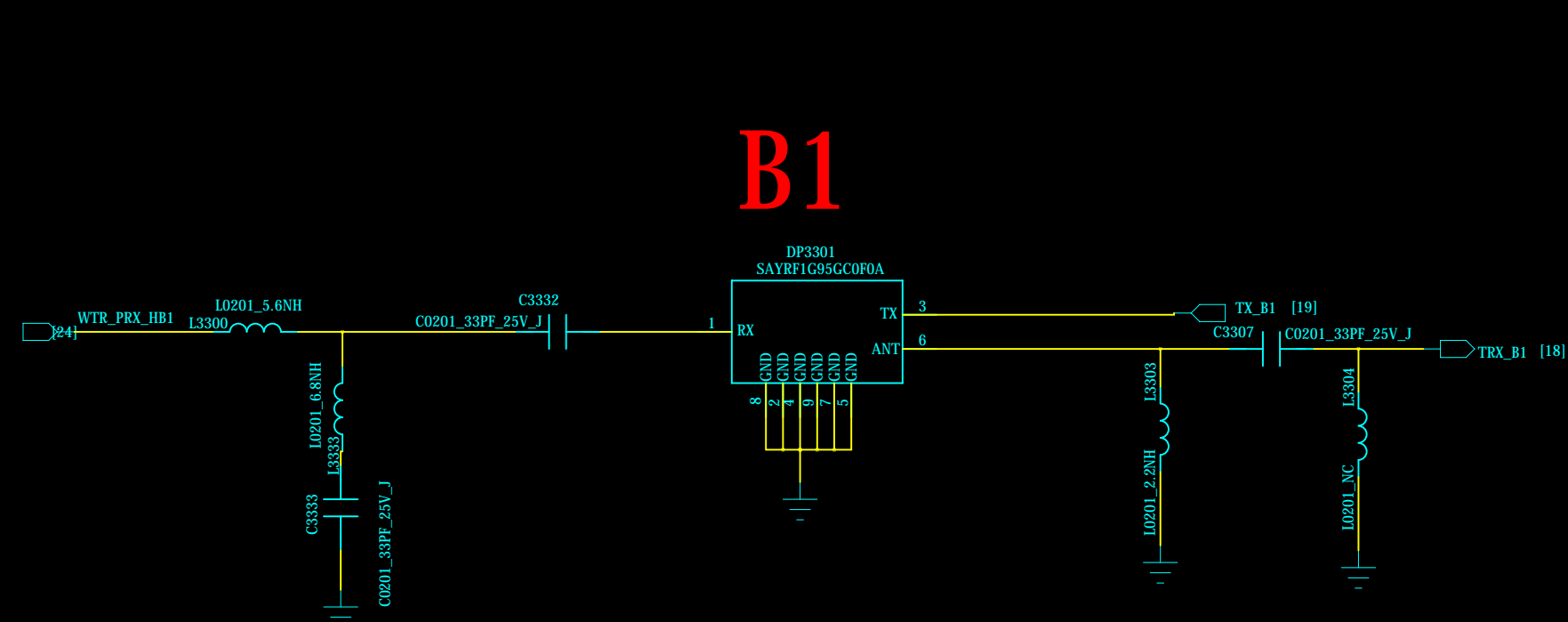
B28B

NOTE:

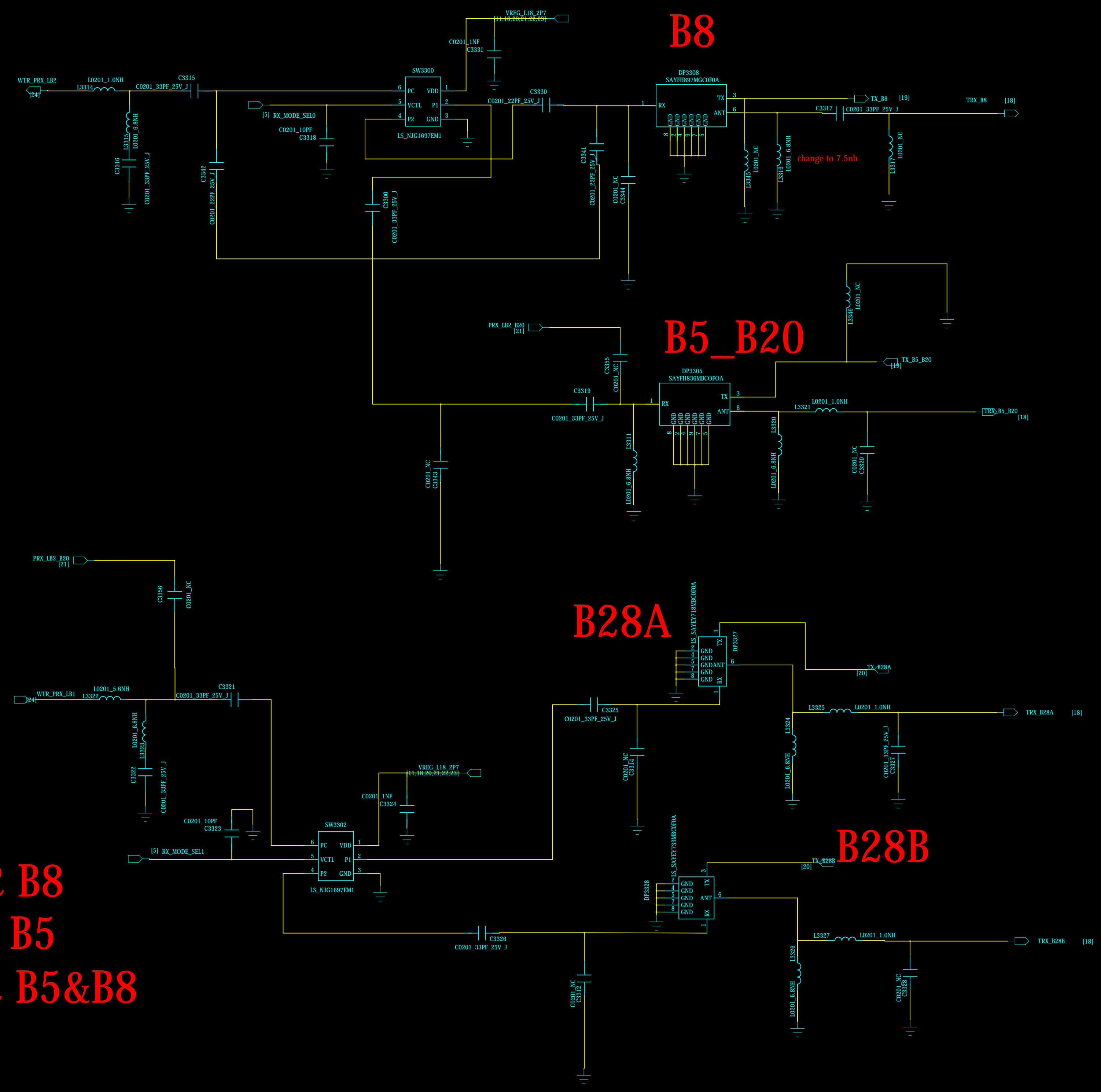
Euro: No B28 Band

Lati:support B28

Aust:support B28



NOTE:
Euro: PRX_LB1 FOR B20 PRX_LB2 FOR B8
Lati: PRX_LB1 FOR B28 PRX_LB2 FOR B5
Aust: PRX_LB1 FOR B28 PRX_LB2 FOR B5&B8



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