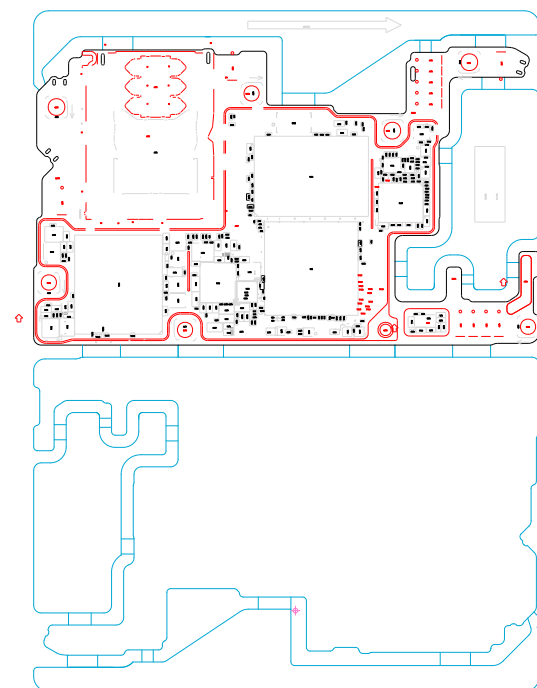
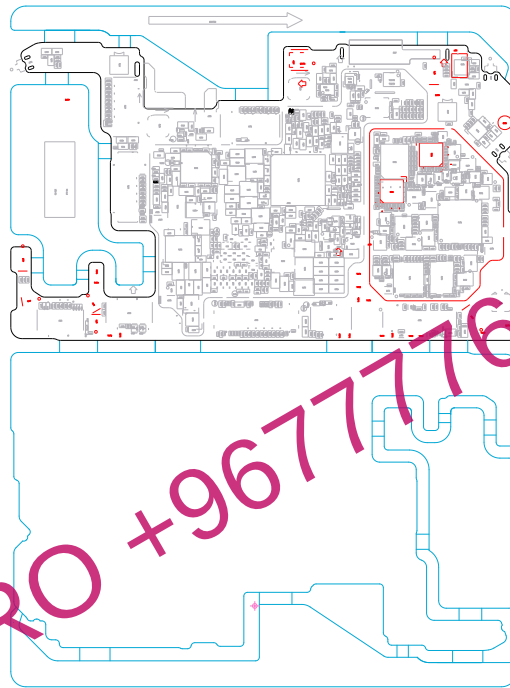


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123456

Baseband Schematic

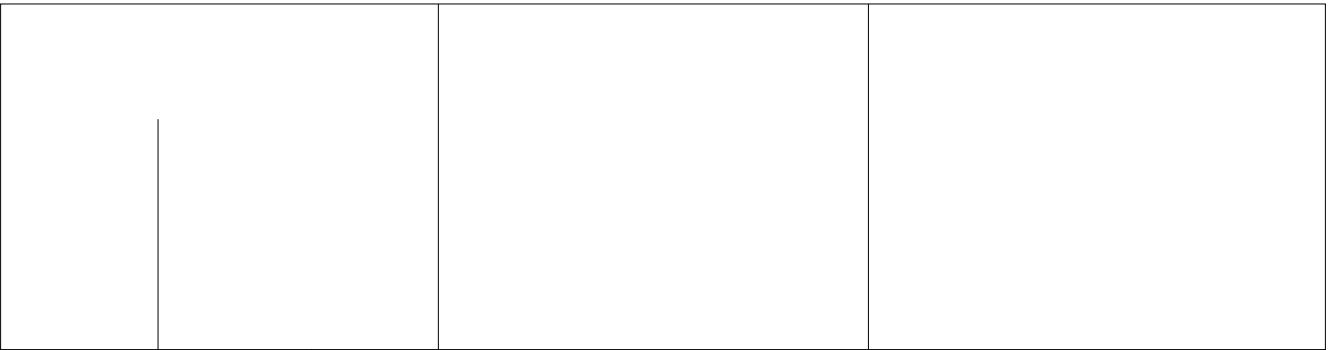
- A
- 1. Connects
 - 2. Block Diagramm
 - 3. SOC PWR1
 - 4. SOC PWR2
 - 5. SOC PWR3
 - 6. SOC HS Part
 - 7. SOC GPIO Part
 - 8. SOC RF Part
 - 9. SOC GND
 - 10. Hi6555_LDO
 - 11. Hi6555_BUCK
 - 12. Hi6555_Digital
 - 13. Hi6555_Codec/Hi6422V320
 - 14. Hi6422V320 reserved
 - 15. LPDDR4x
 - 16. UFS
 - 17. Hi6405_CODEEC
 - 18. BATTERY
 - 19. TP/LCD
 - 20. TP/LCD Reserve
 - 21. Flash and Camera Power
 - 22. Front CAM/MIPI switch
 - 23. RCAM Interface
 - 24. CAM Reserve
 - 25. Charger management
 - 26. Direct Charger
 - 27. Wireless Charger
 - 28. USB/TYPE_C interface
 - 29. Interface Reserve
 - 30. MIC/RCV/VIB/HAC
 - 31. Headphone/HIFI
- B
- C
- D

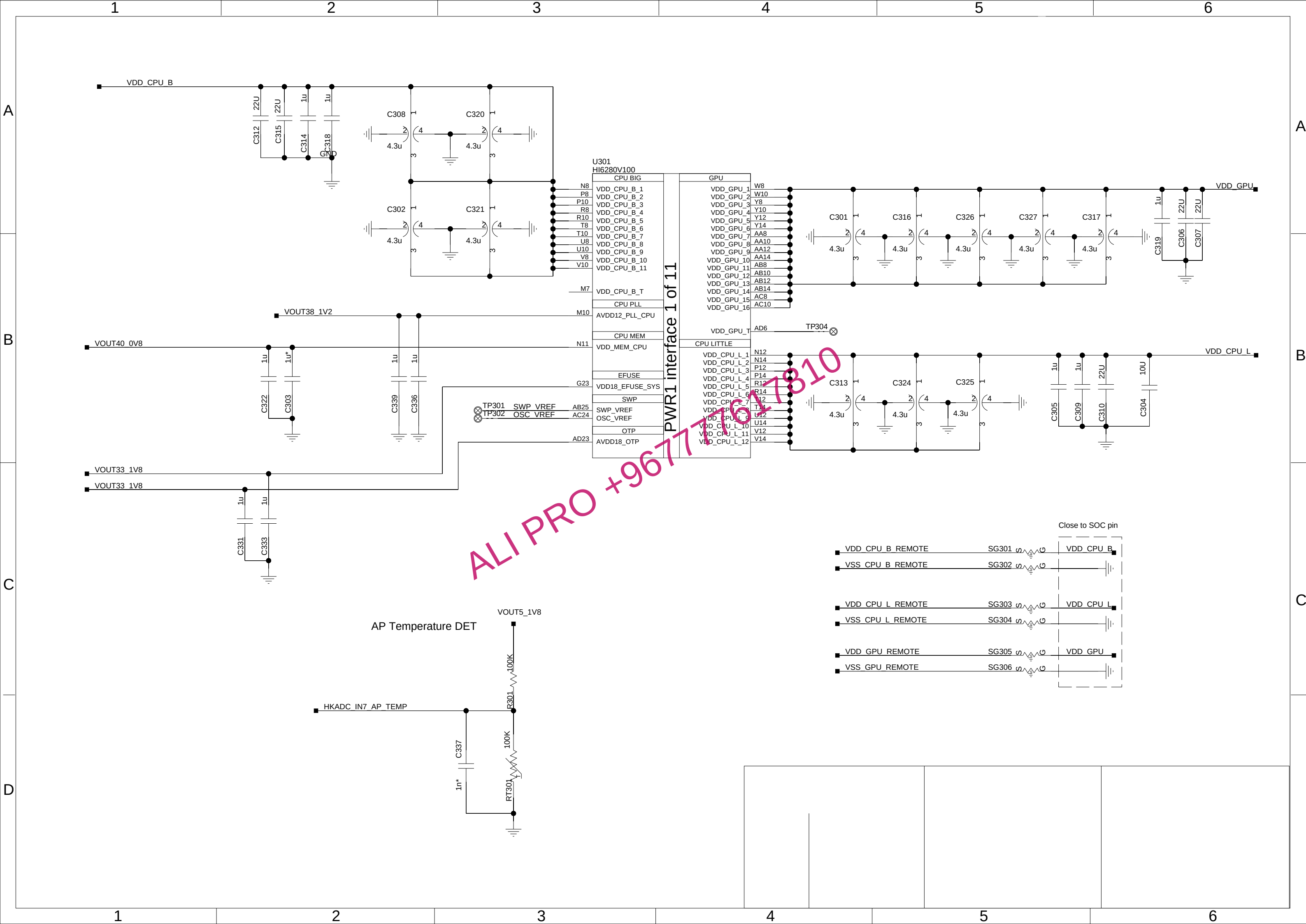
- 32. Smart PA
- 33. Smart PA Reserve
- 34. Sensor
- 35. SIM/SD Card
- 36. FPC1
- 37. FPC2
- 38. LED/KEY
- 39. NFC_BB
- 40. Test point/shields

RF Schematic

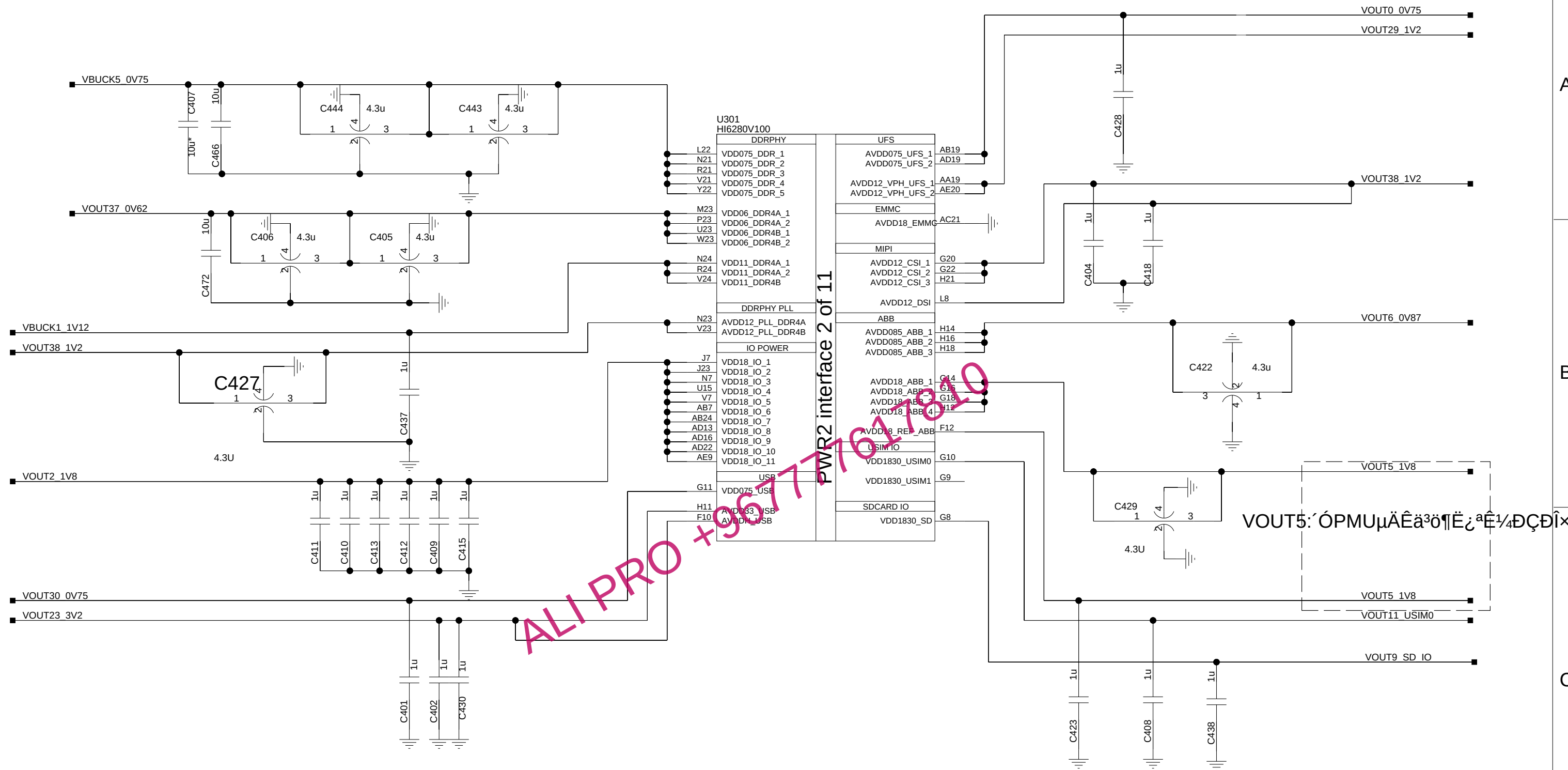
41-84. RF

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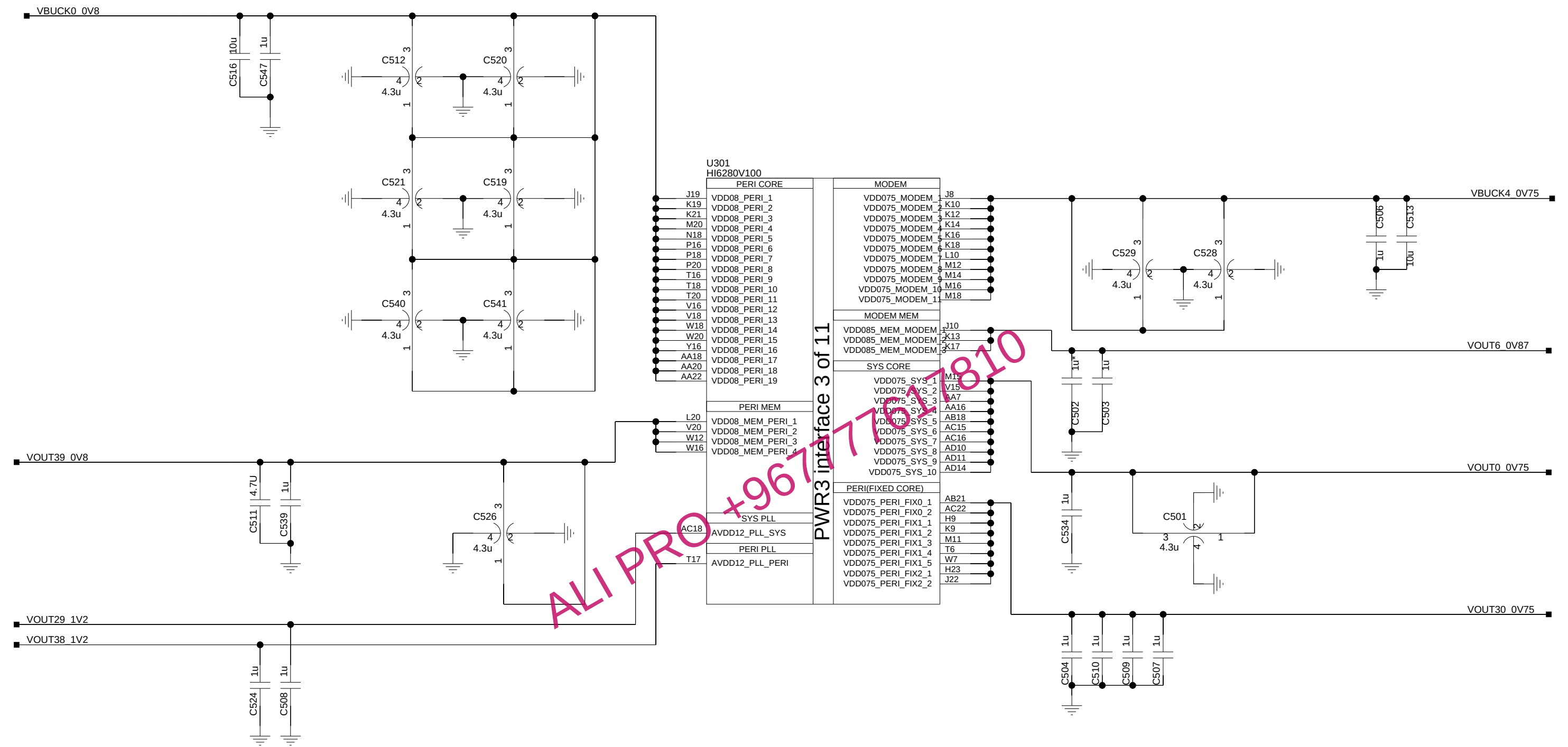




4. SOC PWR2



5. SOC PWR3



[illegible]

7. SOC GPIO Part

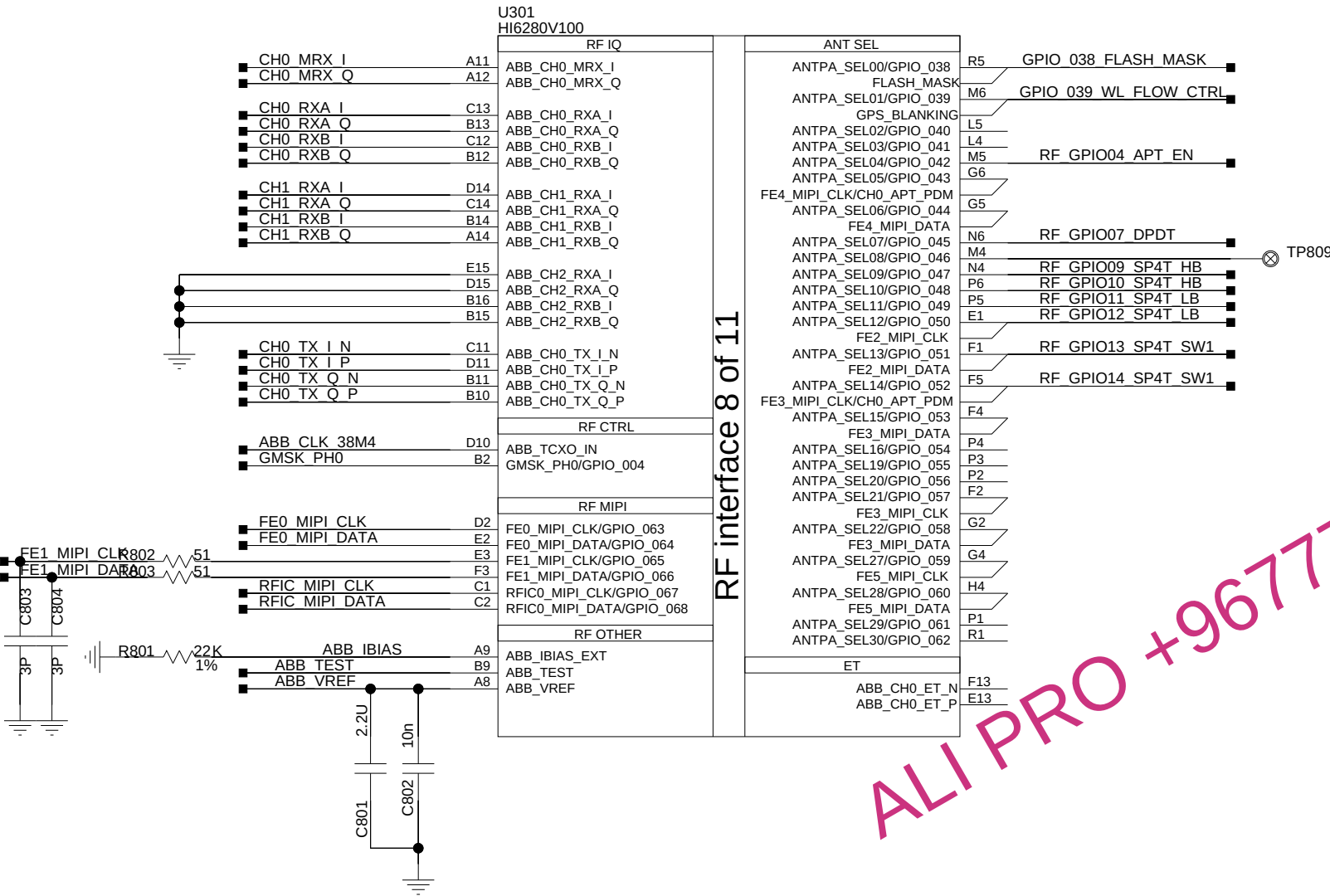
U301 HI6280V100

DS1 interface 6 of 11

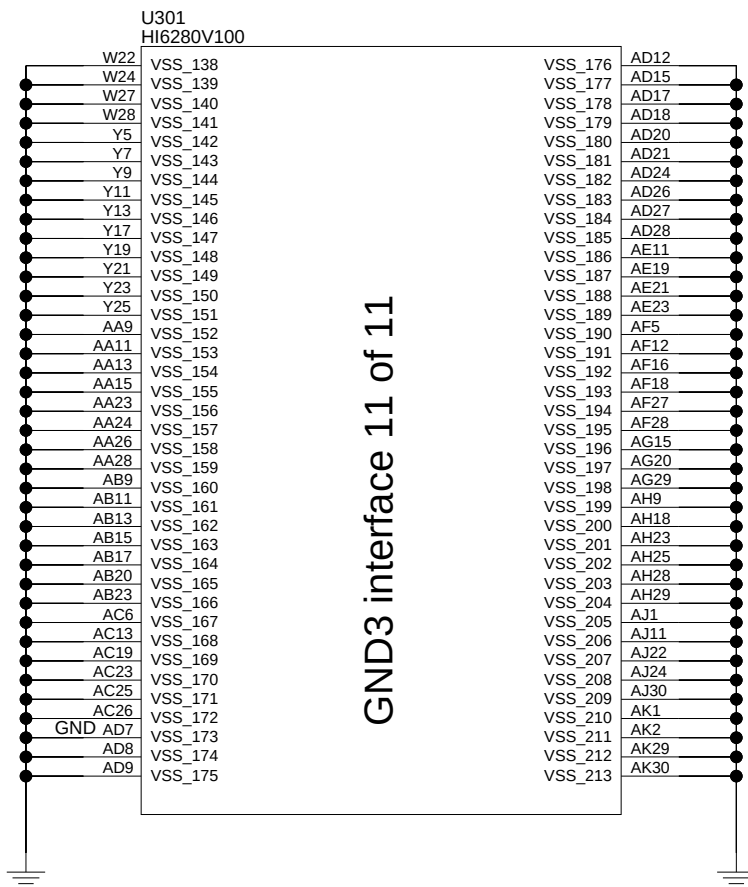
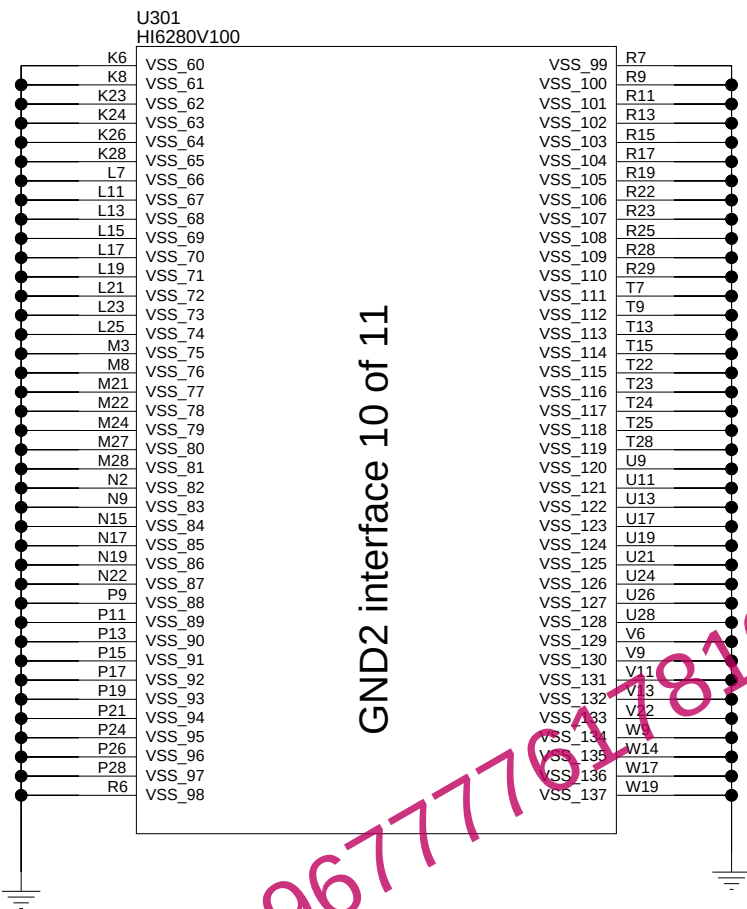
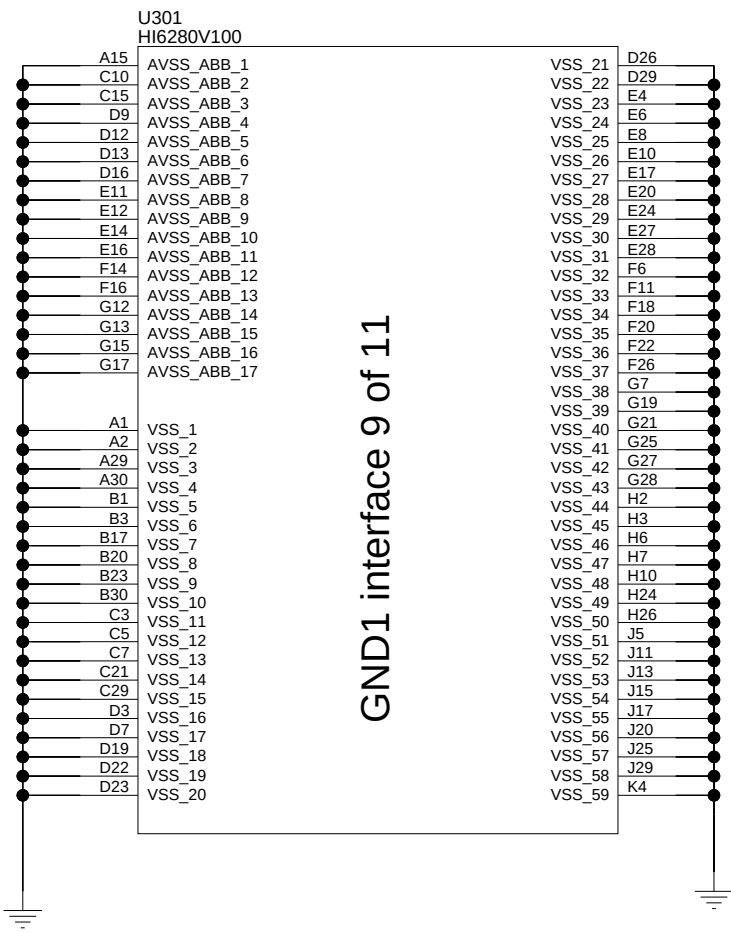
DS2 interface 7 of 11

GPIO_037_DDR_TYPE_SEL=0-EMCP
GPIO_037_DDR_TYPE_SEL=1-ÔÃ±DDR

8. SOC RF Part

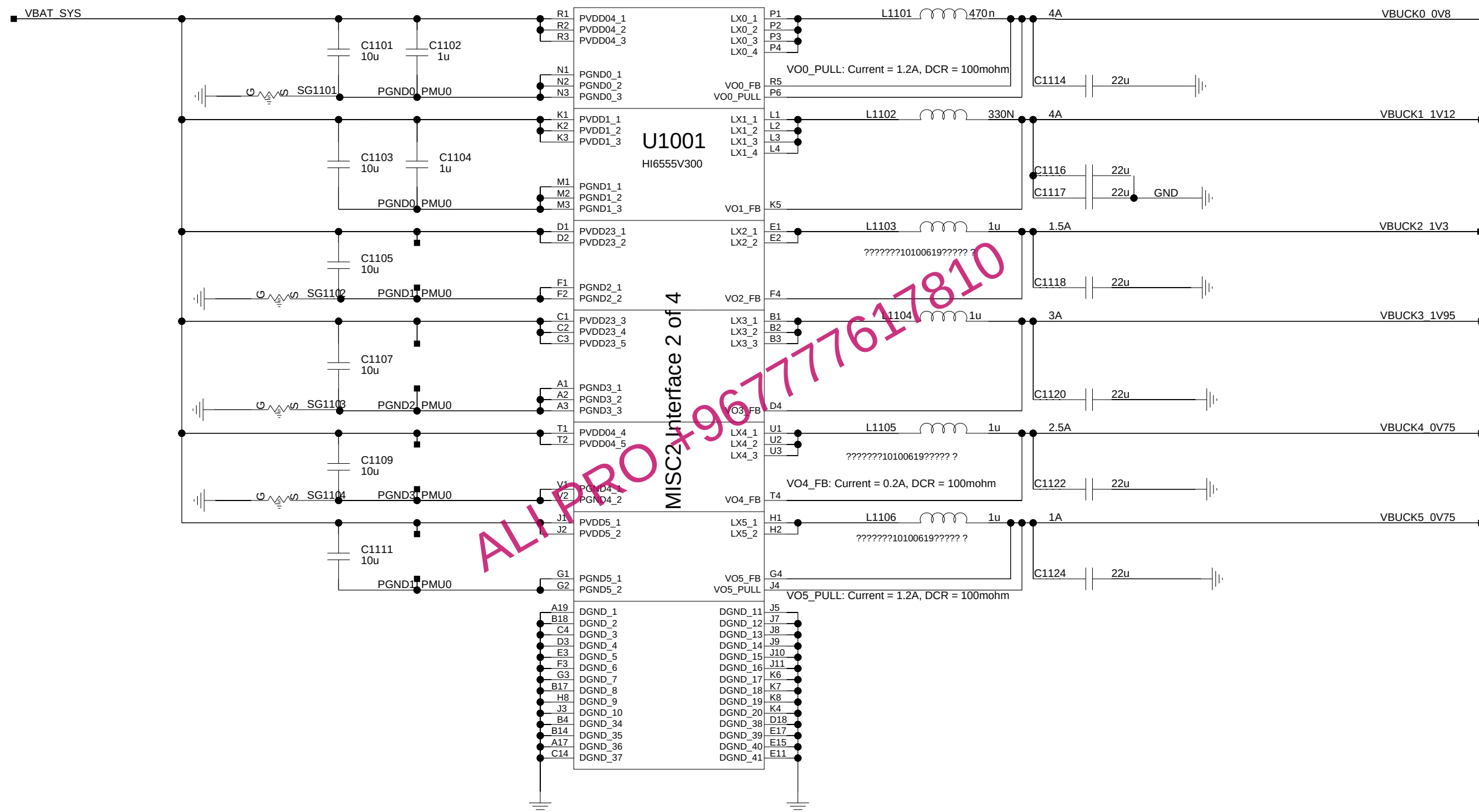


9. SOC GND

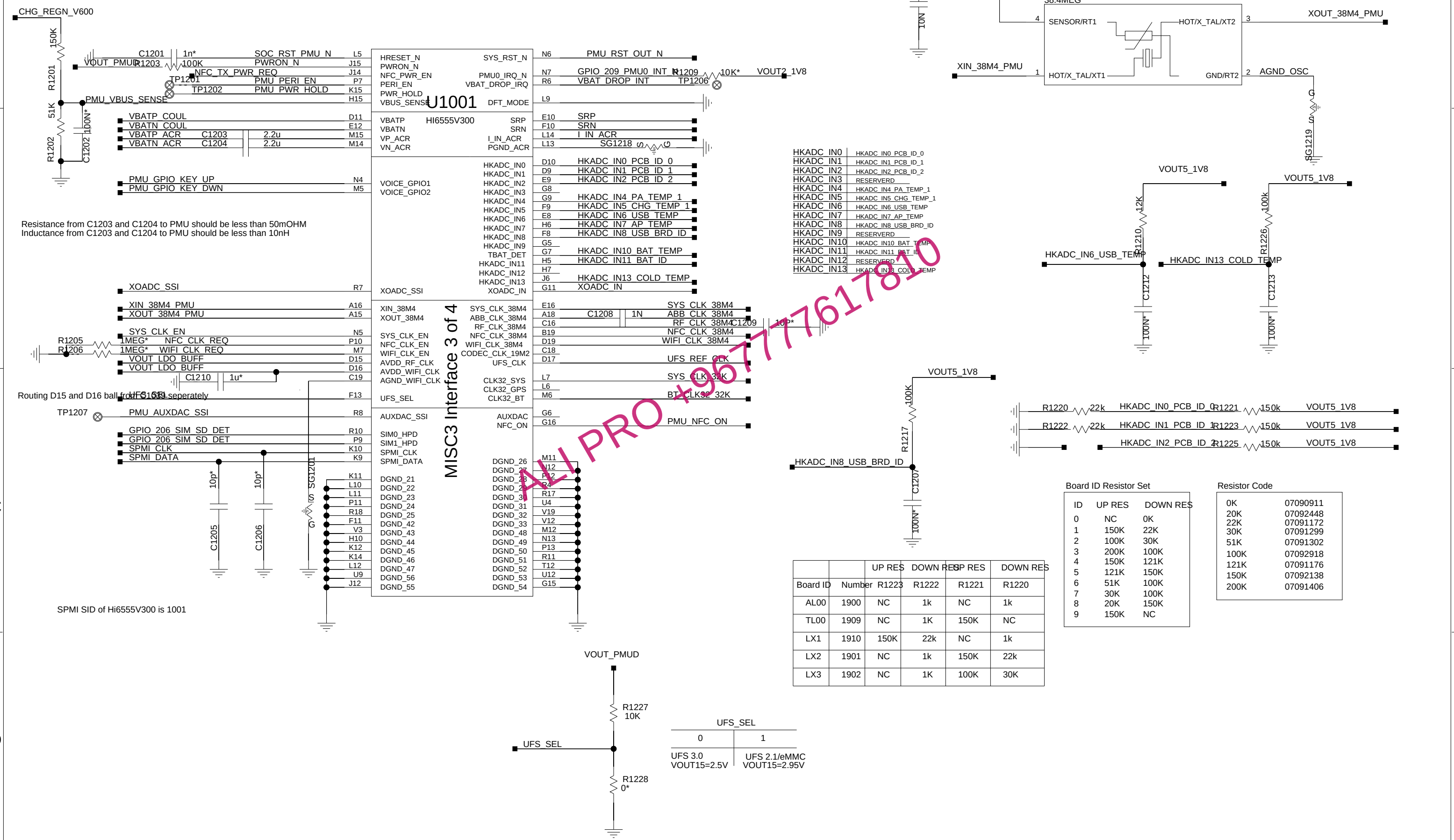


10. Hi6555_LDO

11. Hi6555_BUCK



12. Hi6555_Digital



Resistance from C1203 and C1204 to PMU should be less than 50mOHM
Inductance from C1203 and C1204 to PMU should be less than 10nH

Routing D15 and D16 ball from C1205 separately

SPMI SID of Hi6555V300 is 1001

HKADC_IN0	HKADC_IN0_PCB_ID_0
HKADC_IN1	HKADC_IN1_PCB_ID_1
HKADC_IN2	HKADC_IN2_PCB_ID_2
HKADC_IN3	RESERVED
HKADC_IN4	HKADC_IN4_PA_TEMP_1
HKADC_IN5	HKADC_IN5_CHG_TEMP_1
HKADC_IN6	HKADC_IN6_USB_TEMP
HKADC_IN7	HKADC_IN7_AP_TEMP
HKADC_IN8	HKADC_IN8_USB_BRD_ID
HKADC_IN9	RESERVED
HKADC_IN10	HKADC_IN10_BAT_TEMP
HKADC_IN11	HKADC_IN11_BAT_ID
HKADC_IN12	RESERVED
HKADC_IN13	HKADC_IN13_COLD_TEMP

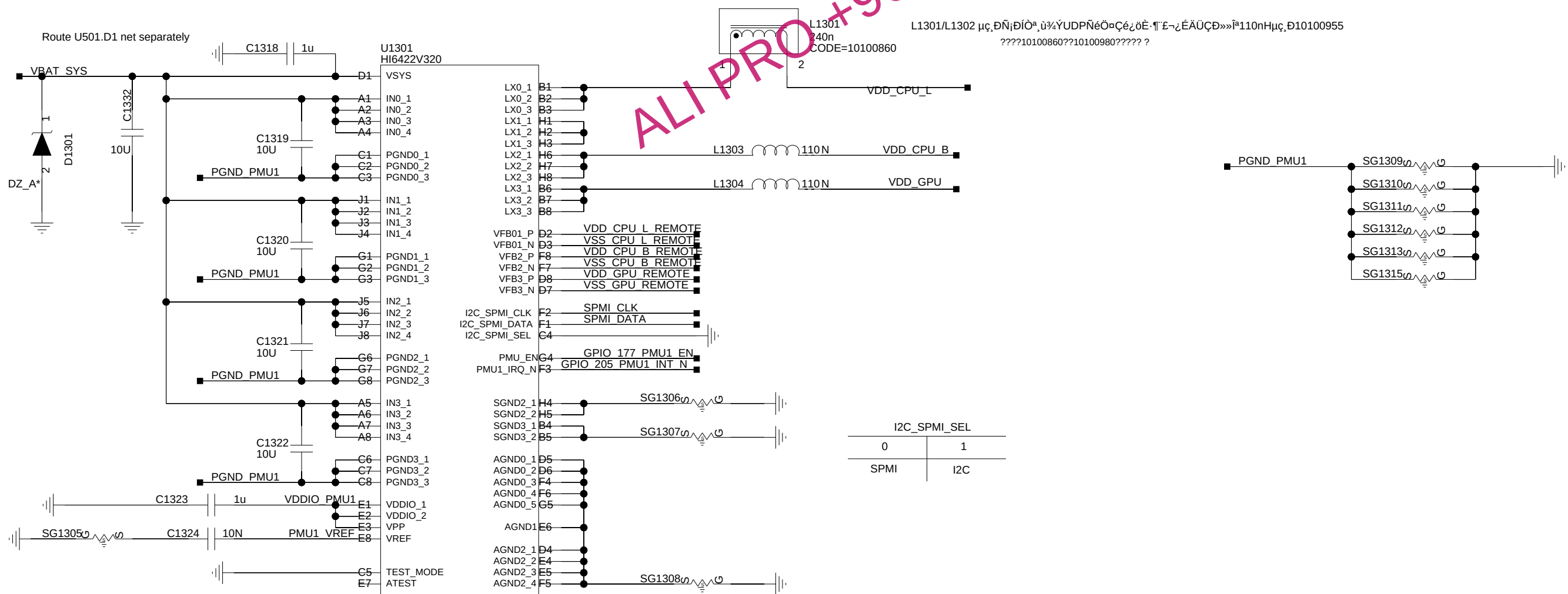
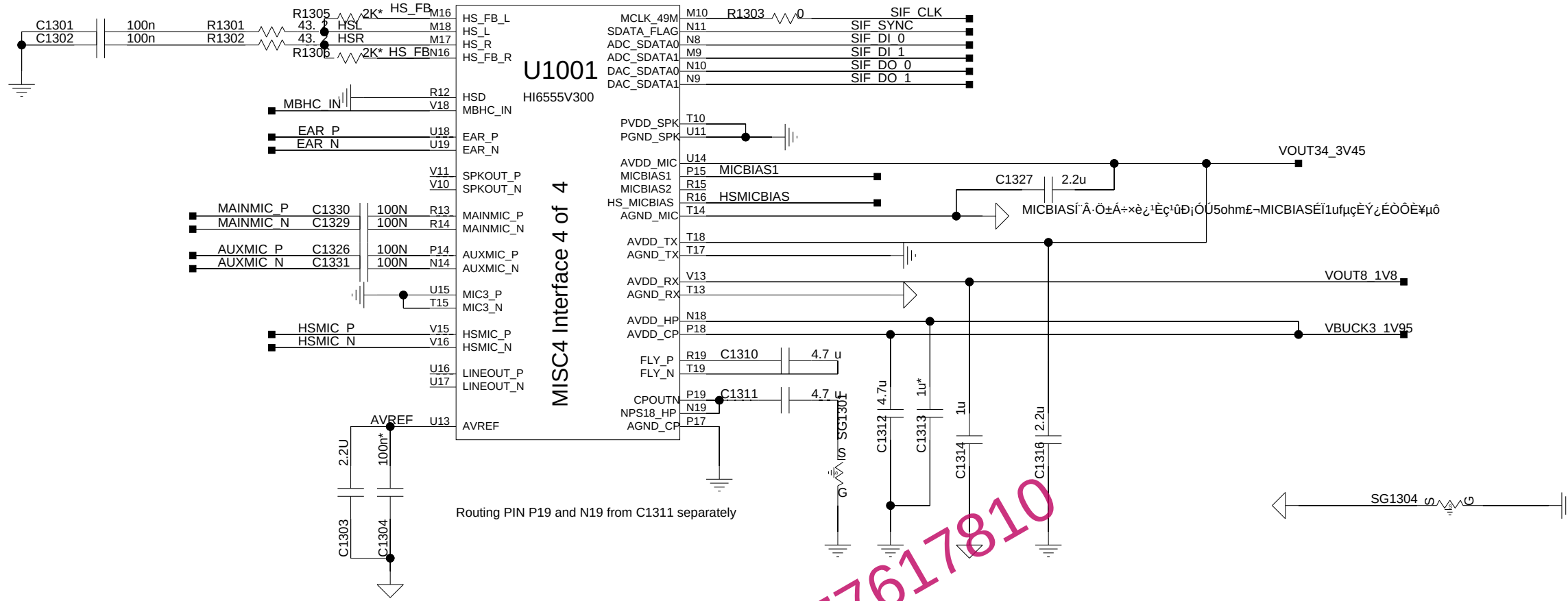
Board ID	Number	UP RES	DOWN RES	UP RES	DOWN RES
AL00	1900	NC	1k	NC	1k
TL00	1909	NC	1K	150K	NC
LX1	1910	150K	22k	NC	1k
LX2	1901	NC	1k	150K	22k
LX3	1902	NC	1K	100K	30K

ID	UP RES	DOWN RES
0	NC	0K
1	150K	22K
2	100K	30K
3	200K	100K
4	150K	121K
5	121K	150K
6	51K	100K
7	30K	100K
8	20K	150K
9	150K	NC

Resistor Code	Resistor Code
0K	07090911
20K	07092448
22K	07091172
30K	07091299
51K	07091302
100K	07092918
121K	07091176
150K	07092138
200K	07091406

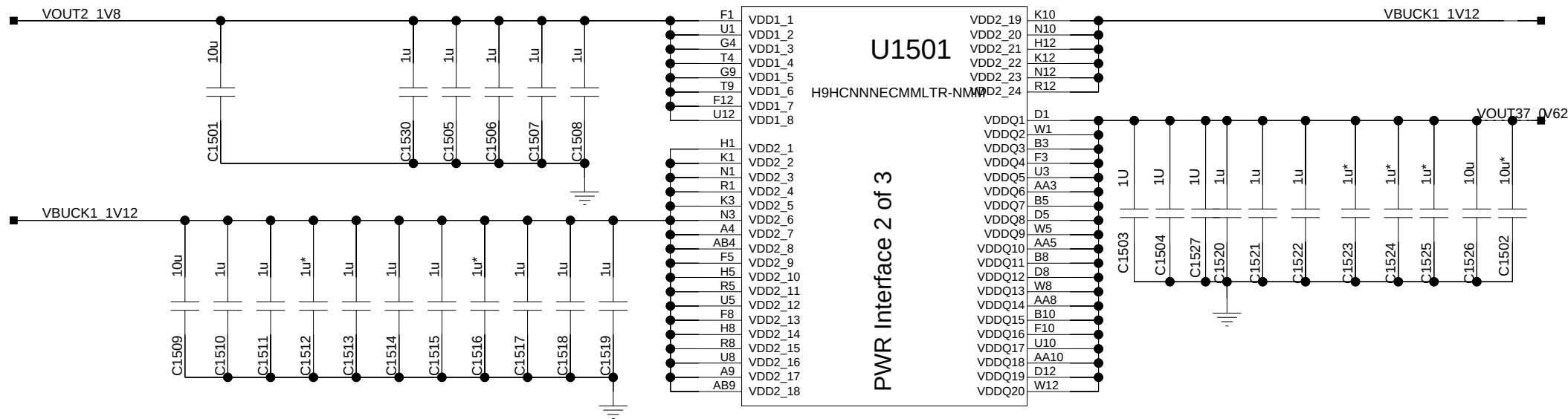
UFS_SEL	UFS_SEL
0	1
UFS 3.0 VOUT15=2.5V	UFS 2.1/eMMC VOUT15=2.95V

13. Hi6555_Codec/Hi6422

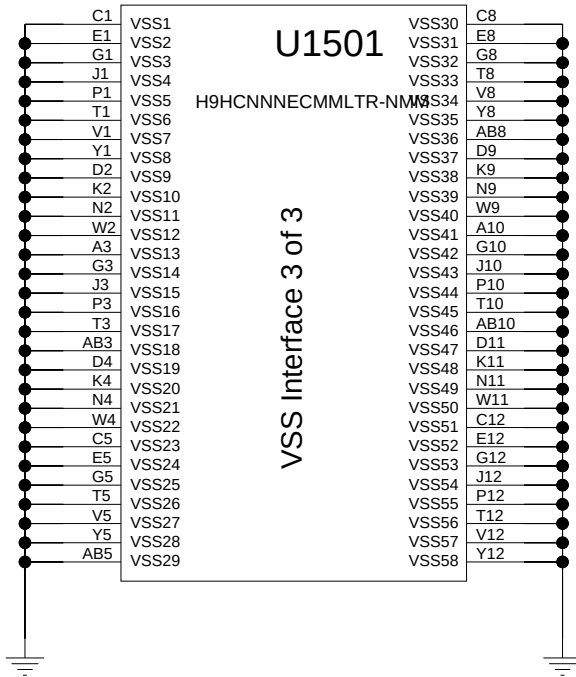
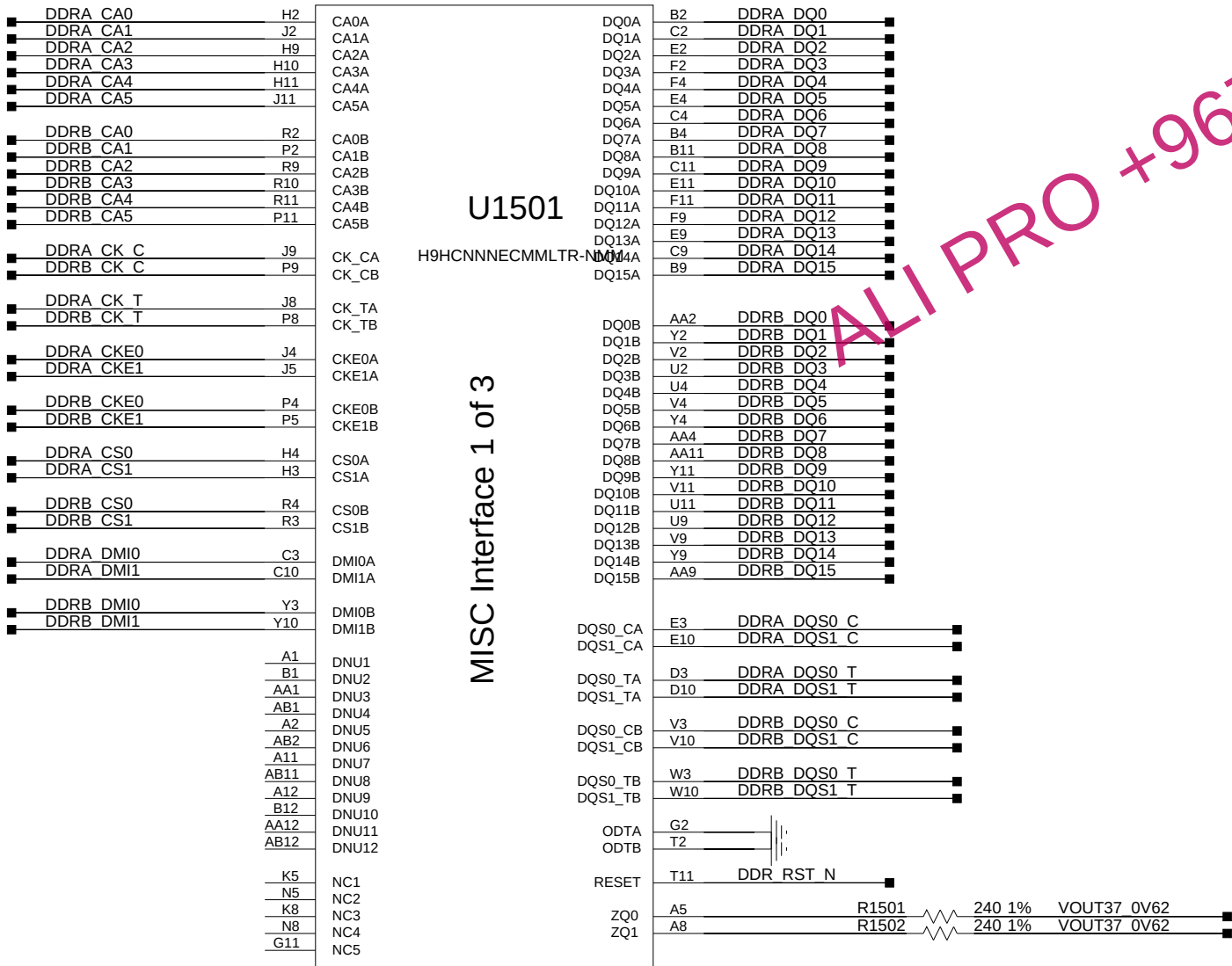


I2C_SPMI_SEL	
0	1
SPMI	I2C

15. LPDDR4x

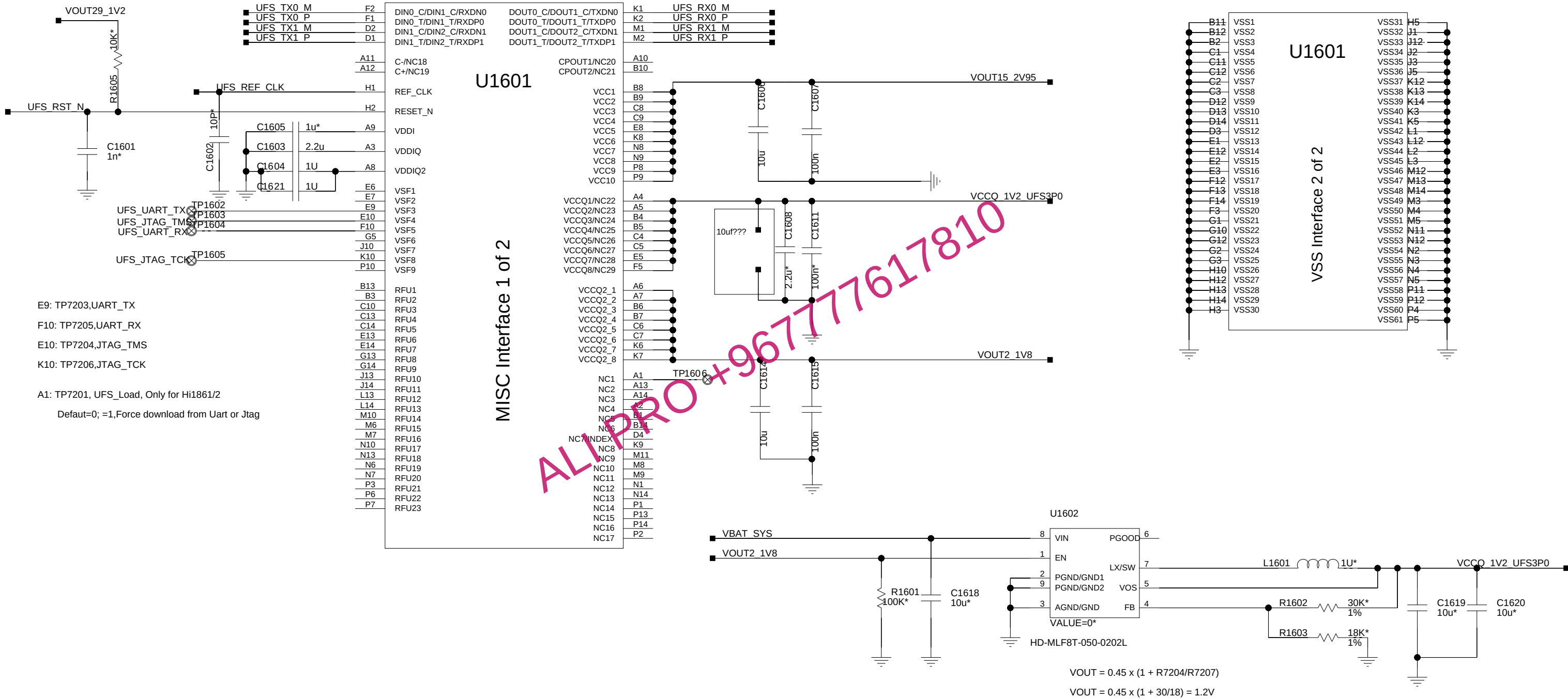


6G 40020606
8G 40020607

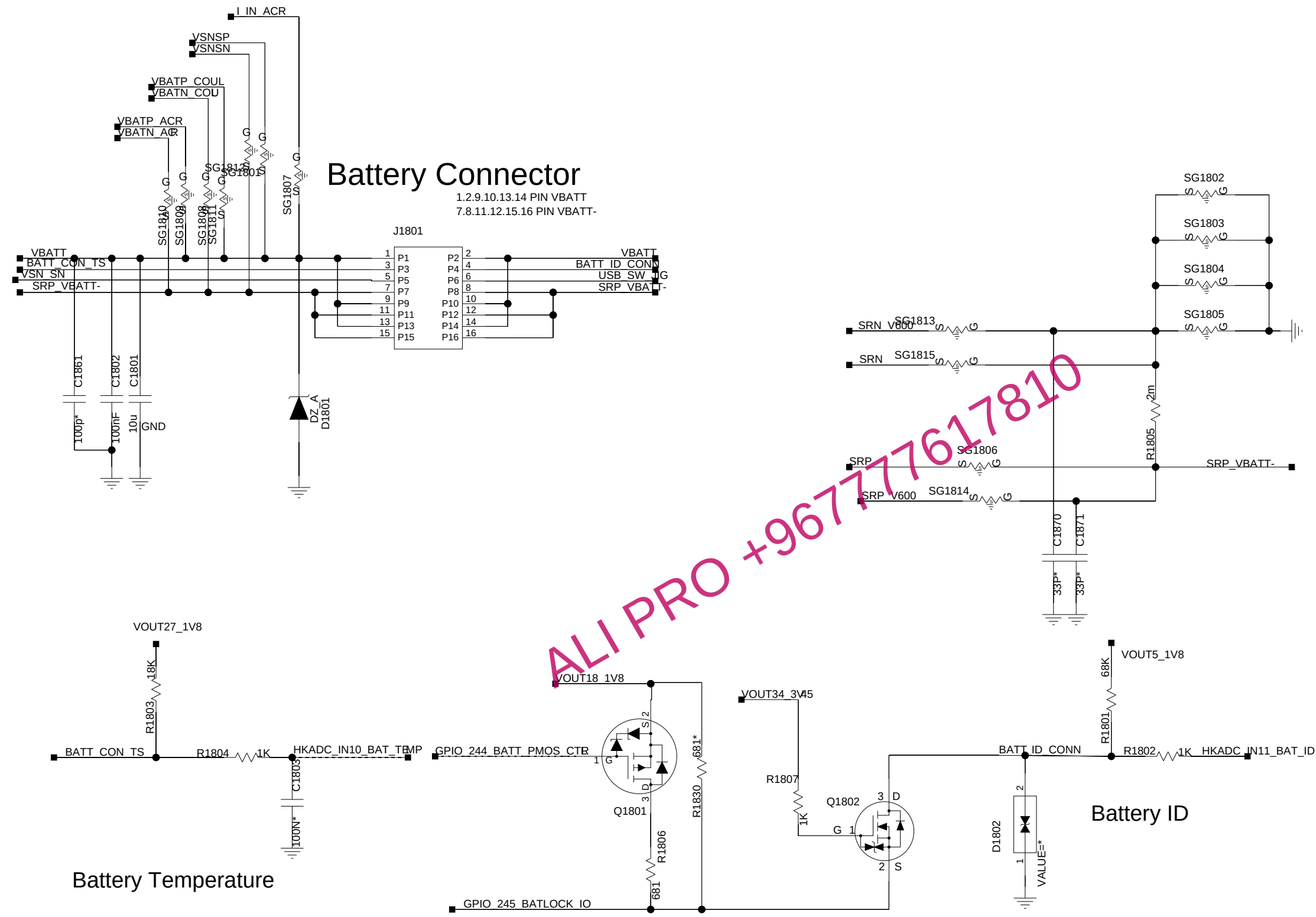


16. UFS

Voltage Domain	UFS2.1	UFS3.0
VCC(V)	2.95	2.5
VCCQ(V)	NA	1.2
VCCQ2(V)	1.8	NA

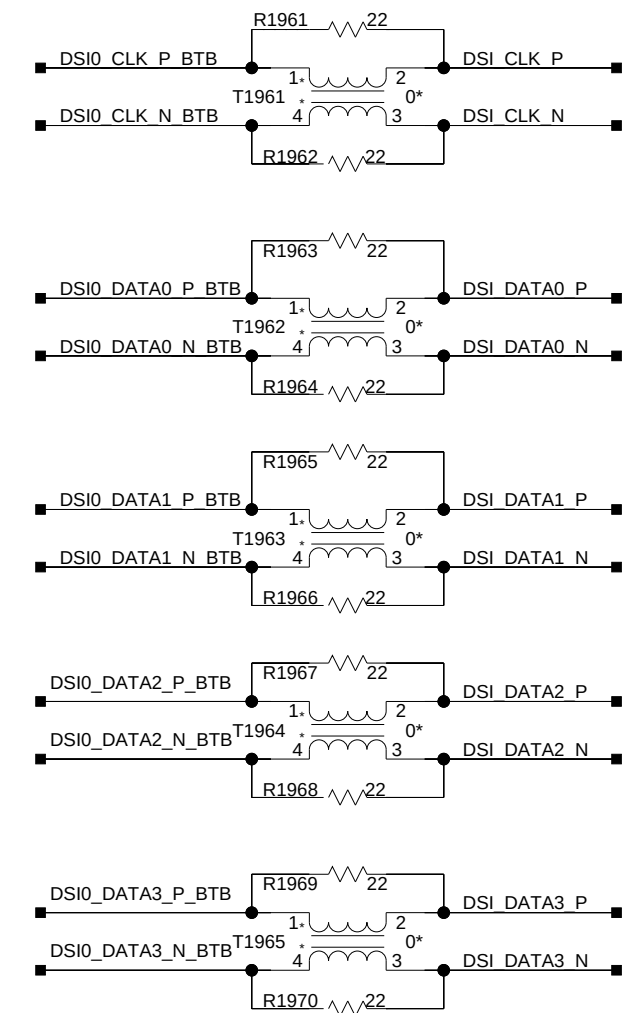
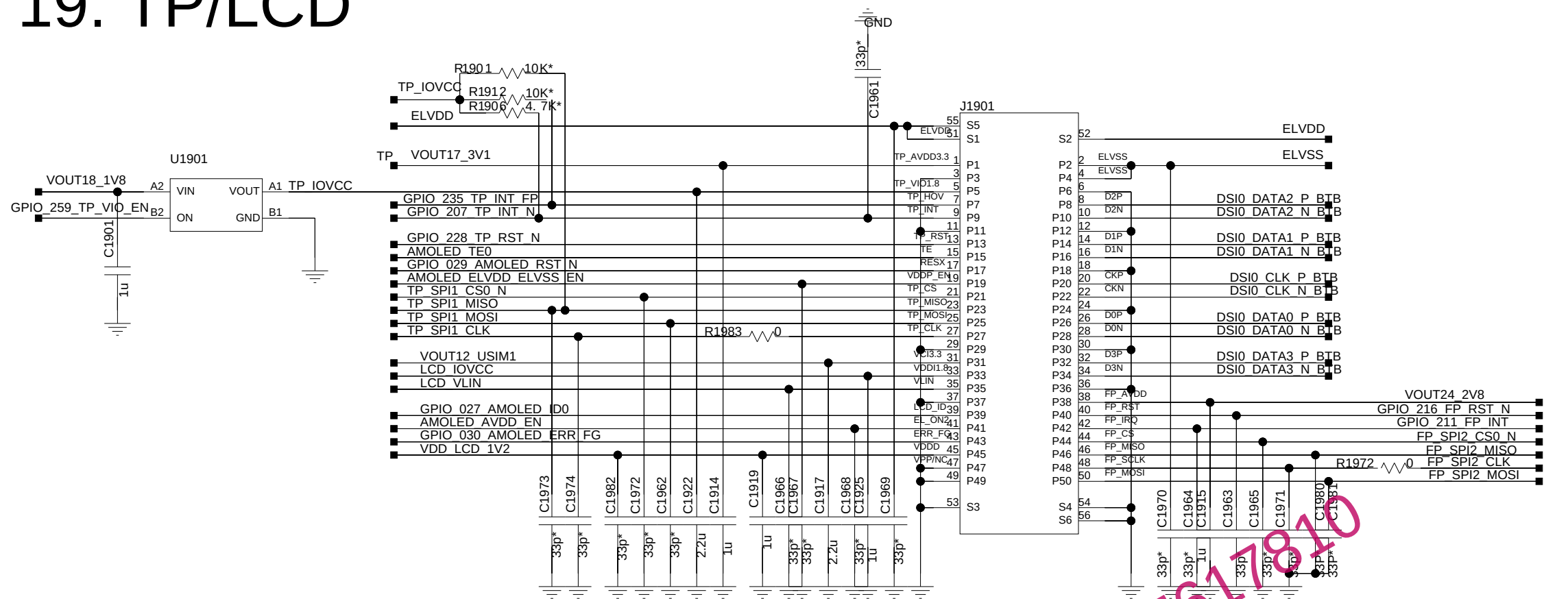


18. Battery

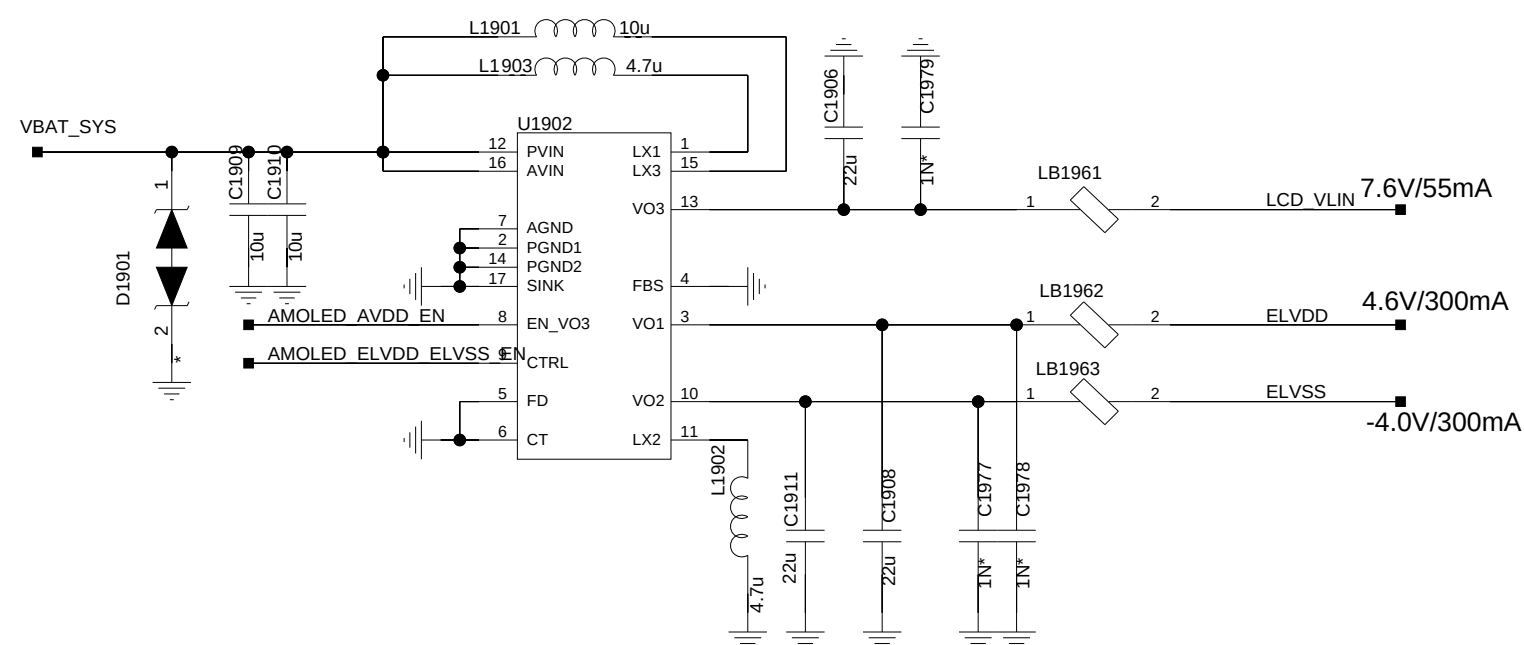


CAD note:
1.Trace for 9A
2.Differential trace

19. TP/LCD



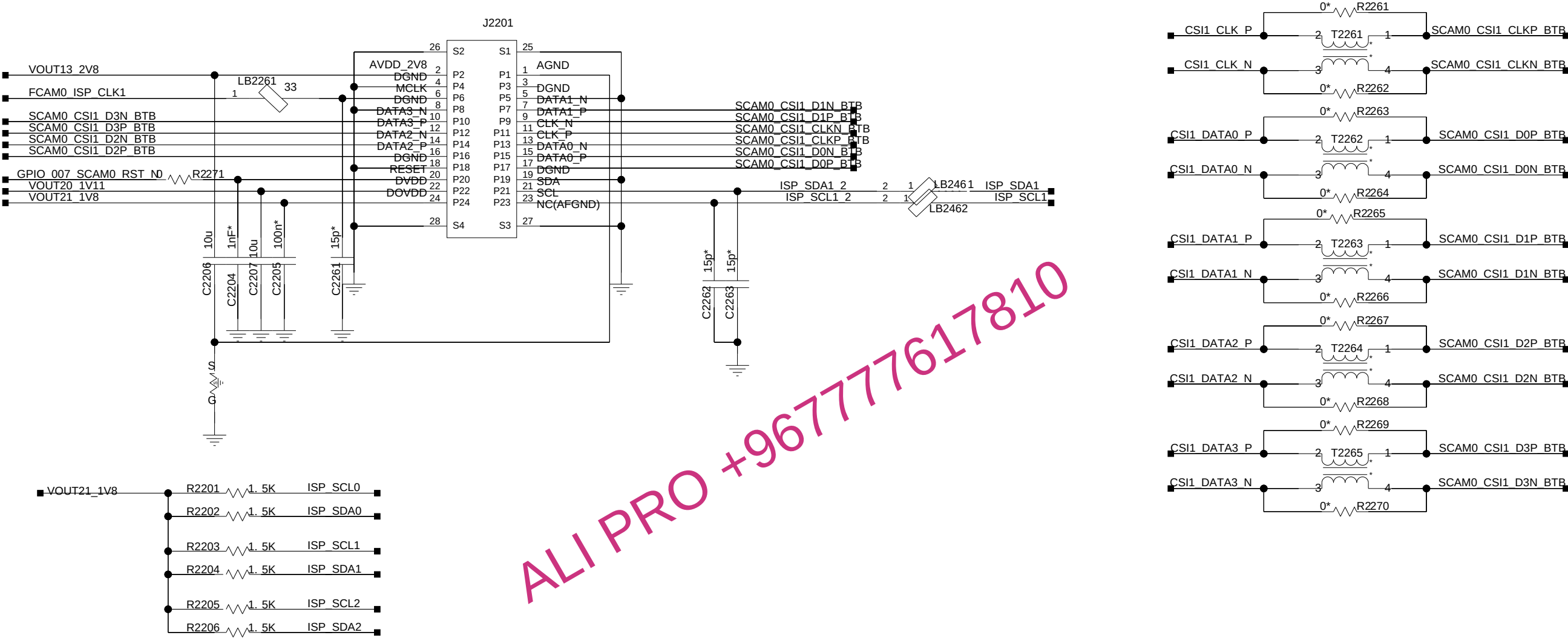
AMOLED Voltage Driver



21. Flash

22. Front CAM

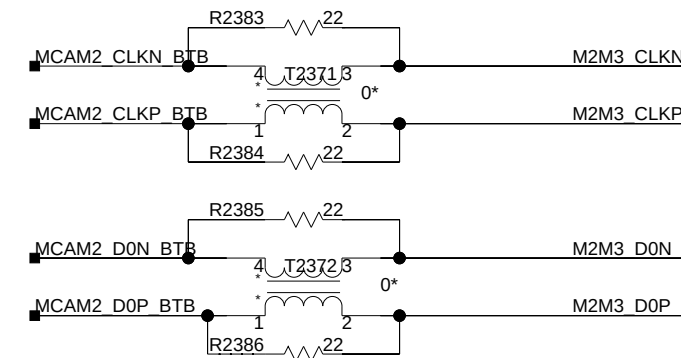
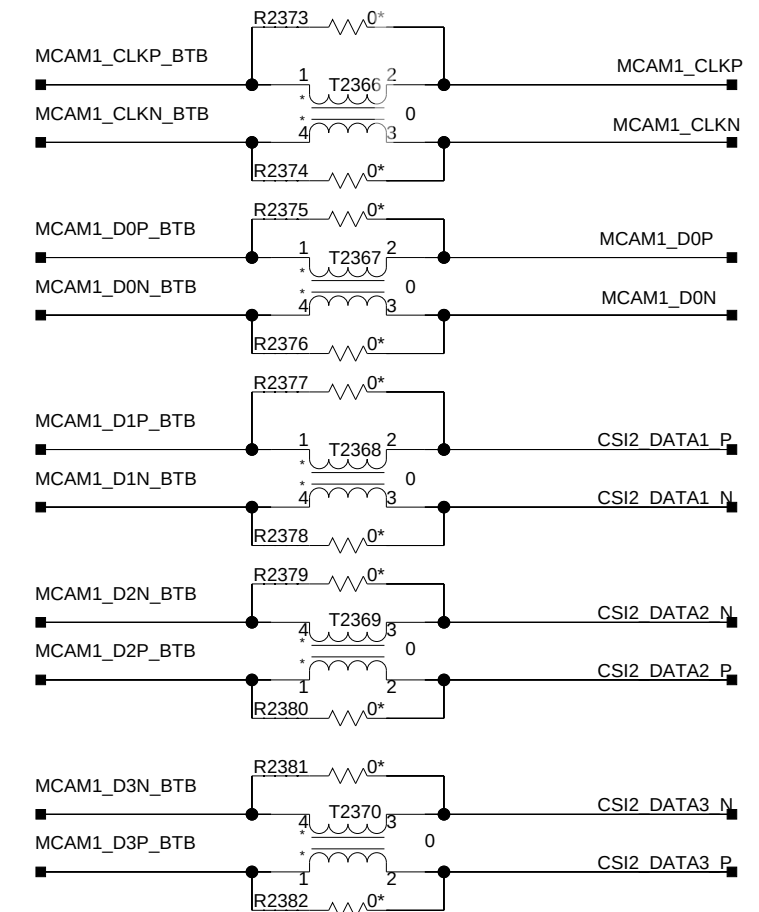
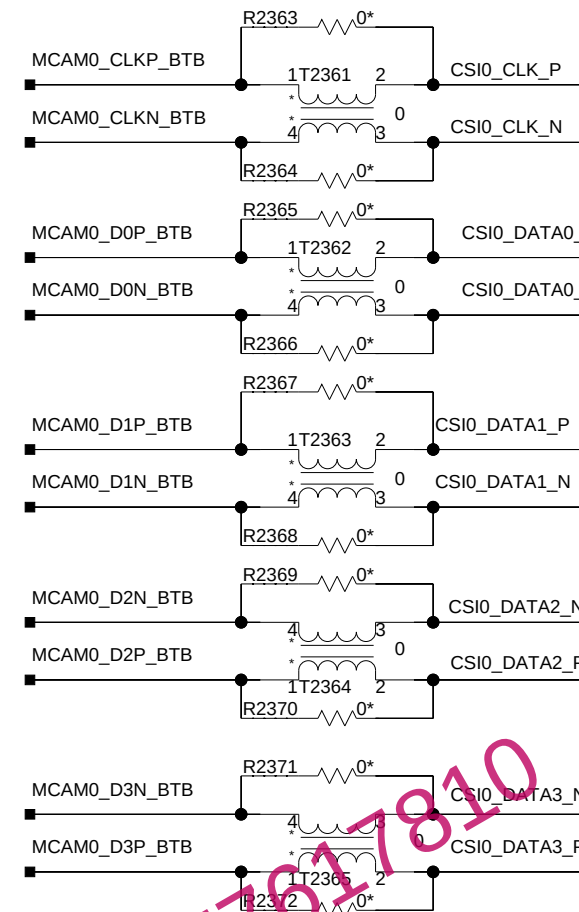
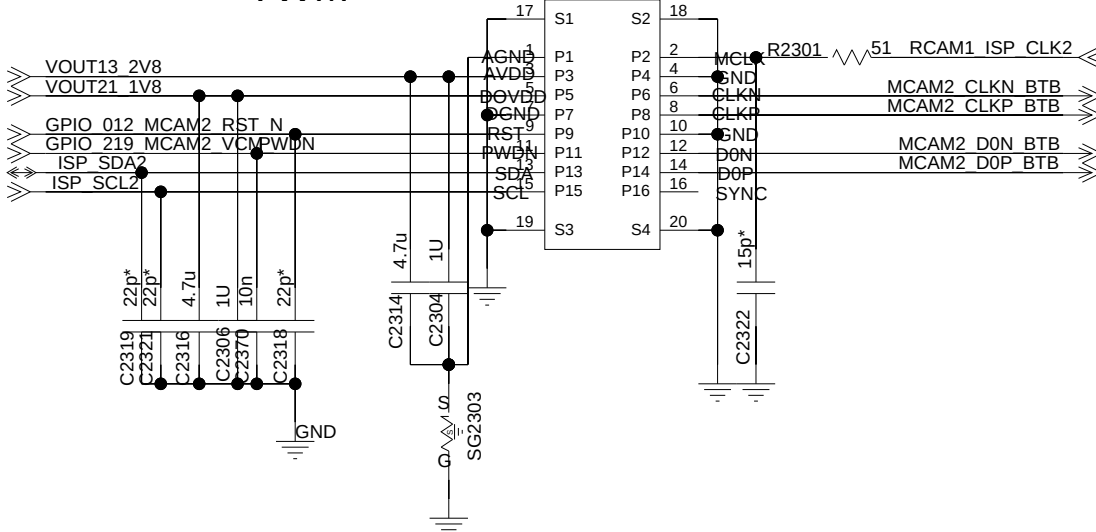
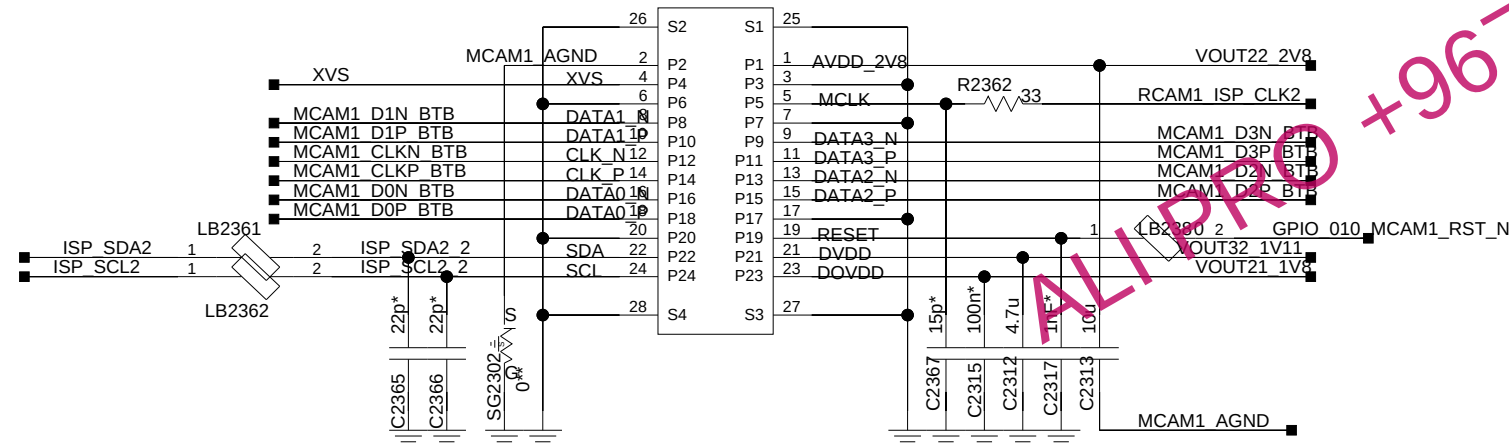
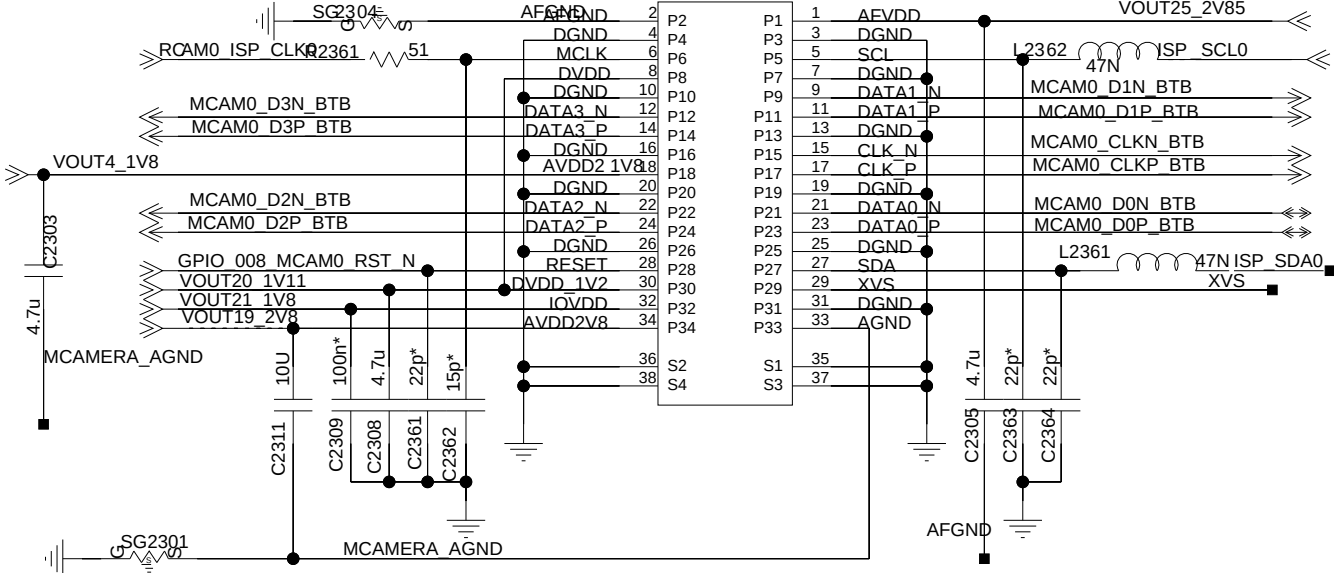
THTWM Slave CAM



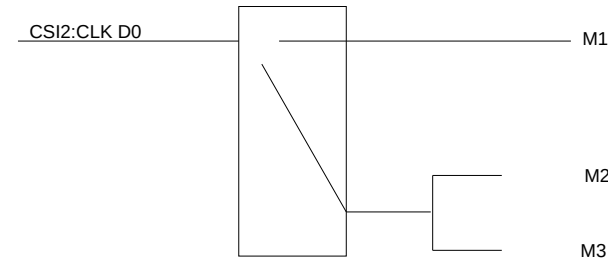
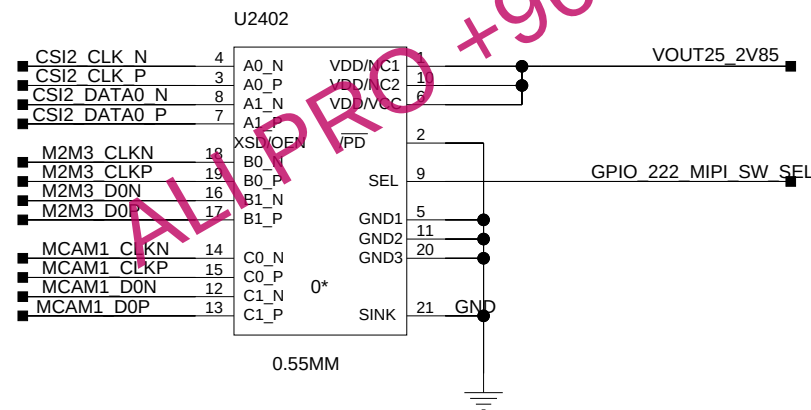
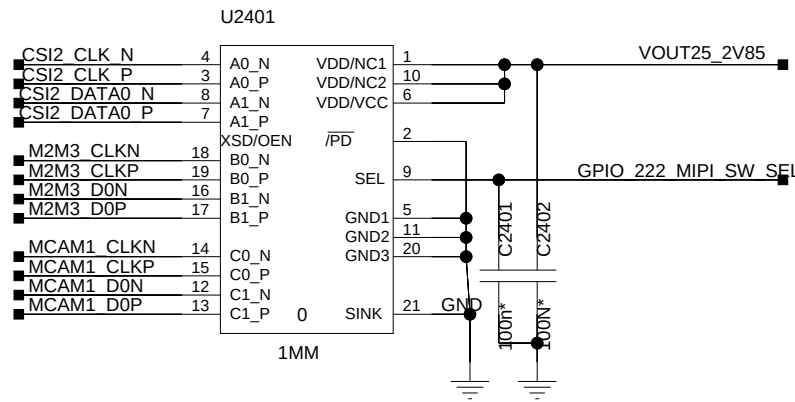
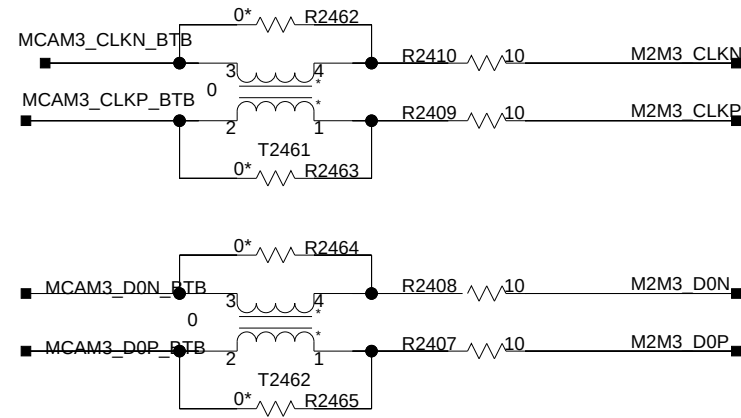
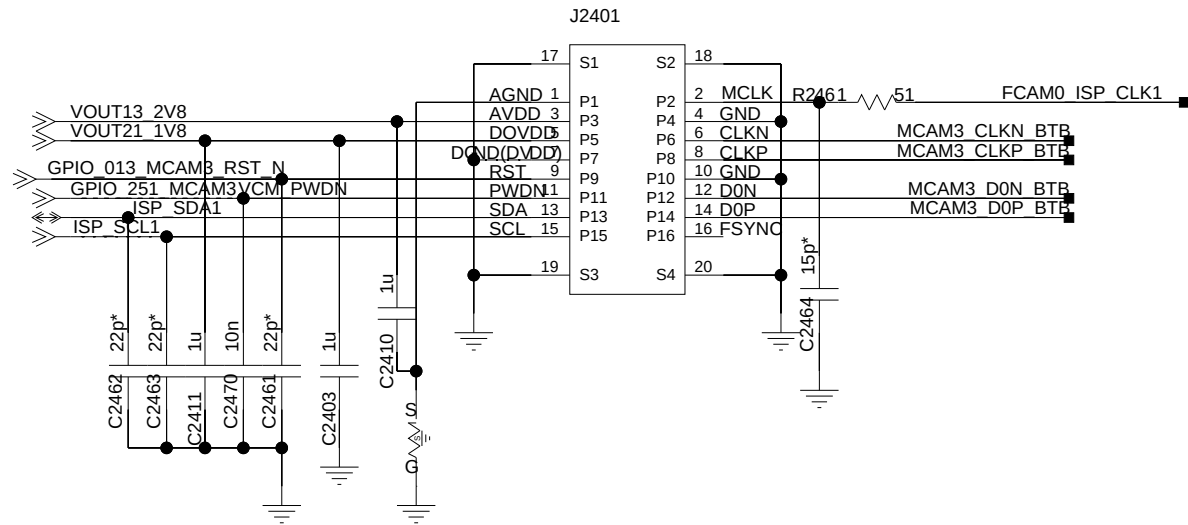
BootMode0	BootMode1	Function(BootROM Start)
1	1	SPI Start
1	0	SSI Start
0	1	I2C Start
0	0	SLIMBUS Start

23. Rear CAM1

FOEIM Main



24. Rear CAM2

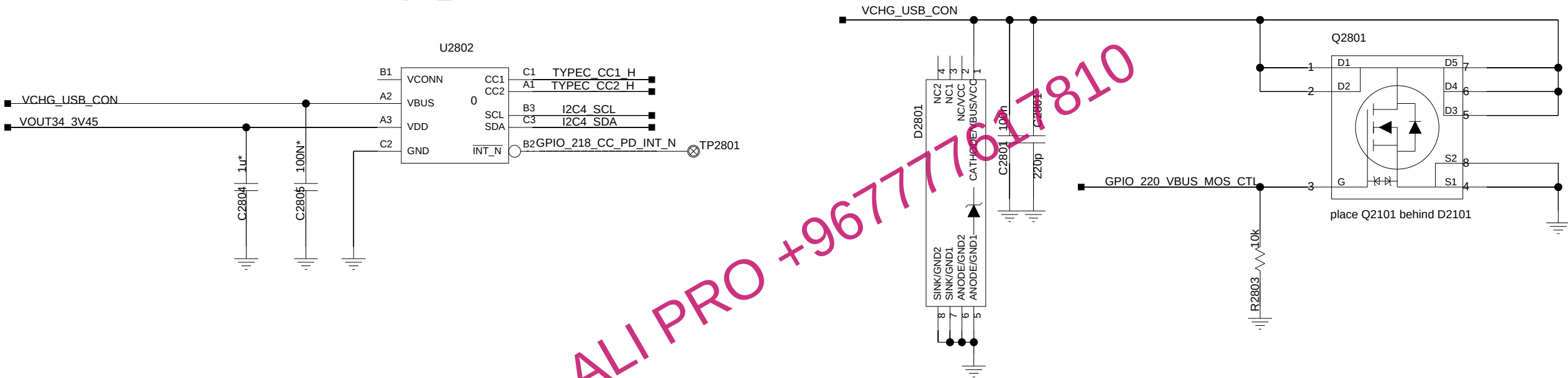


M1 M2 M3

25 High Voltage charger

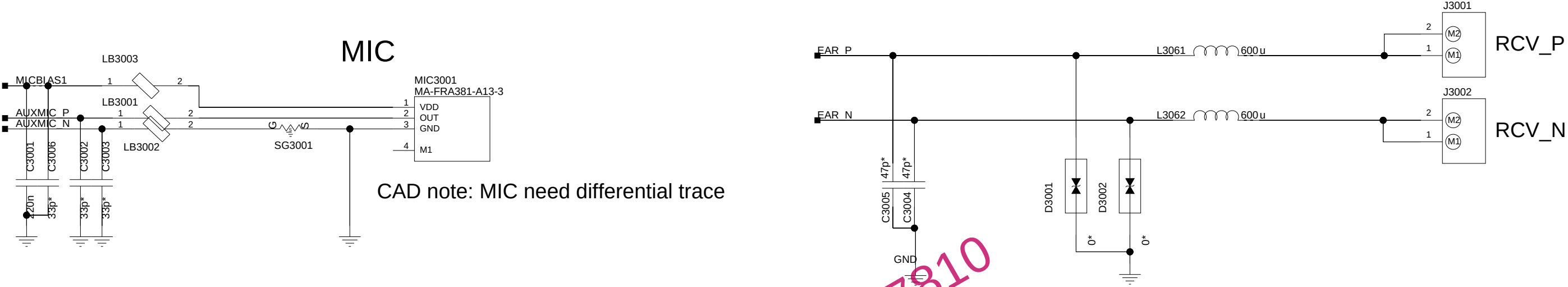
28.USB

PD



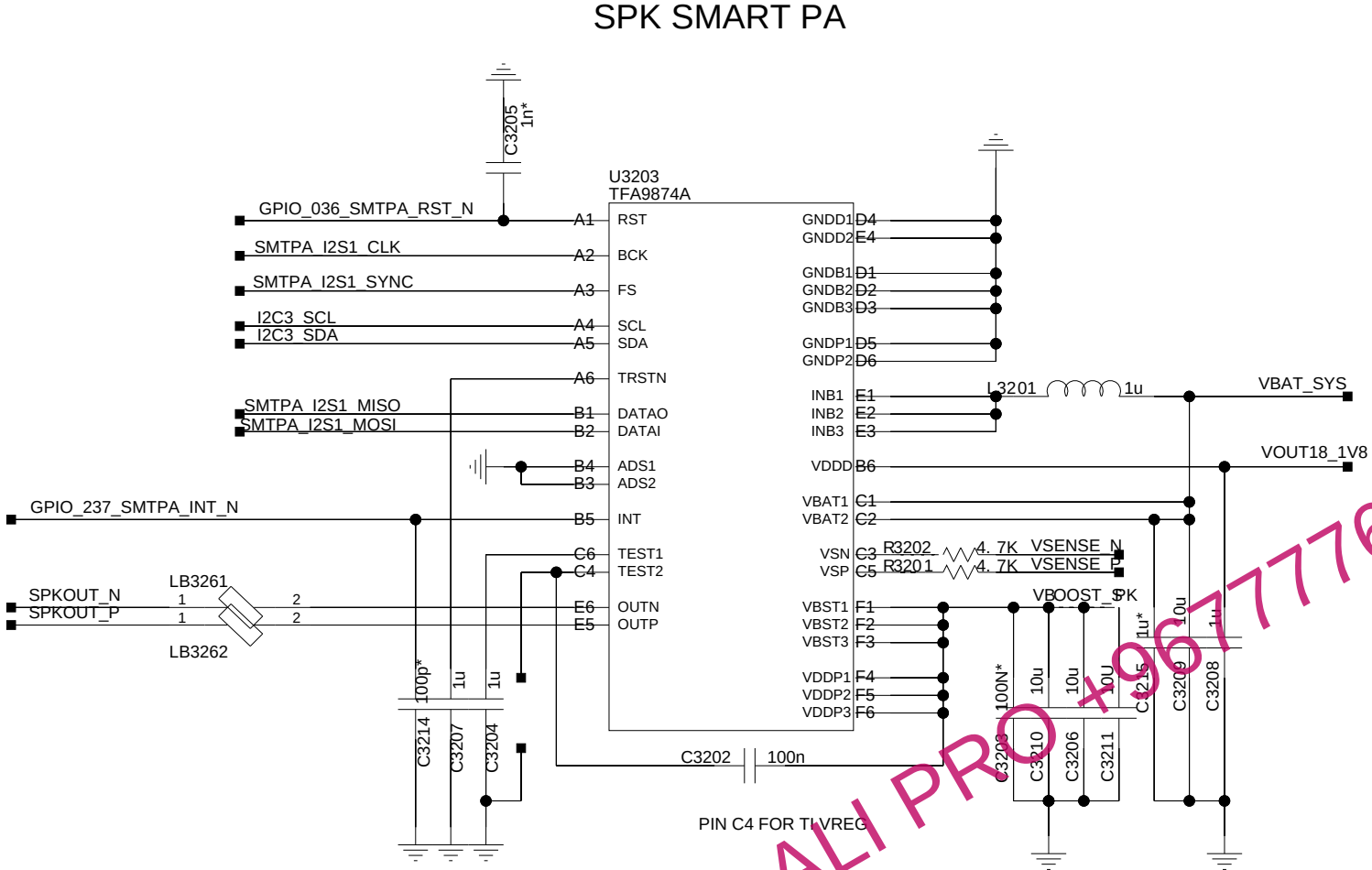
29. Reserved_USB

30. MIC/RCV/VIB/HAC

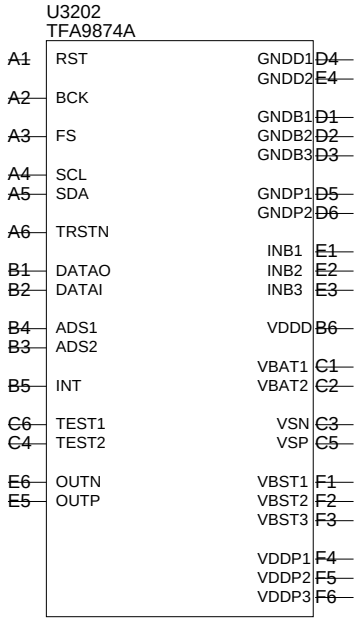


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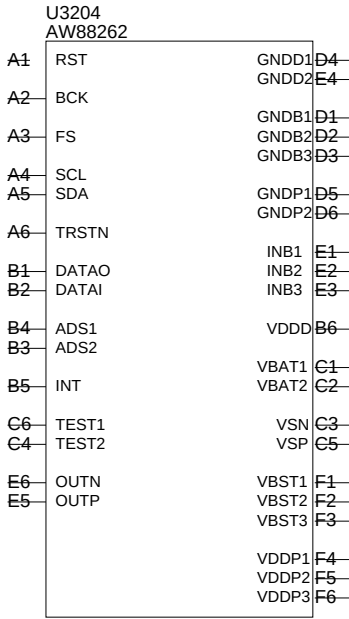
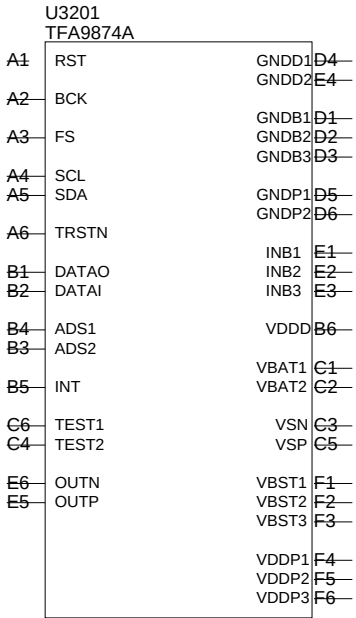
32 SmartPA



SMART PA For Cirrus Share PAD with U2500

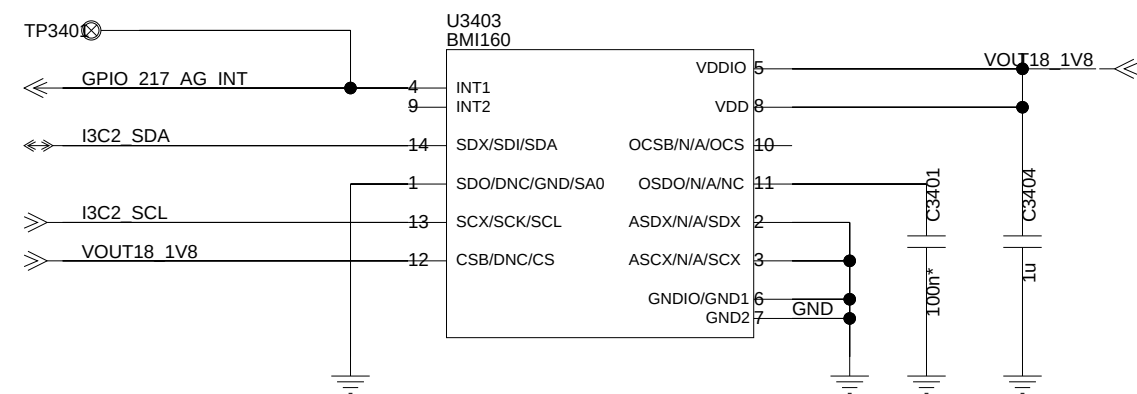


SMART PA For Cirrus Share PAD with U2501

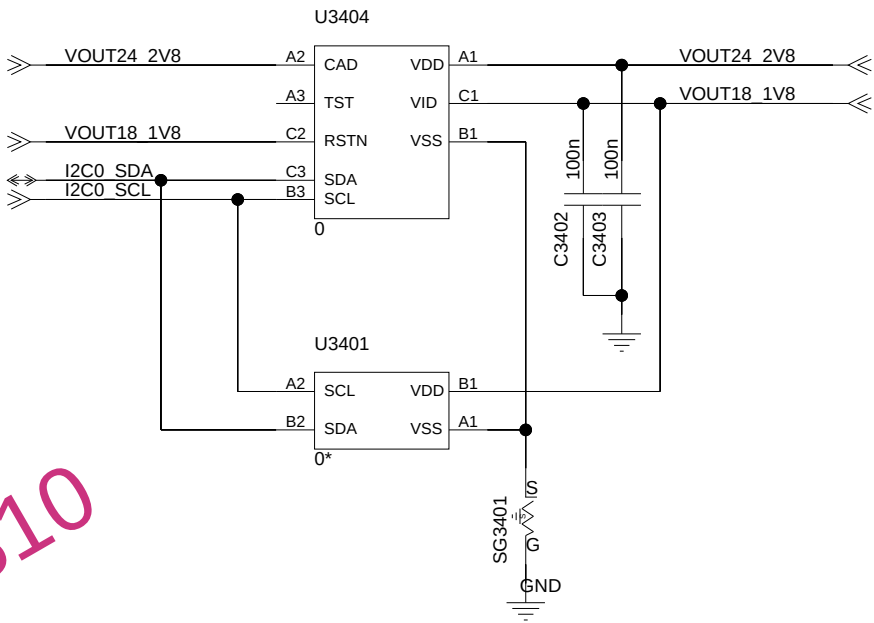


34 Sensor

Accelerometer and Gyroscope 6-AXIS SENSOR

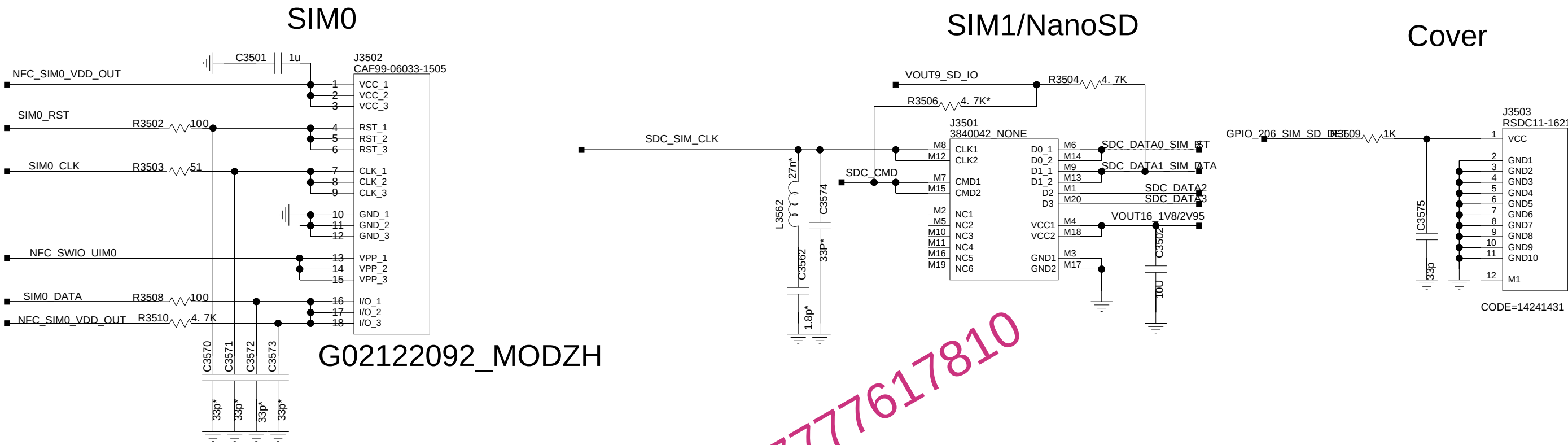


Compass 0259 or 0269 Compass



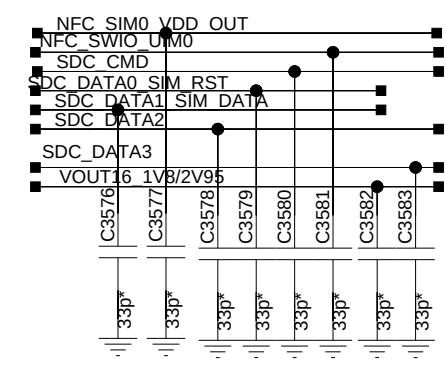
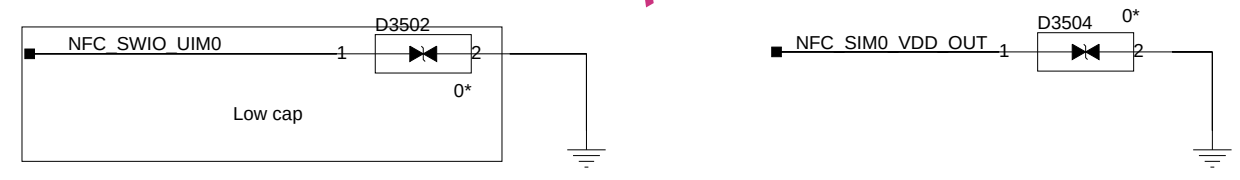
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35. SIM/SD

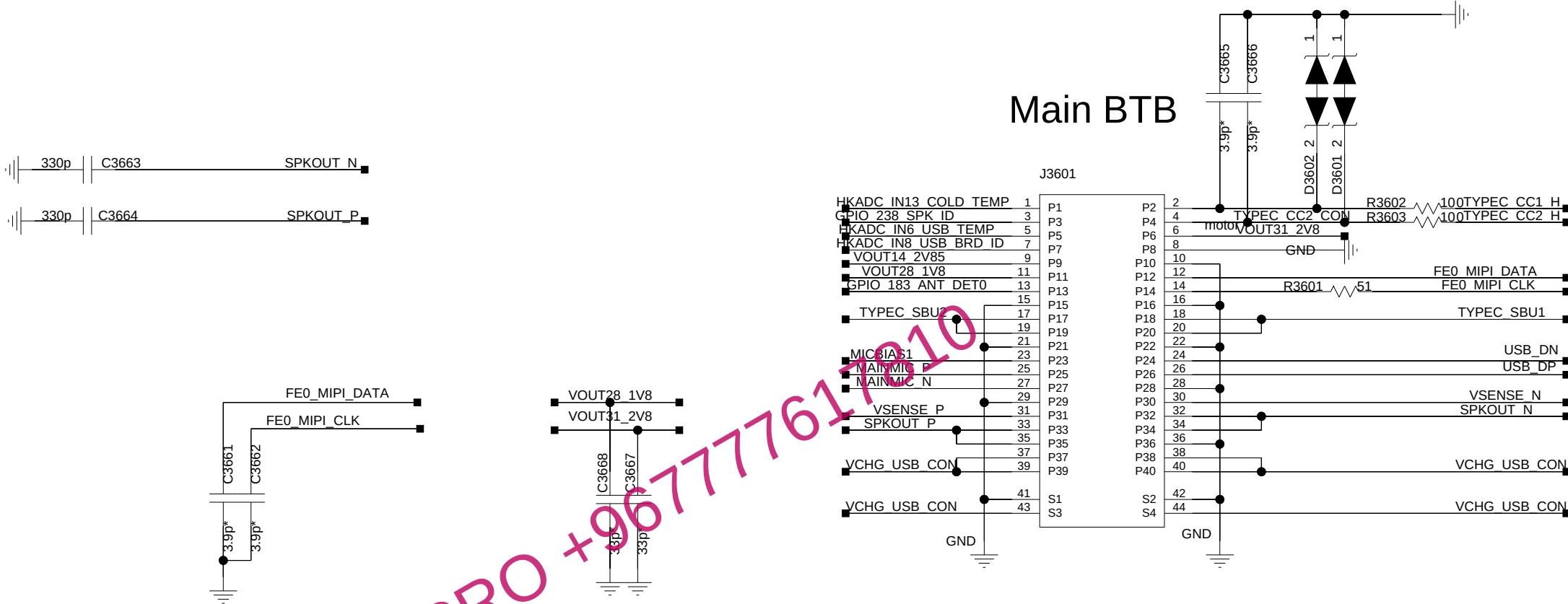


G02122092_MODZH

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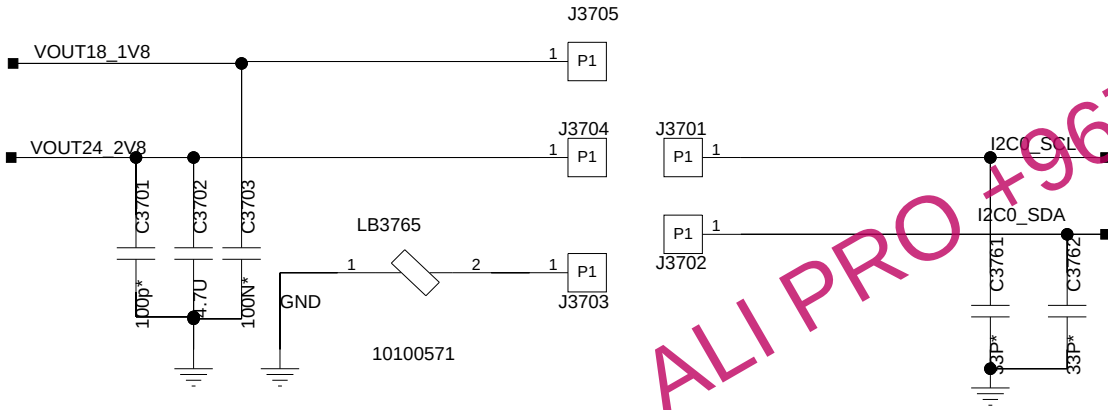


36 Main FPC Interface



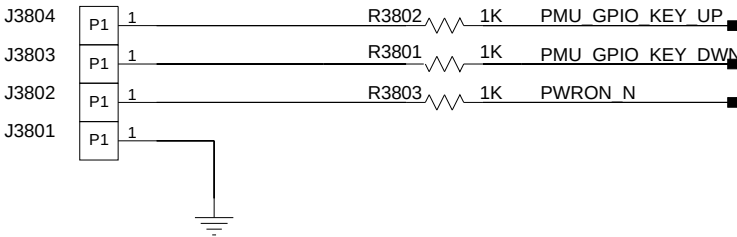
37.Other FPC Interface

PROXIMITY&RGB AMBIENT



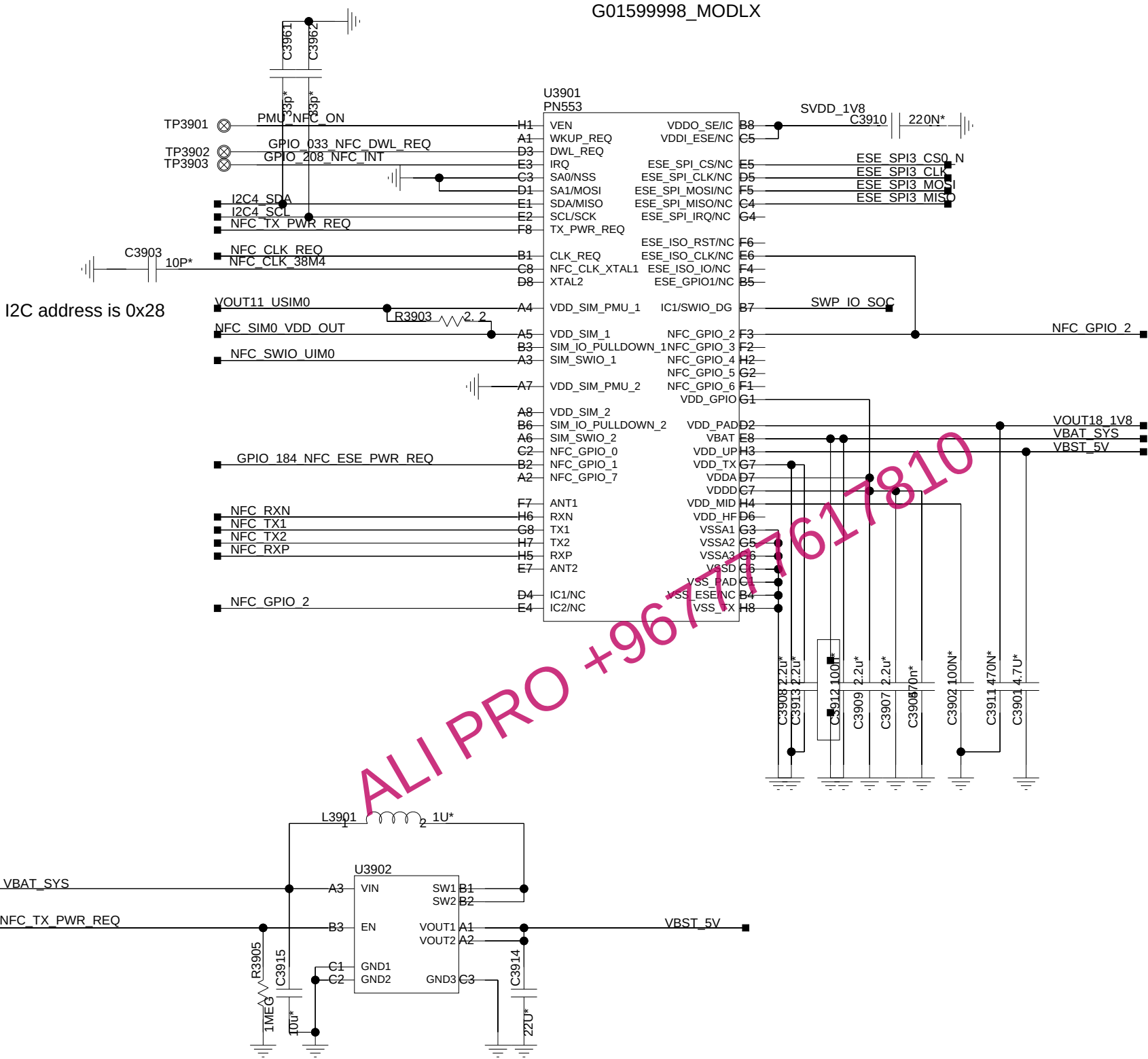
38 LED/KEY

KEY SPRING



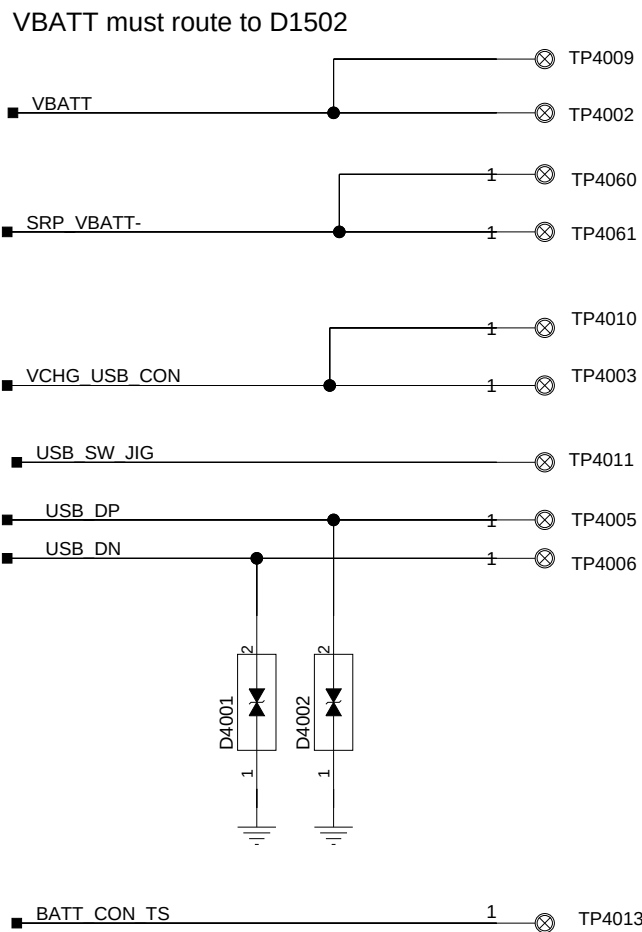
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39 NFC_BB

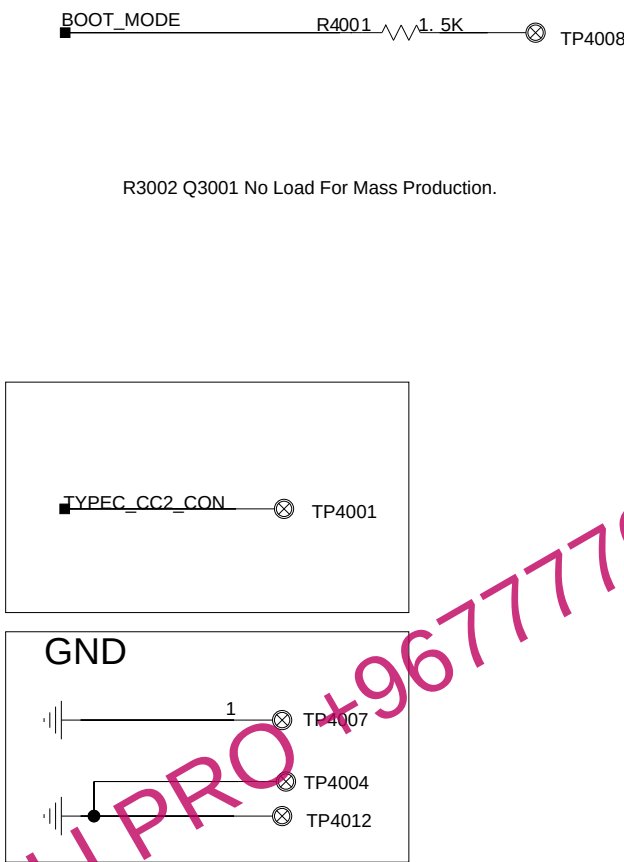


40 Test point/Shields

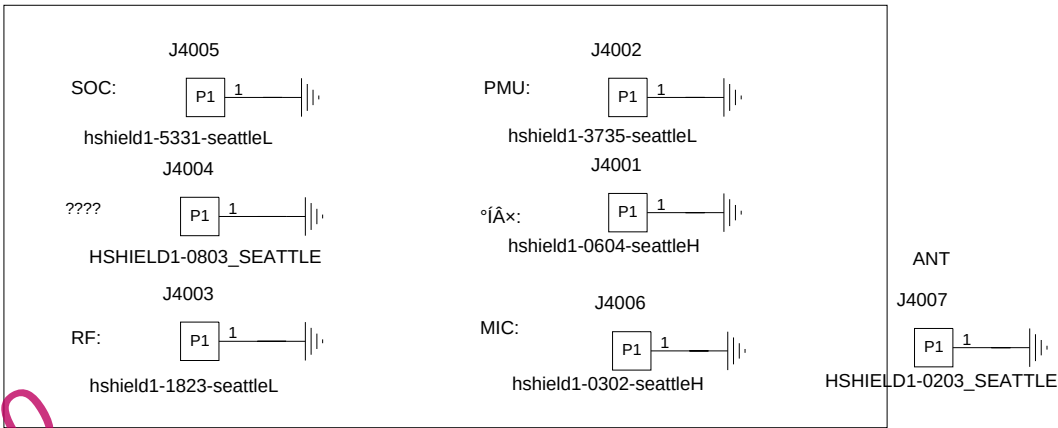
CBT/PT



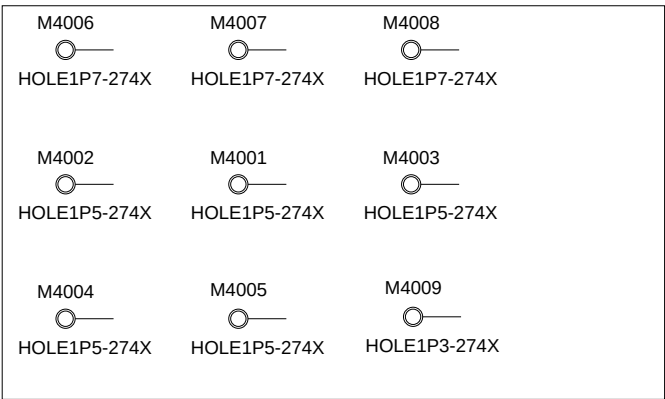
BOOT MODE



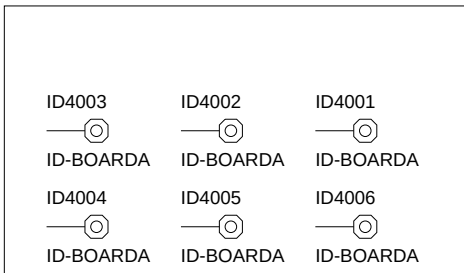
Shielding



HOLE

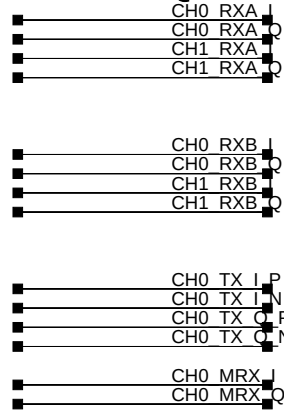


MARK POINT

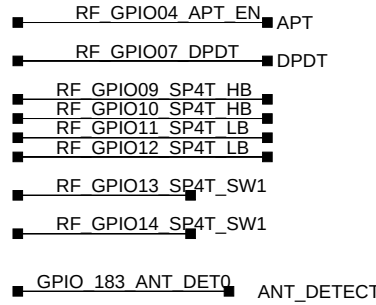


41.RF Interface

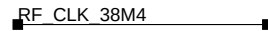
RFIC IQ



GPIO Interface



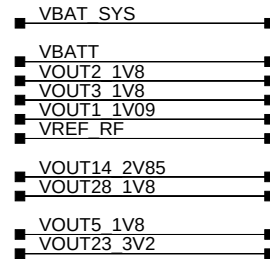
CLK



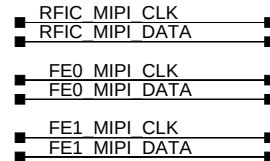
HKADC



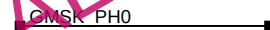
POWER



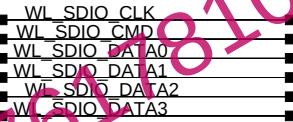
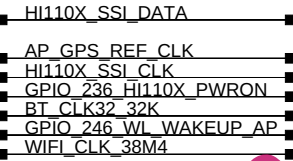
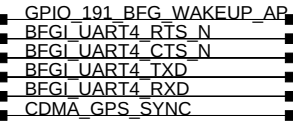
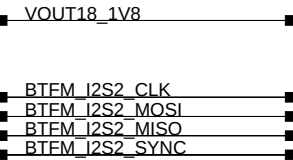
MIPI Interface



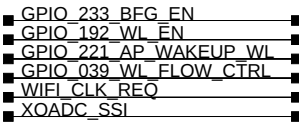
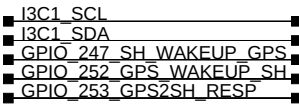
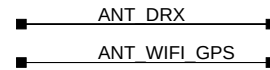
GSM



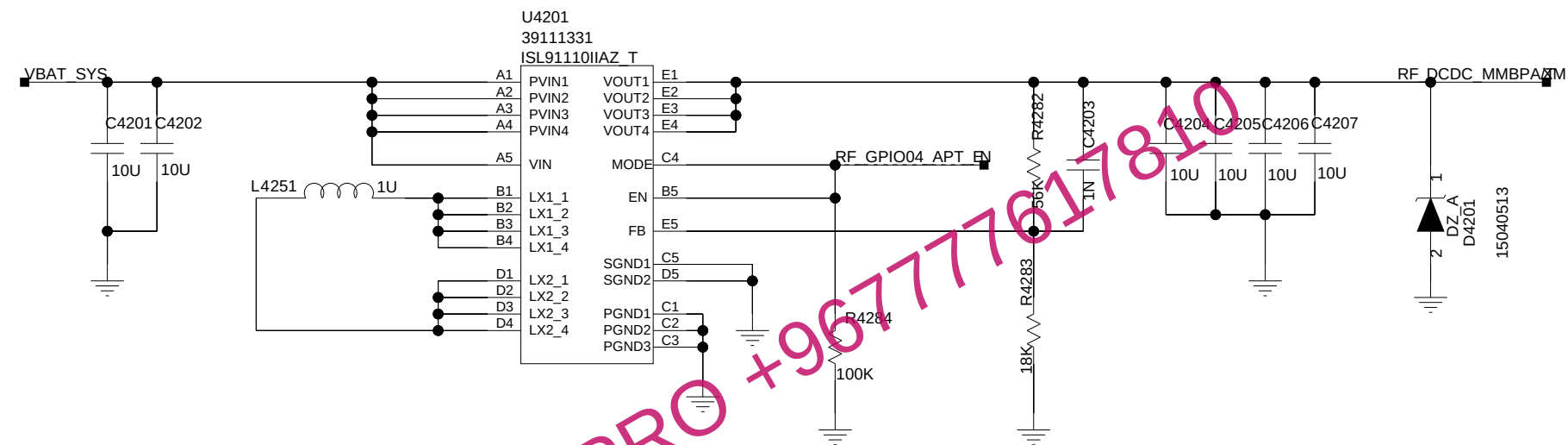
NC Interface



ANT



42.RF_PMU

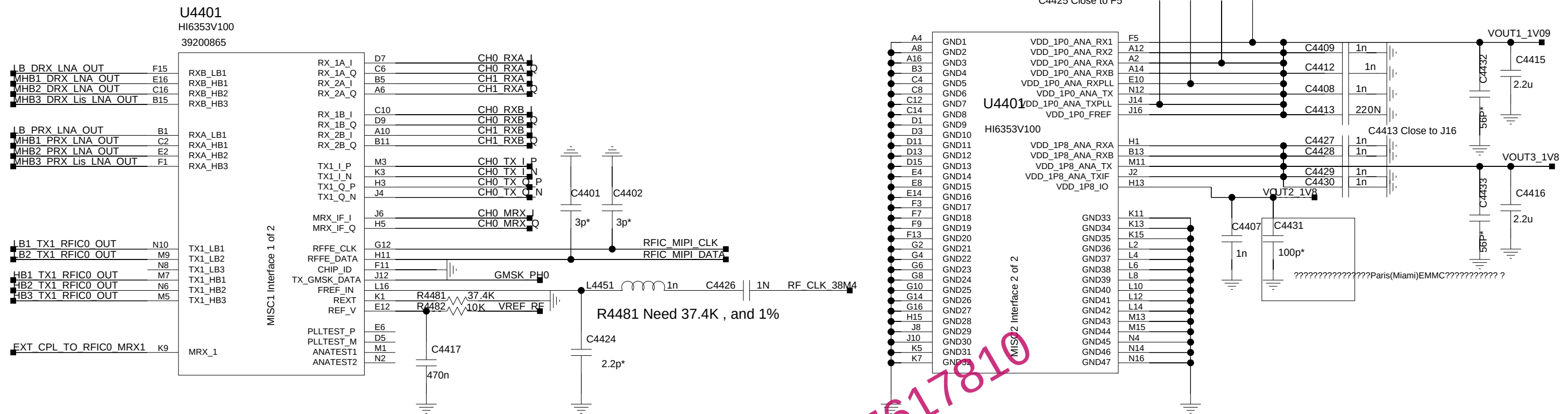

$$V_{out} = 2.5 \cdot V_{CON}$$
$$V_{out} = 2.5 \cdot 1.4 = 3.5 \text{ V}$$

U3201 Truth Table	
EN	State
0	shut down
1	normal

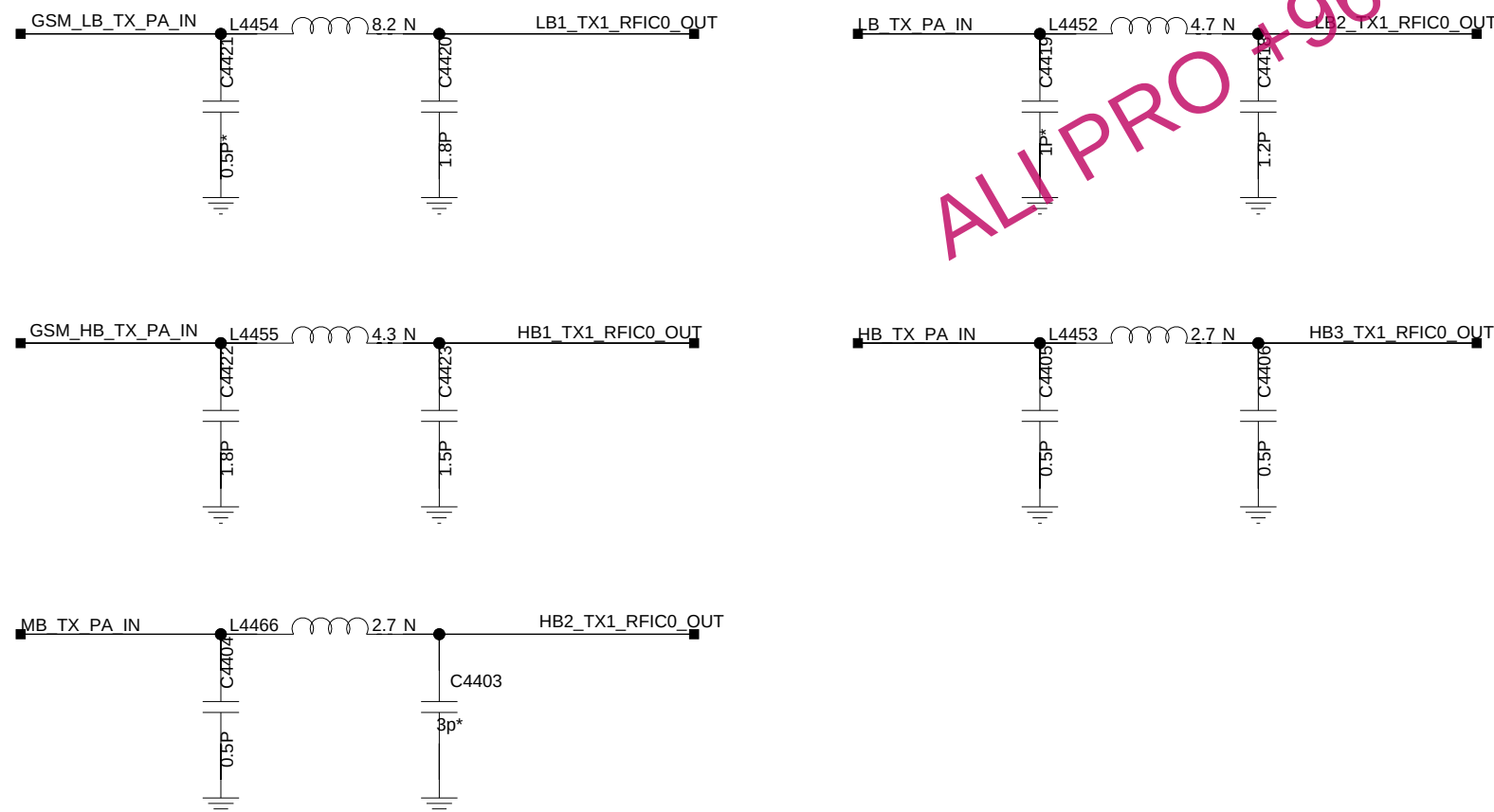
mode	State
0	PWM Only
1	PFM or PWM

BP	State
0	automatic analog bypass mode
1	Force Bypass Mode

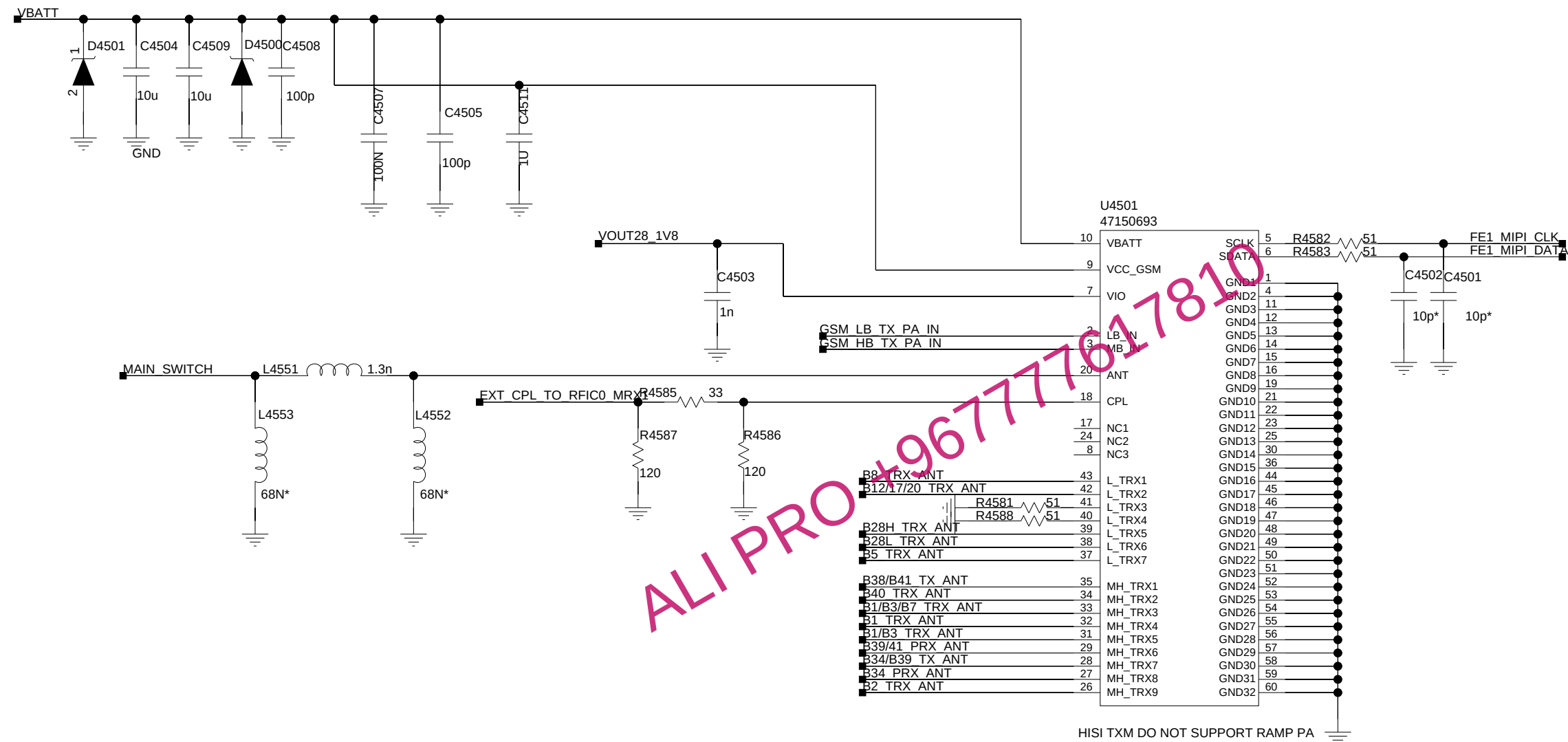
44.RFIC0_Hi6353



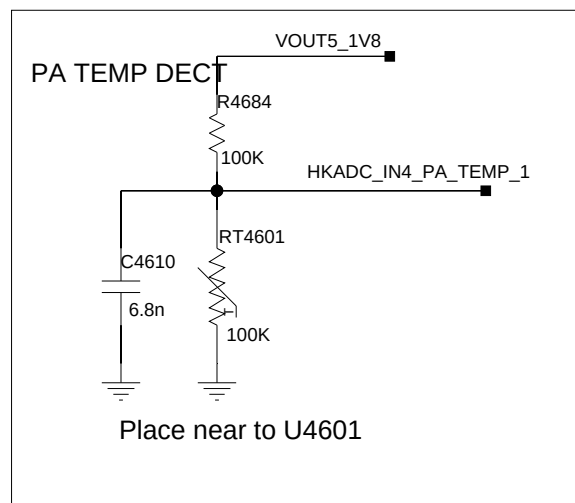
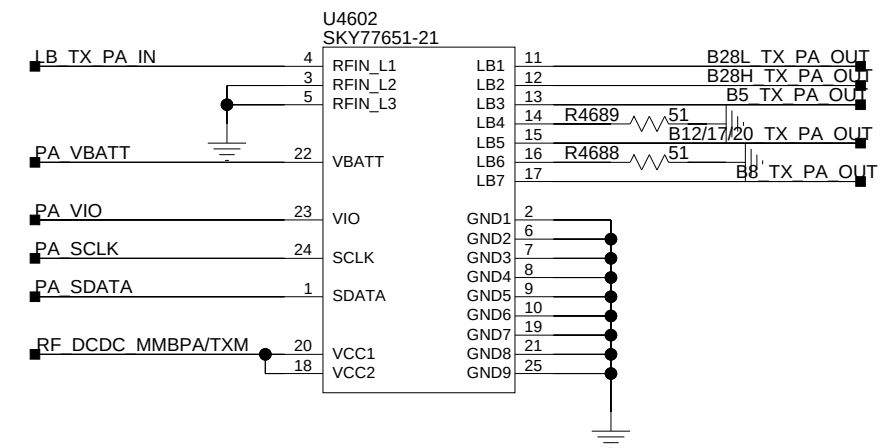
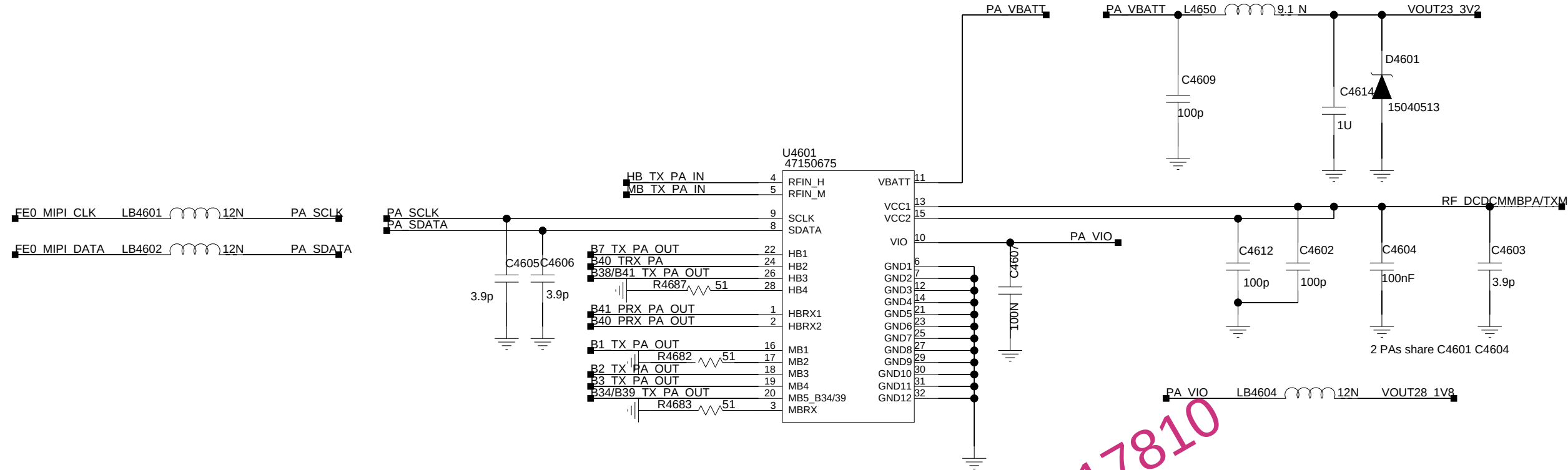
RFIC_TX_OUTPUT_MATCHING



45.TXM

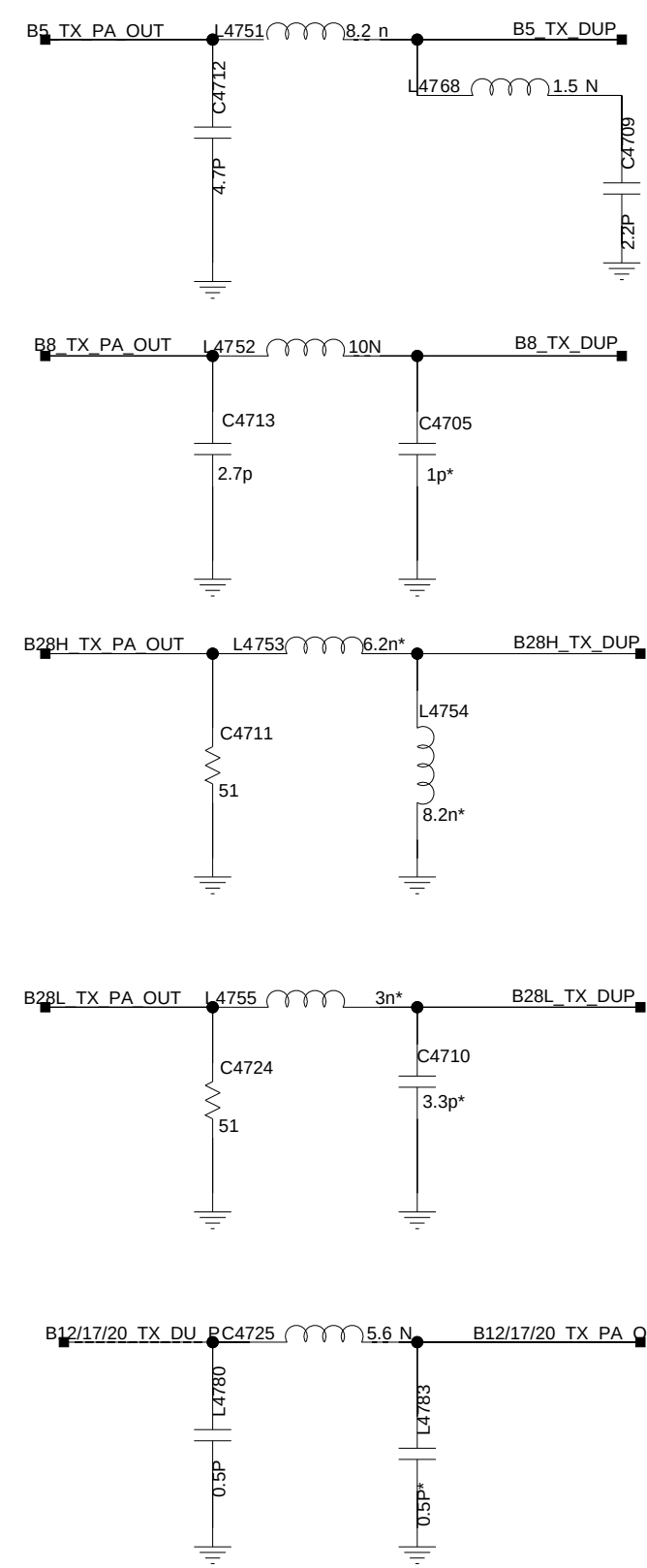


46.MMBPA

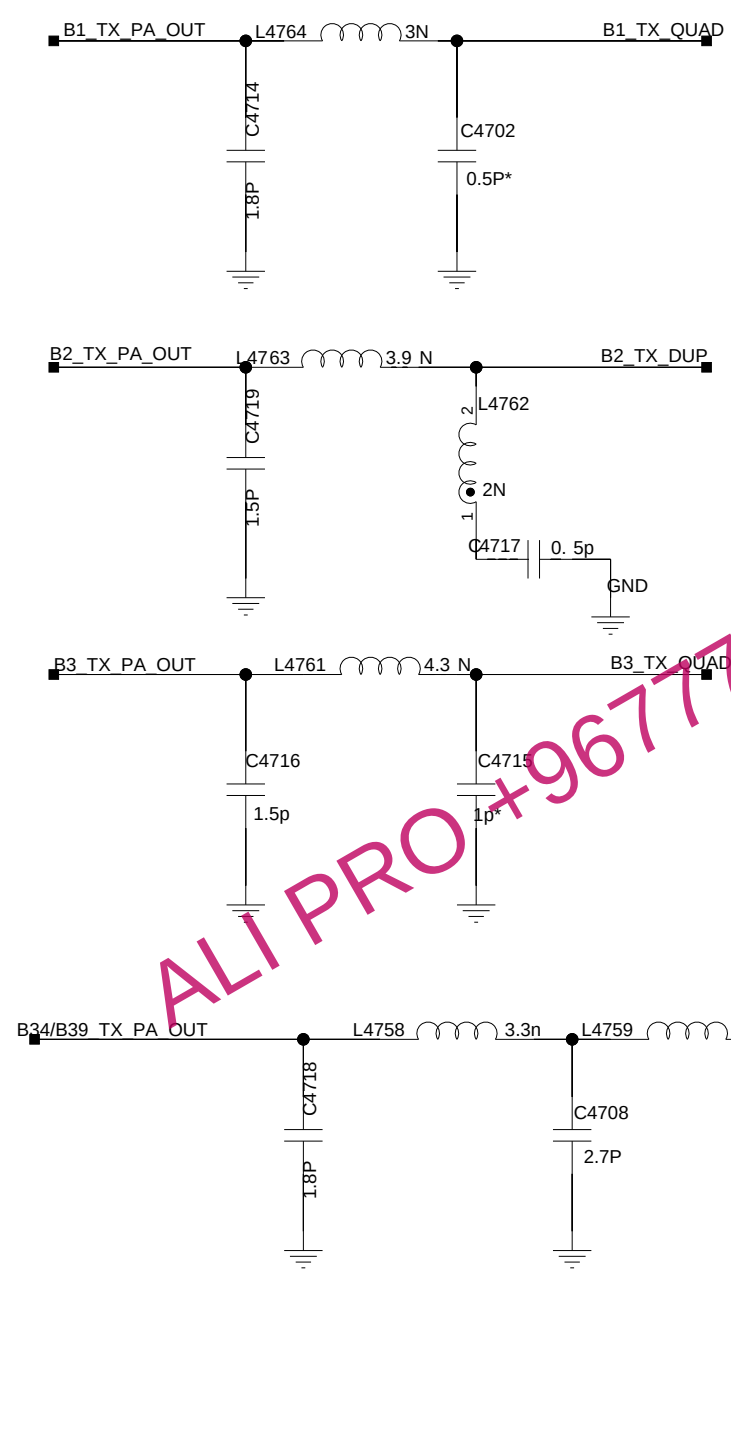


47.TX_Loadpull

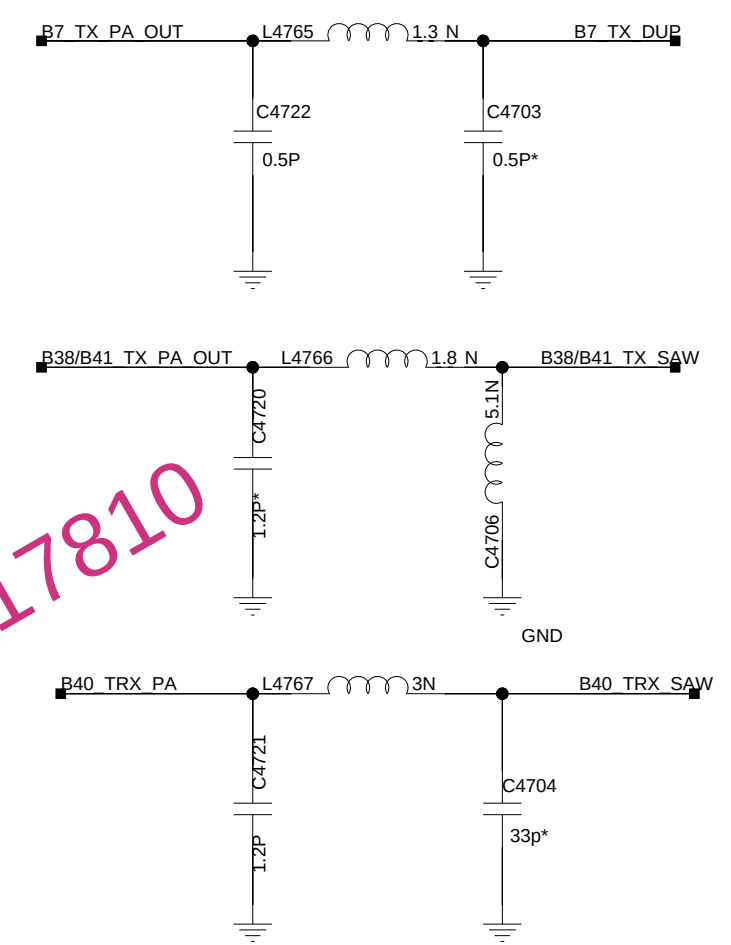
LB_match



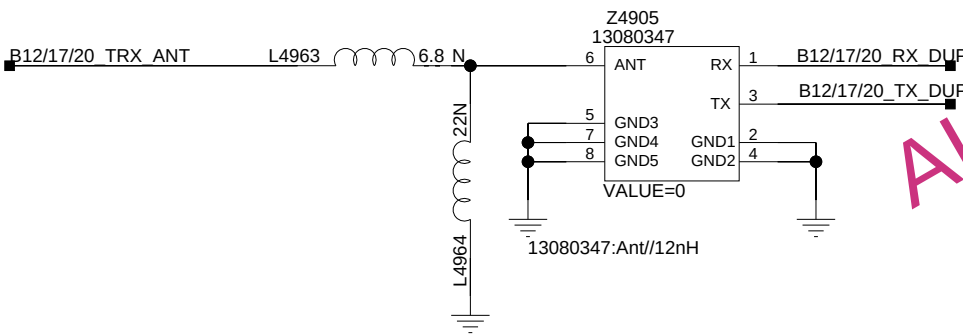
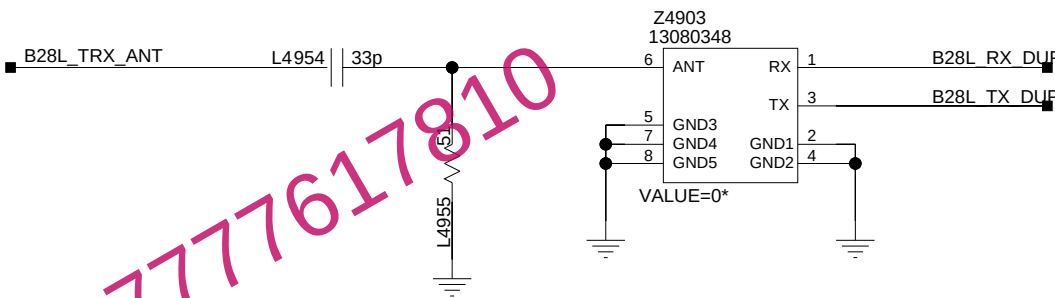
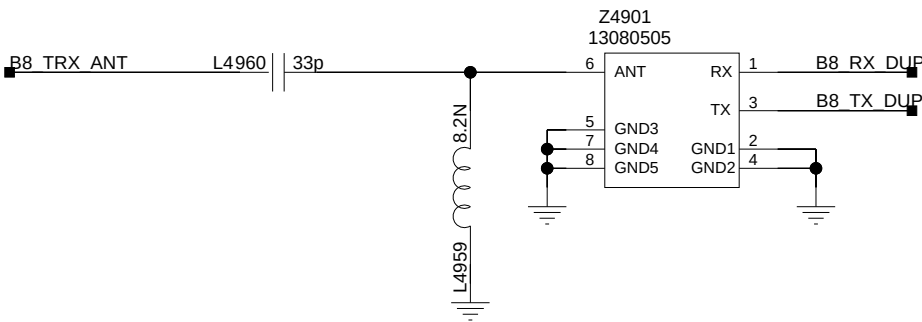
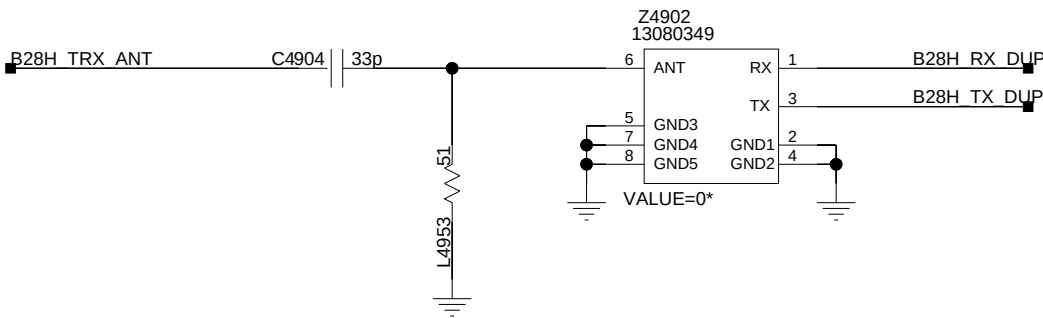
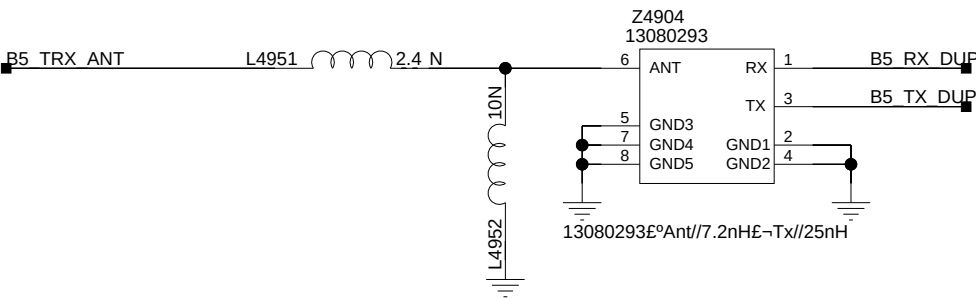
MB_match



HB_match

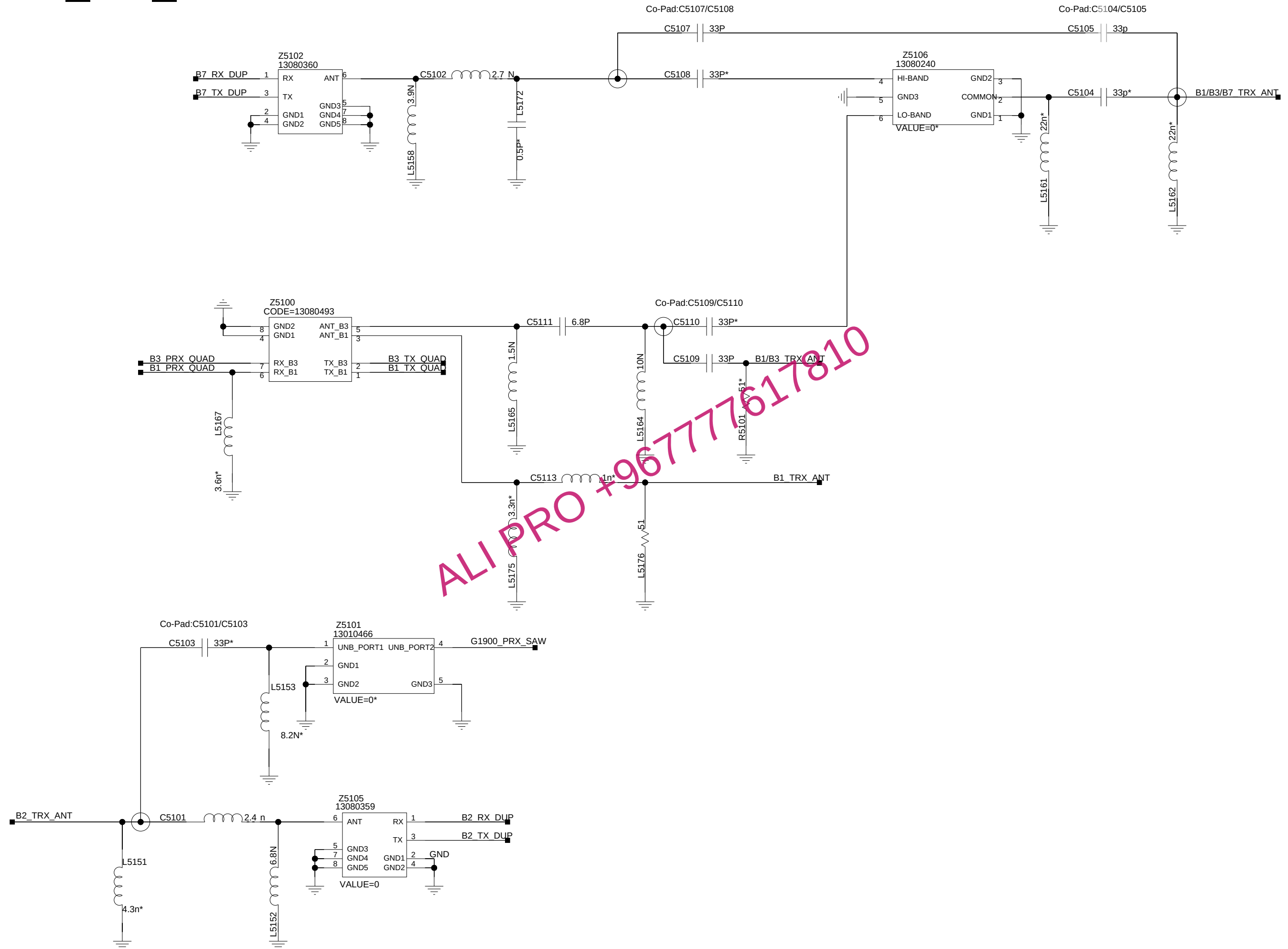


49. TRX_LB_1

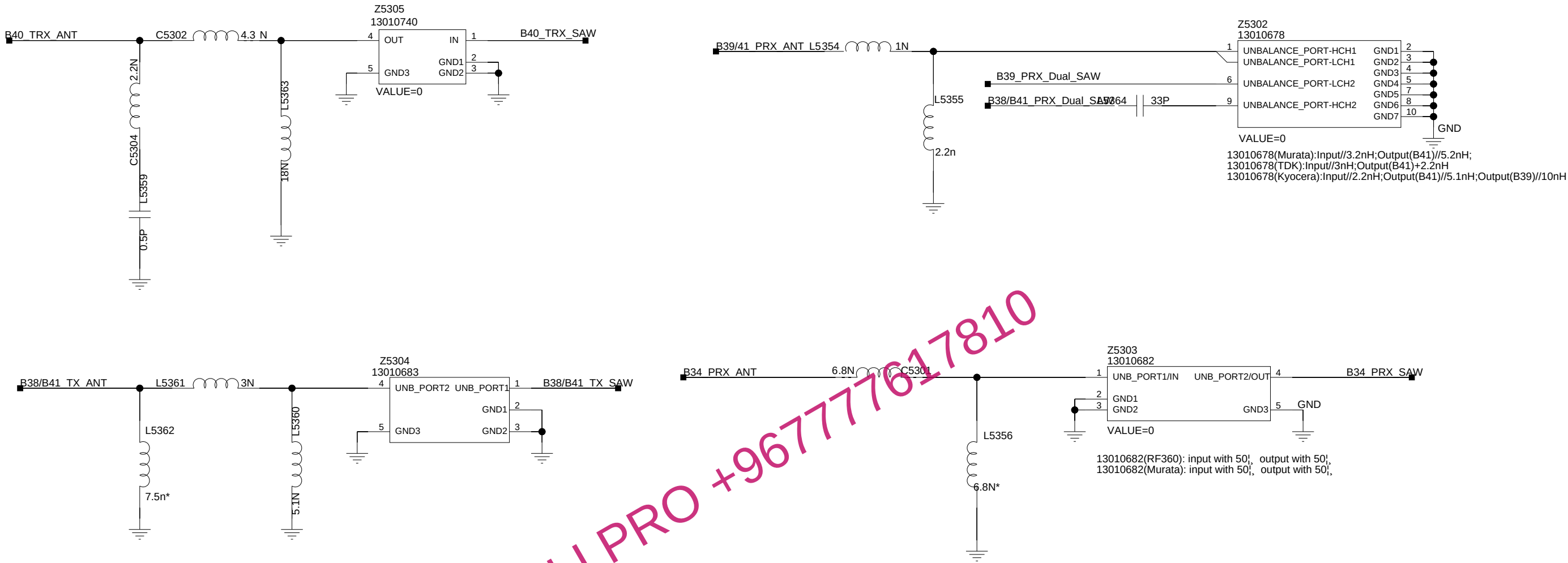


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51.TRX_MB_1

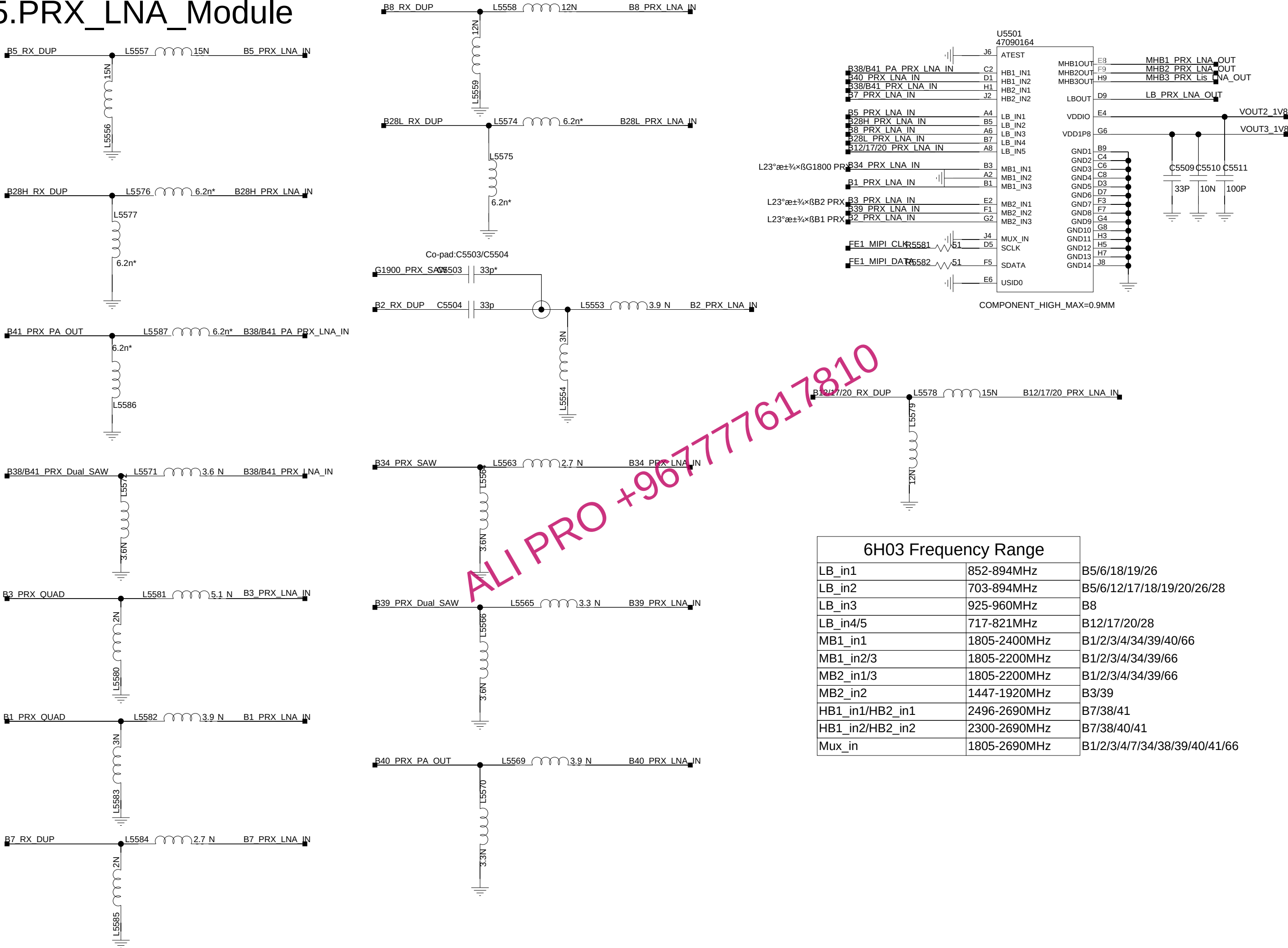


53.TRX_HB_1



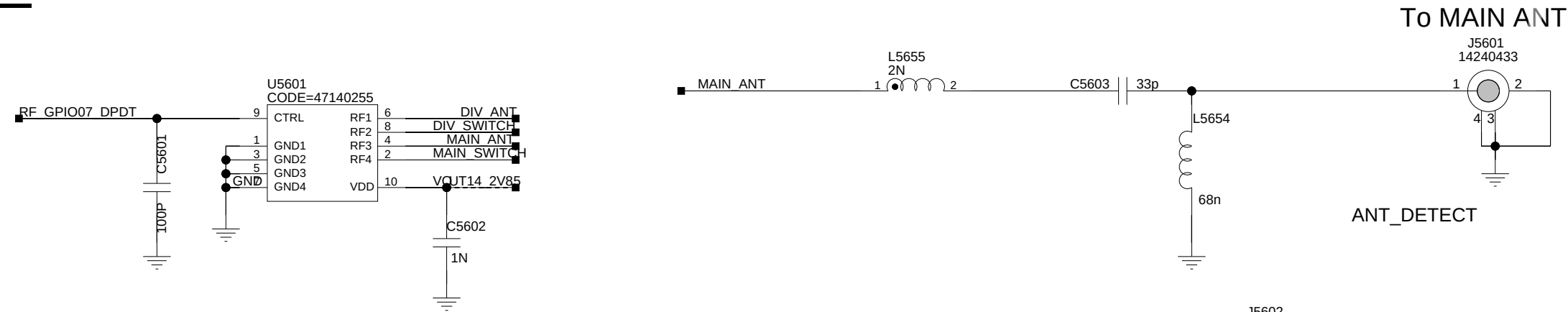
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55.PRX_LNA_Module



6H03 Frequency Range		
LB_in1	852-894MHz	B5/6/18/19/26
LB_in2	703-894MHz	B5/6/12/17/18/19/20/26/28
LB_in3	925-960MHz	B8
LB_in4/5	717-821MHz	B12/17/20/28
MB1_in1	1805-2400MHz	B1/2/3/4/34/39/40/66
MB1_in2/3	1805-2200MHz	B1/2/3/4/34/39/66
MB2_in1/3	1805-2200MHz	B1/2/3/4/34/39/66
MB2_in2	1447-1920MHz	B3/39
HB1_in1/HB2_in1	2496-2690MHz	B7/38/41
HB1_in2/HB2_in2	2300-2690MHz	B7/38/40/41
Mux_in	1805-2690MHz	B1/2/3/4/7/34/38/39/40/41/66

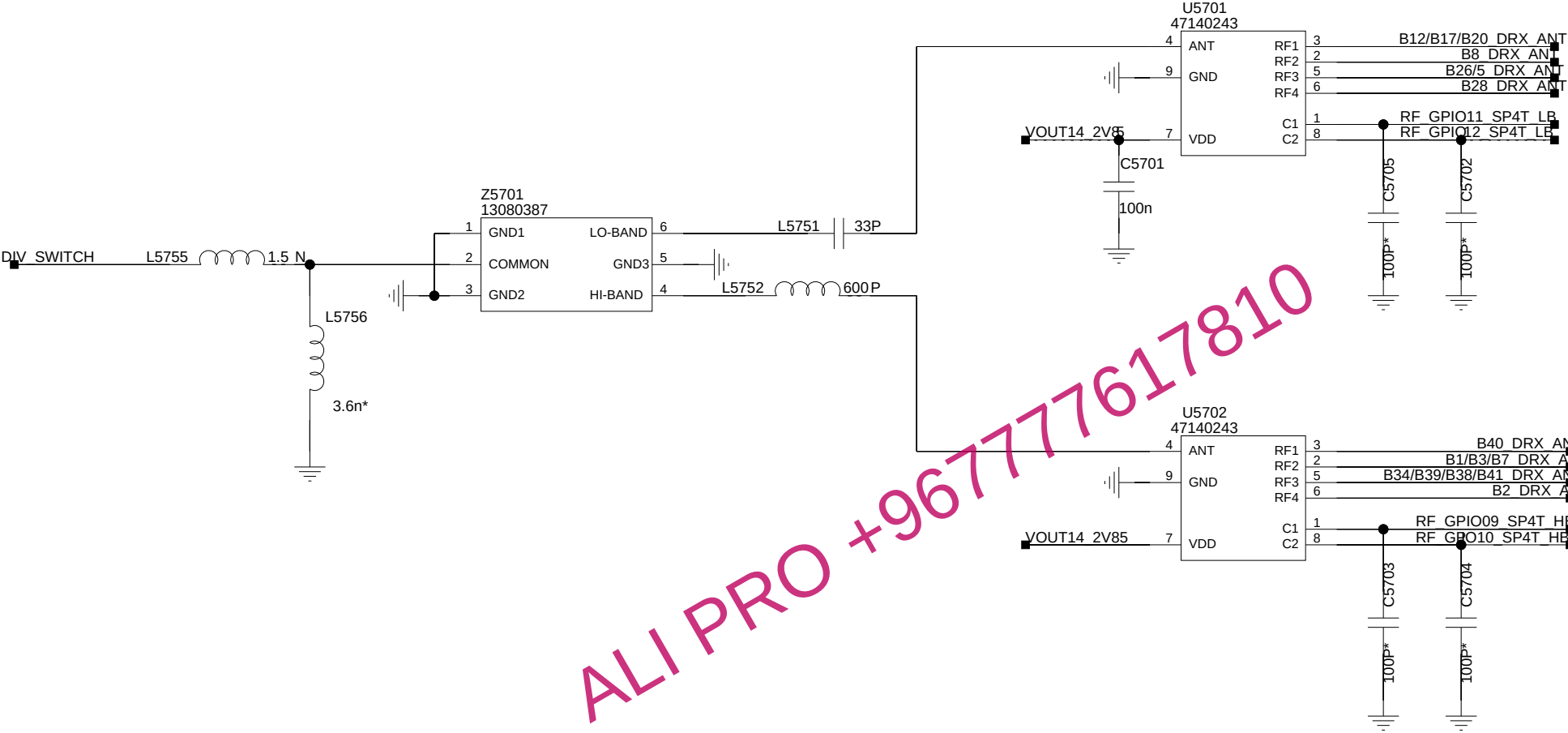
56.Main_DPDT



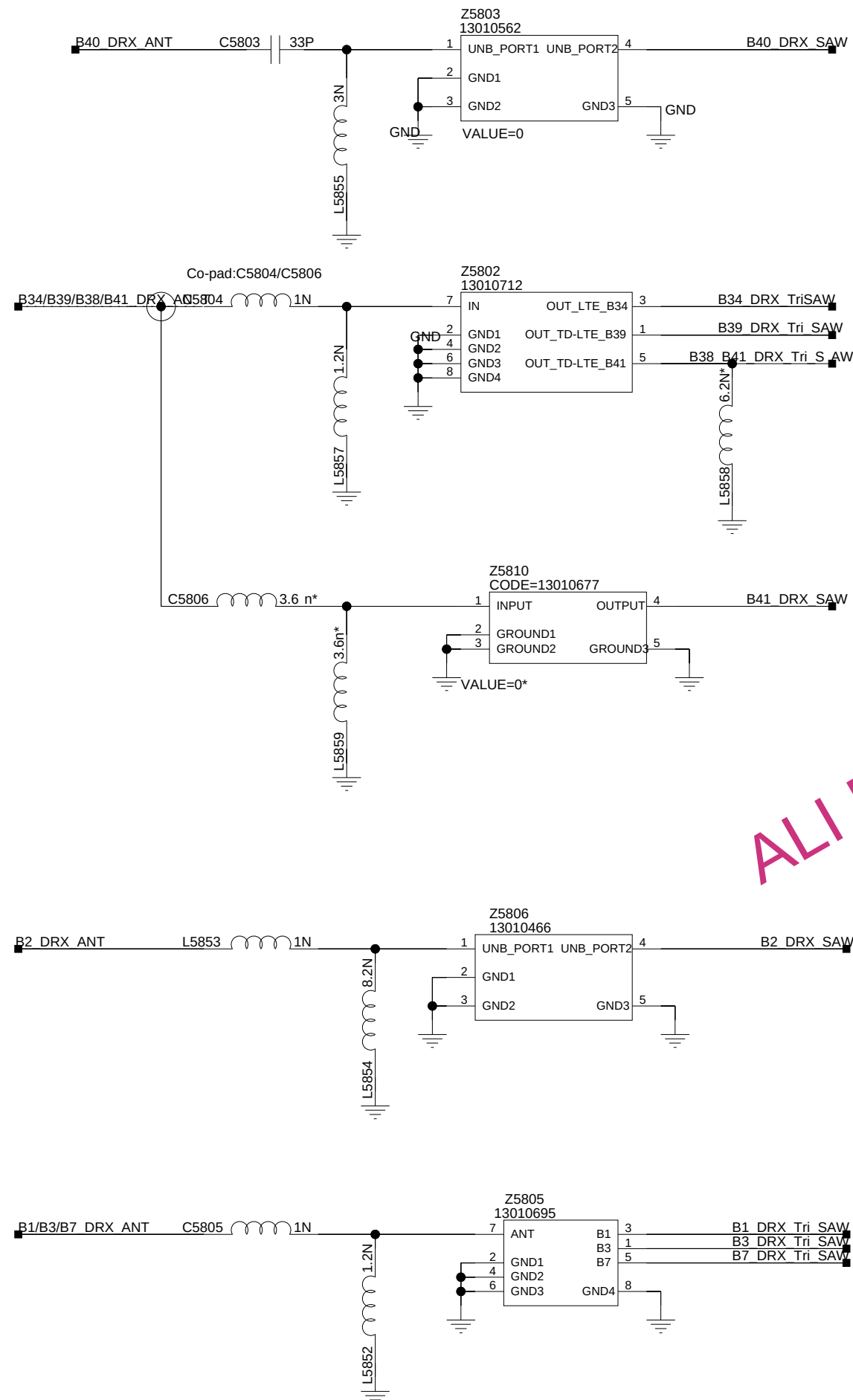
U5601(47140255) Truth Table	
CTRL1	State
0	RF1-RF2;RF3-RF4
1	RF1-RF4;RF3-RF2

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57.DRX_SWITCH

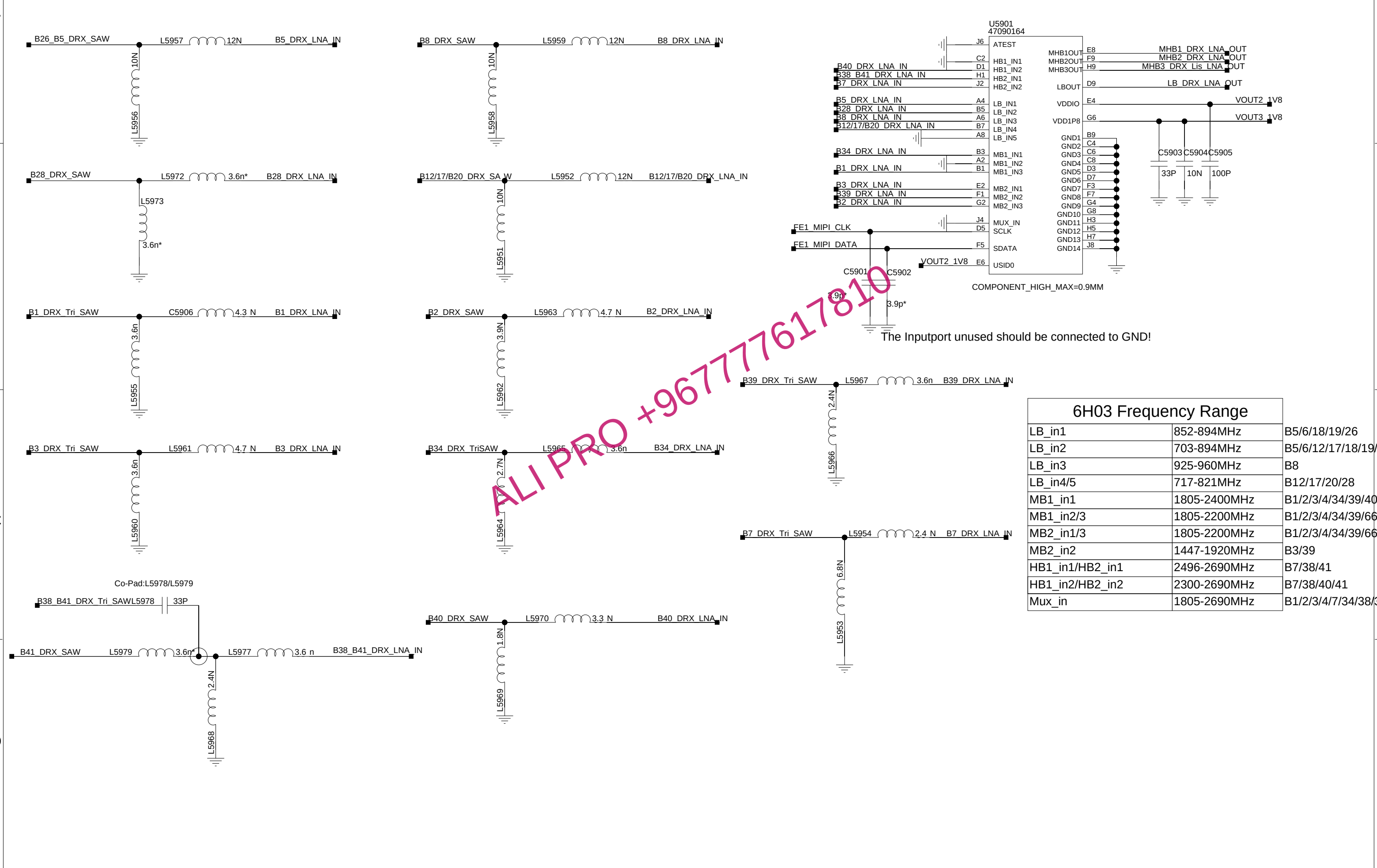


58.DRX_LMHB

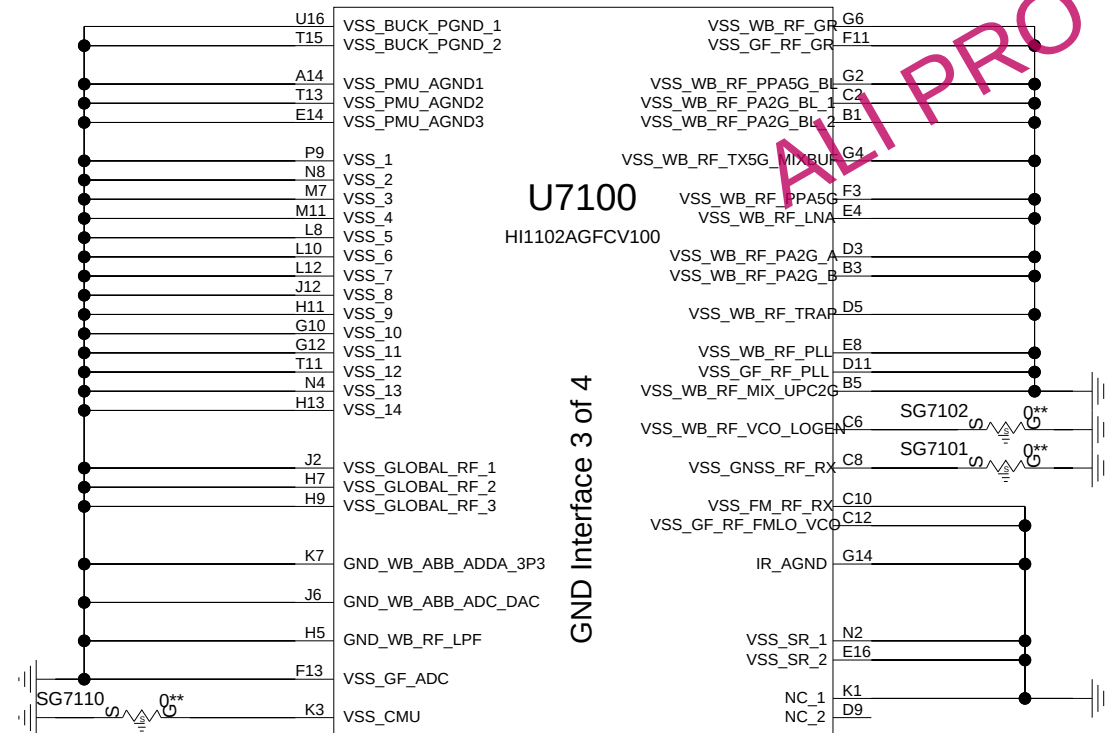
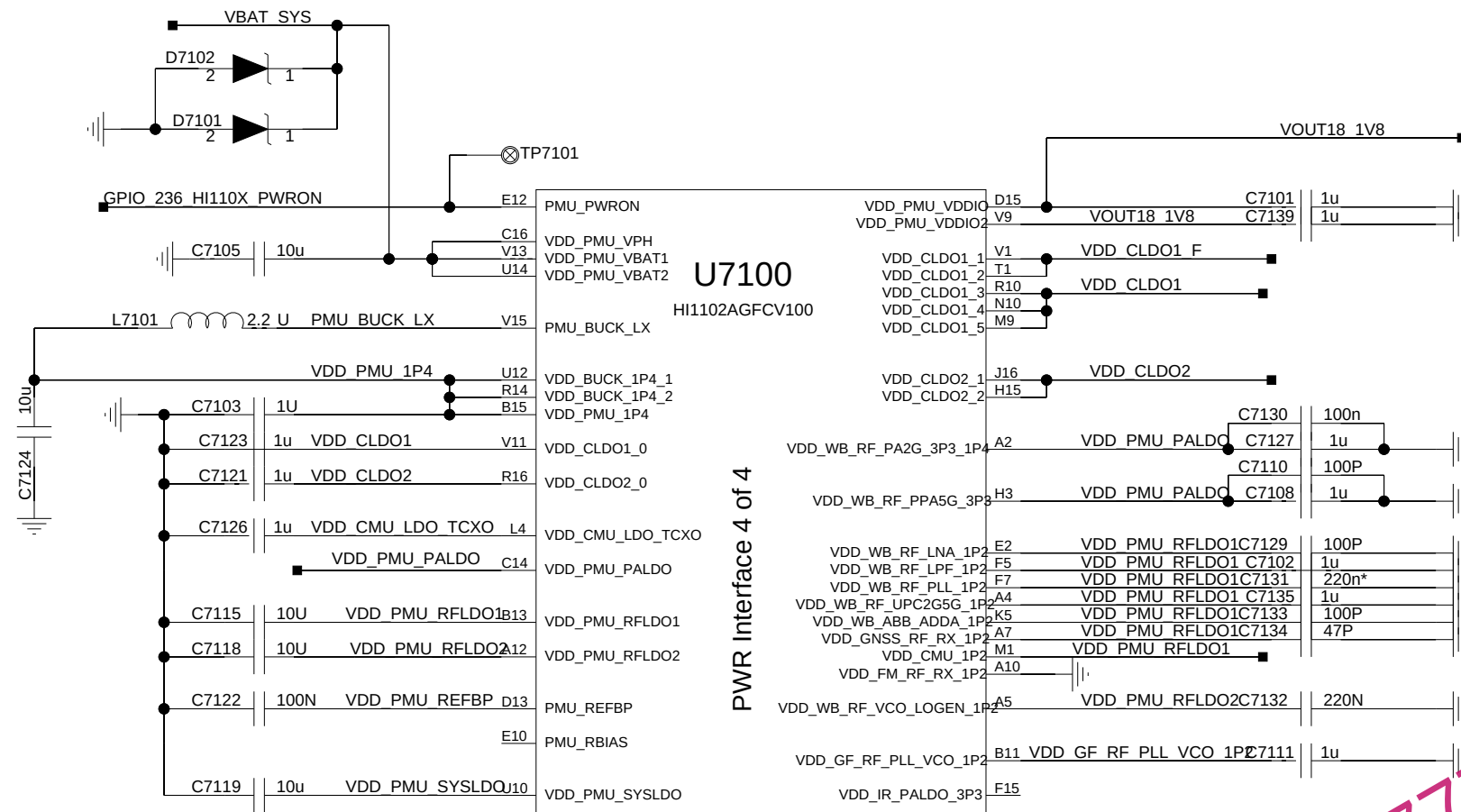


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59.DRX_LNA_Module



71 NC_POWER



VDD_PMU_1P4		(1V4)
VDD_PMU_REFBP	200mA	1V-1V2 (1V2)
VDD_PMU_CLDO1	500mA	1V1
VDD_PMU_CLDO2	500mA	1V1
VDD_PMU_LDO_TCXO	100mA	1V8/2V8
VDD_PMU_PALDO	400mA	2V-3V5 (3V3/1.4V)
VDD_PMU_SYSLDO	30mA	0V6-1V0 (0V9)
VDD_PMU_RFLDO1	250mA	1V2
VDD_PMU_RFLDO2	100mA	1V2
VDD_PMU_RFLDO3	50mA	1V2

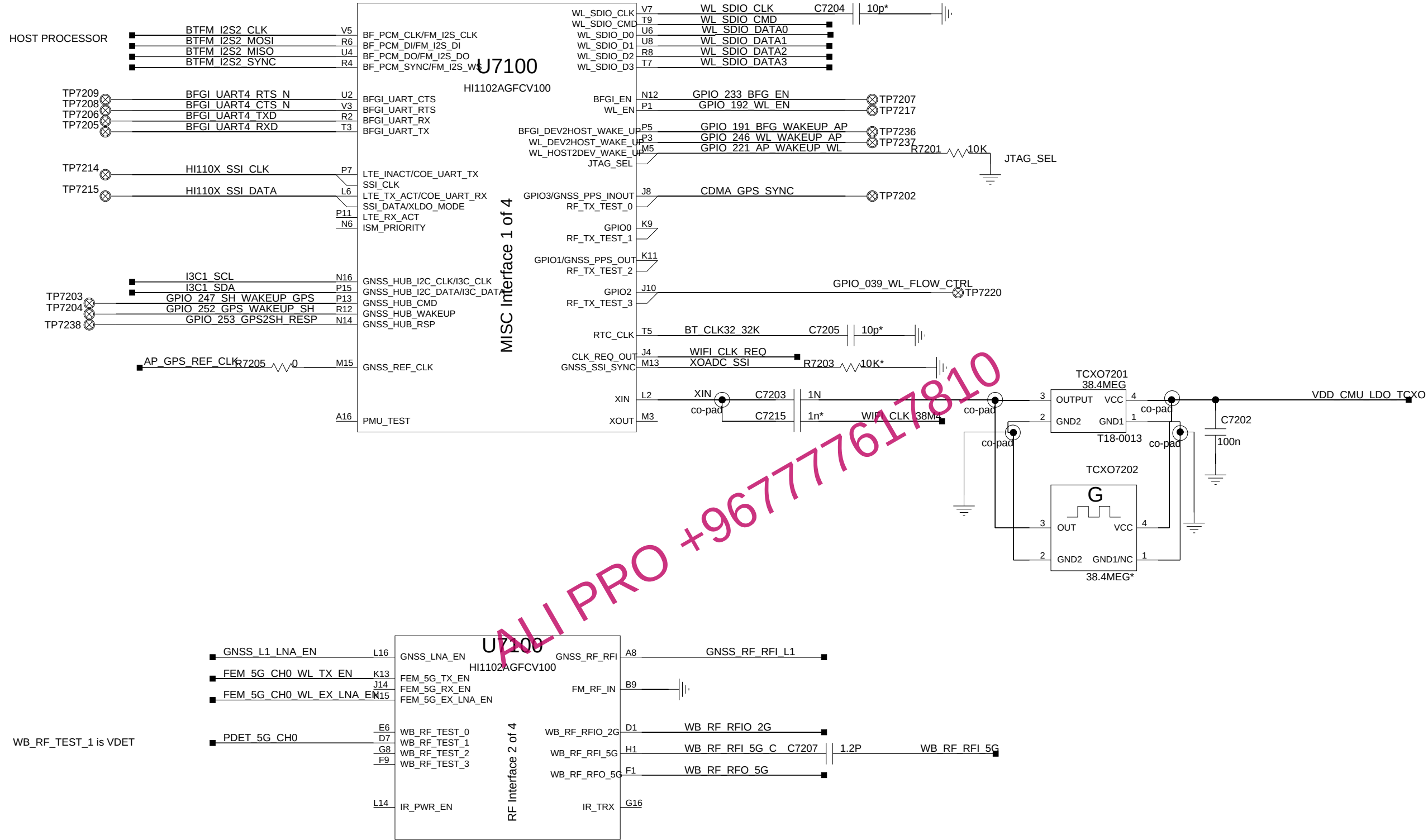
72 NC_BB

A

B

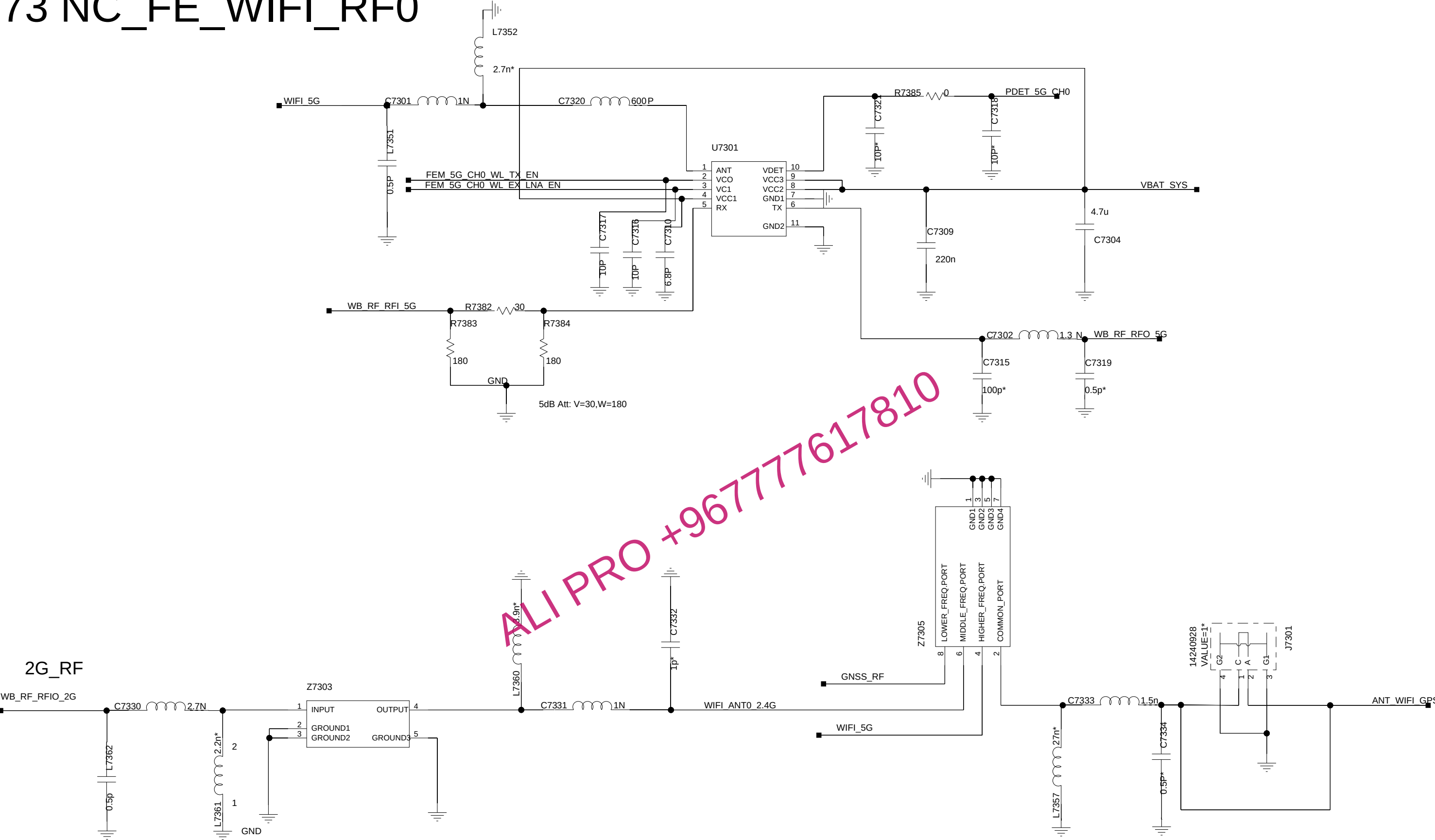
C

D



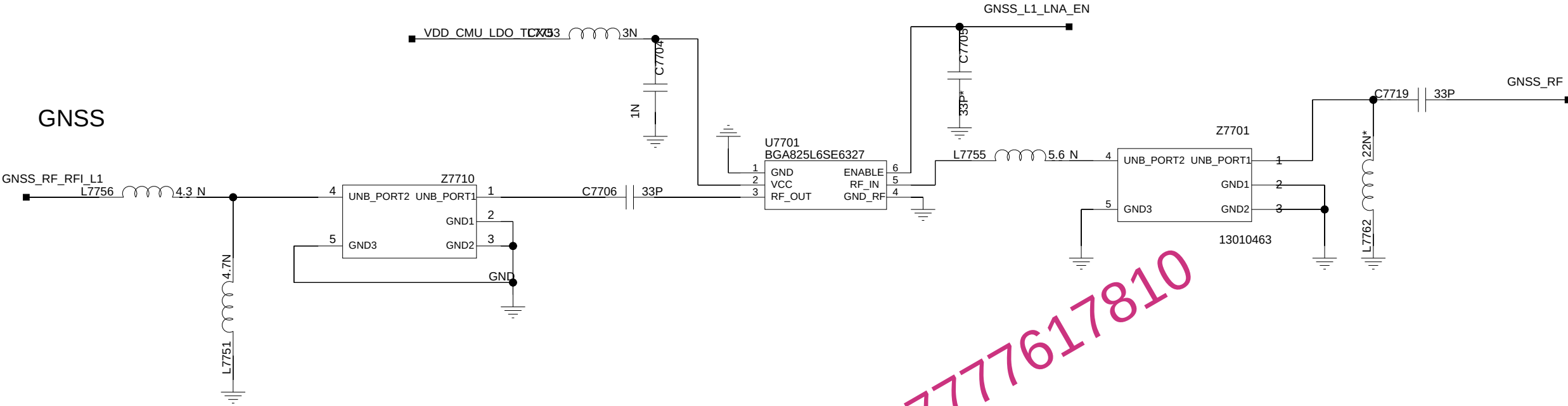
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73 NC_FE_WIFI_RF0

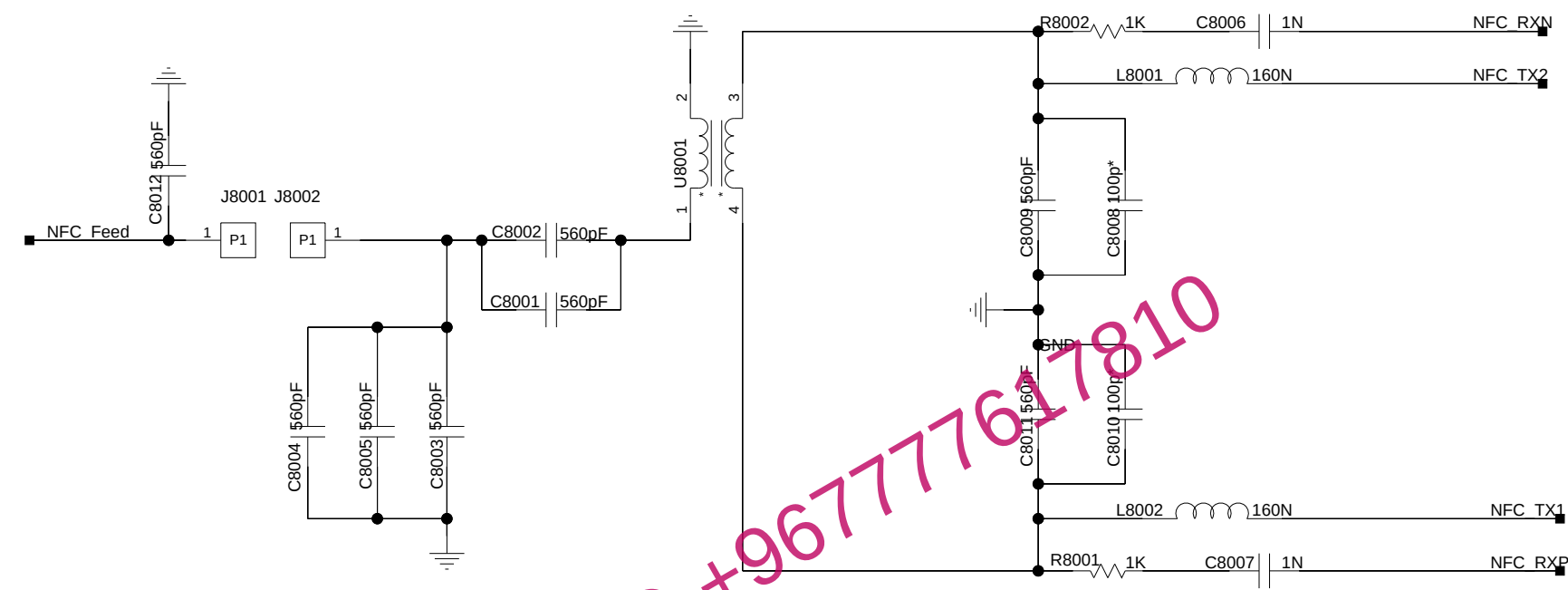


77 NC_GPS

GPS L1



80. ANT_NFC



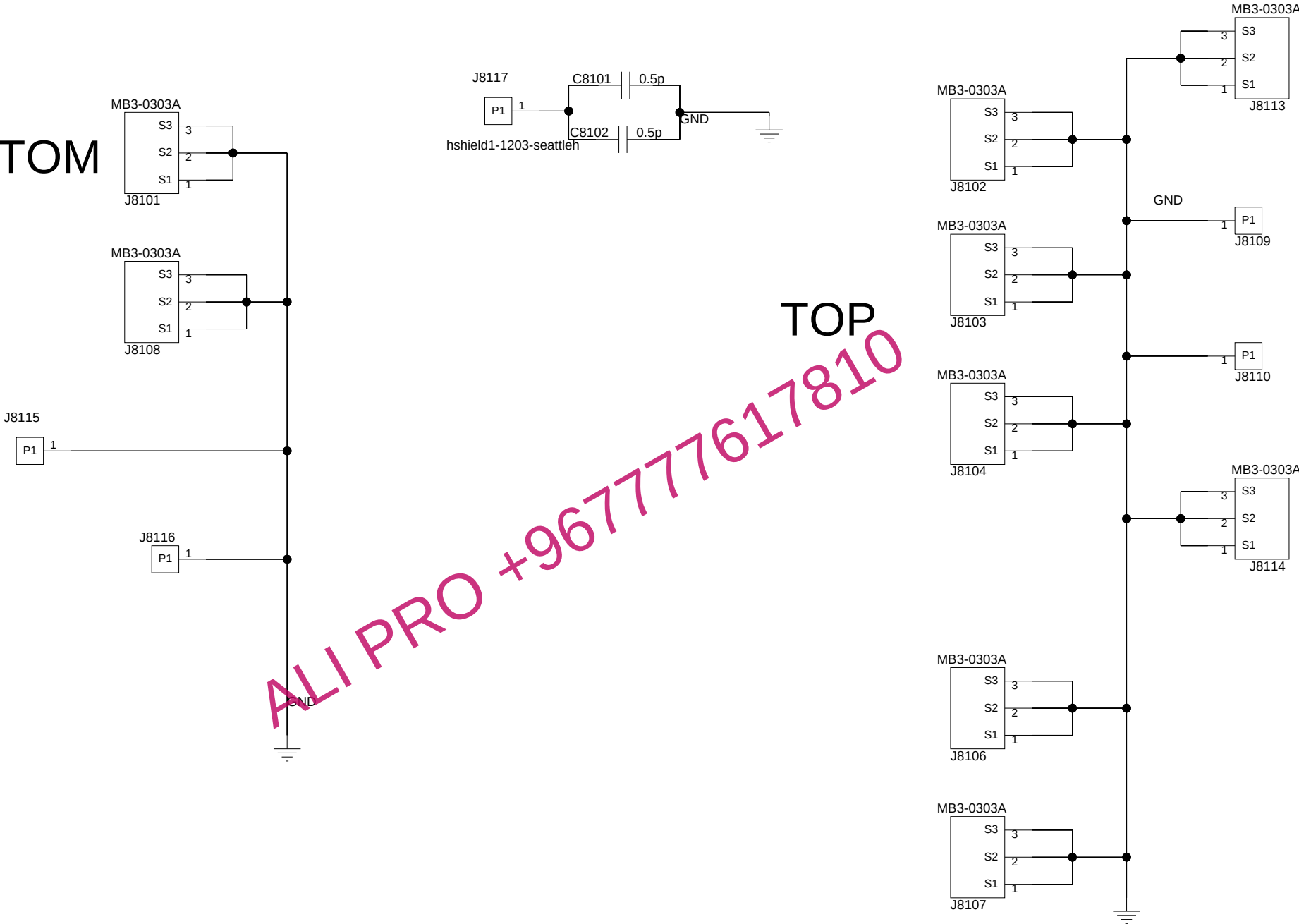
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81. ANT_GND

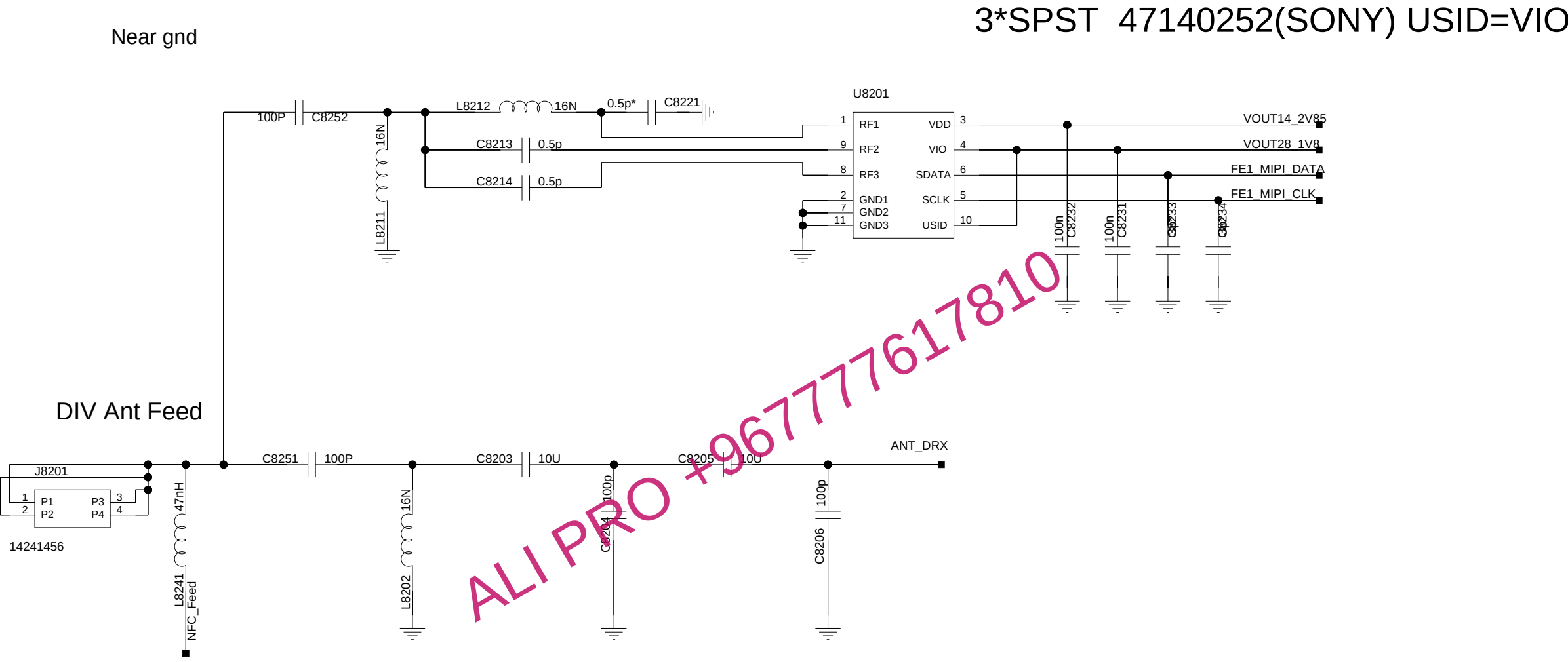
BOTTOM

TOP

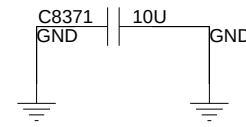
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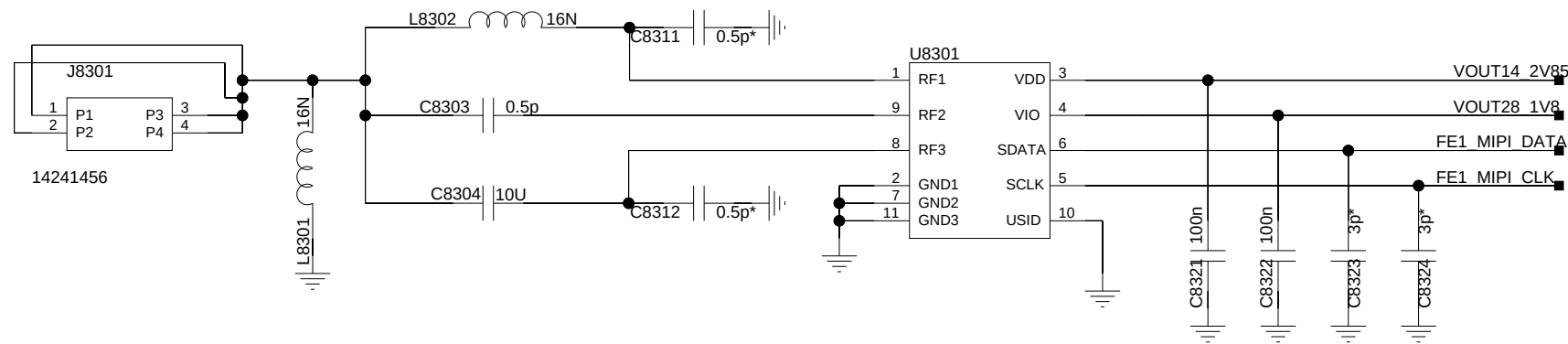
82. ANT_DIV_feed



83. ANT_DIV_tuner

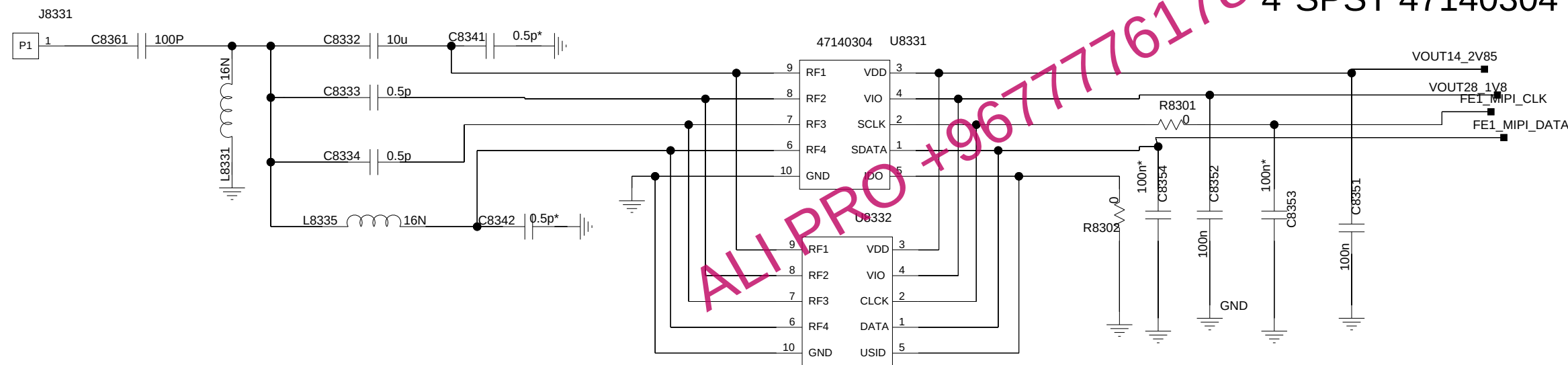


div side gnd



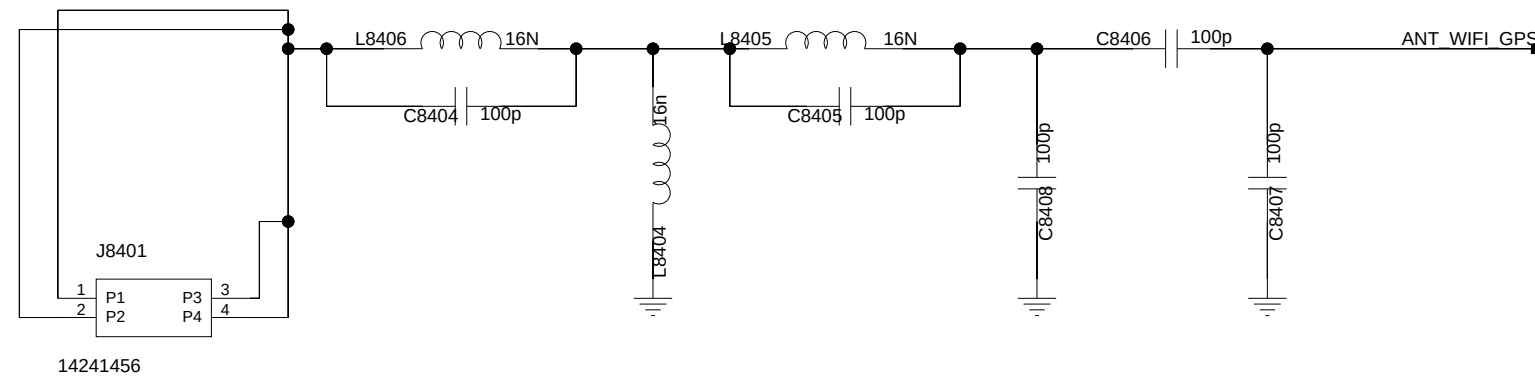
3*SPST 47140252(SONY) USID=GND

div mid gnd



4*SPST 47140304 IDO=GND

84. ANT_GPS_WIFI



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