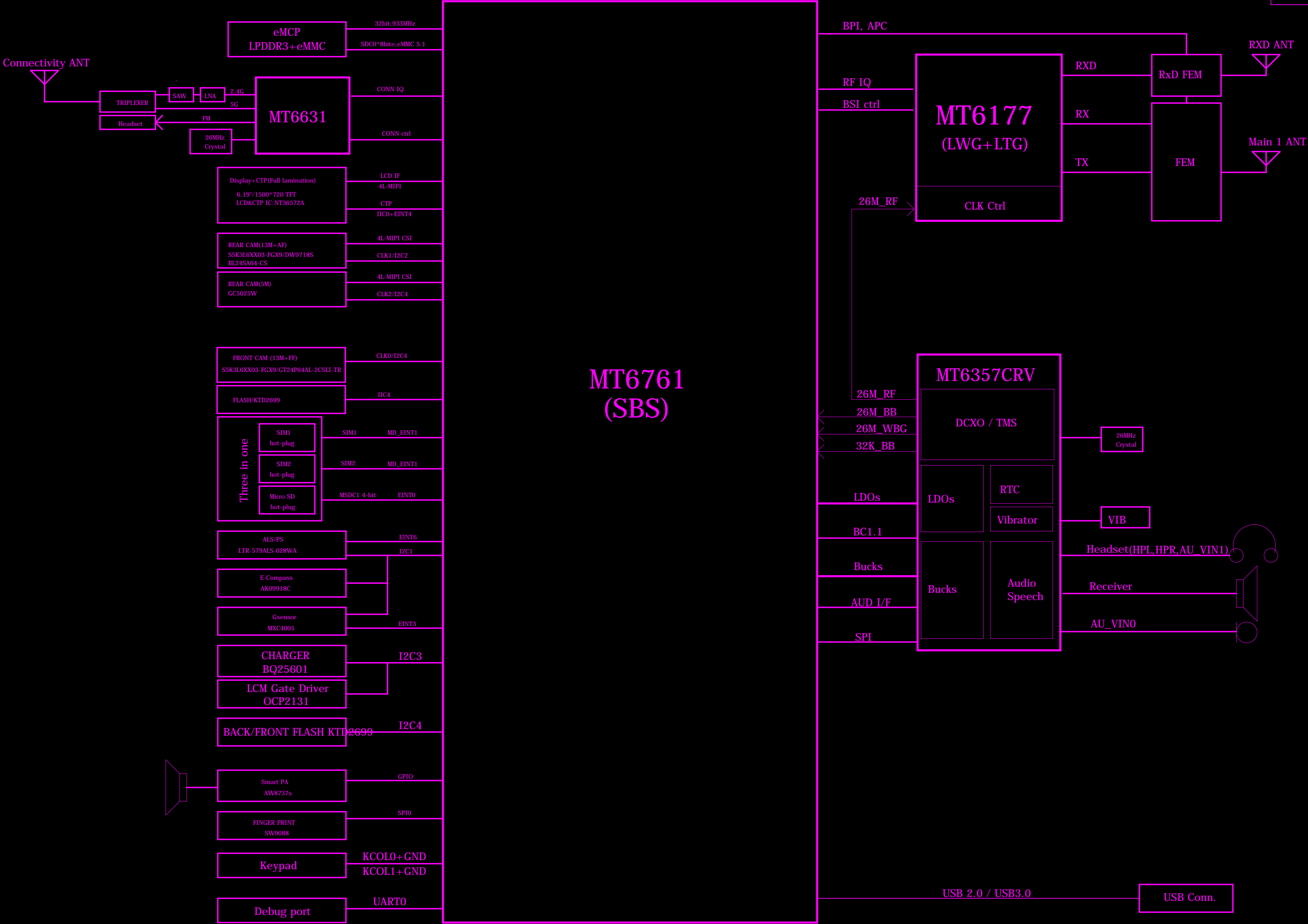


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 00_BLOCK_DIAGRAM		VERSION: V1.0	SHEET: 1 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

I2C_ID_OVERVIEW

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

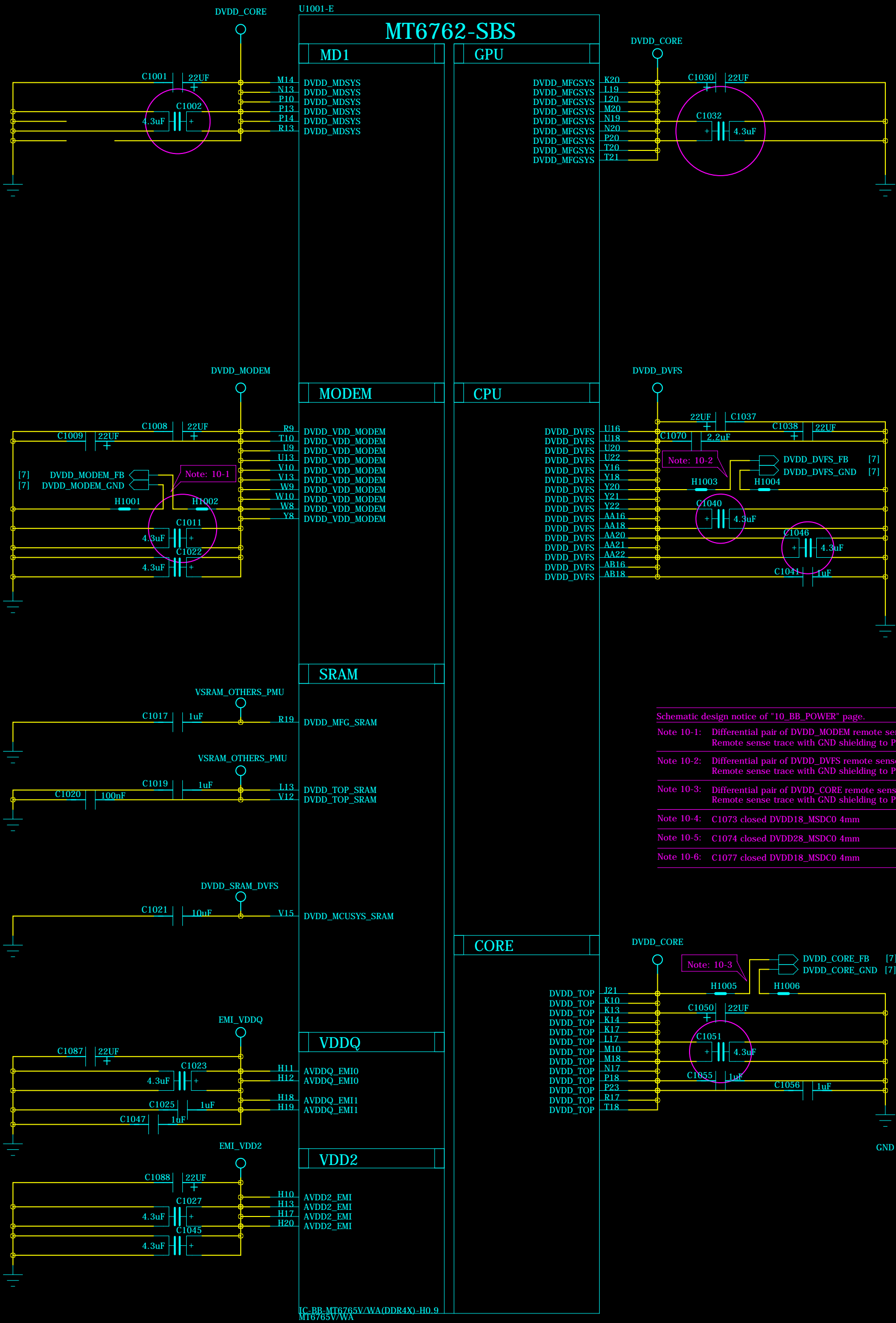
I2C	Function	I2C Spec.	Budget Timing	I2C Slave Address (7-bit mode)
I2C-0	CTP	400 Kbps	Yes.	TP£ " NT36572A£ © I2C address:write:0xC4,read:0xC5
		400 Kbps		
I2C-1	M Sensor	400 Kbps	Yes.	AK09918C-L: I2C ADDRESS:0x18(Write)/0x19(Read)
	A+Gyro Sensor	400 Kbps		
	ALS / PS Sensor	400 Kbps	Yes.	LTR-579ALS-028WA/ ALS + PS I2C address: 0x53 (Write:0xA6, Read:0xA7)
	Gsensor	400 Kbps	Yes.	MXC4005XC: I2C ADDRESS:0x2A(Write)/0x2B(Read) £ " default £ © KXTJ3-1057: I2C ADDRESS:0x1C(Write)/0x1D(Read) £ " reserve £ ©
I2C-2	REAR CAMERA Wide (13M+AF)	400 Kbps	Yes.	Back camera I (S5K3L6XX03-FGX9) I2C address: (Write:0x20, Read:0x21) Drv IC(DW9718S) I2C address: (Write:0x18, Read:0x19) EEPROM IC(BL24SA64-CS) I2C address: (Write:0xA0, Read:0xA1)
I2C-3	Charger IC	400 Kbps	Yes.	BQ25601: I2C ADDRESS:0xD6(Write)/0xD7(Read)
	LCM Gate Driver	400 Kbps	Yes.	OCP2131: I2C Address=0x3E write:0x7C read:0x7D
I2C-4	Flash LED Driver	400 Kbps	Yes.	KTD2699: I2C Address=0x63 write:0xC6 read:0xC7
	REAR CAMERA Tele	400 Kbps	Yes.	Back camera(GC5025W) I2C address: (Write:0x6E, Read:0x6F)
	FRONT CAMERA	400 Kbps	Yes.	Front camera sensor(S5K3L6XX03-FGX9) I2C address: (Write:0x5A Read:0x5B) Front camera EEPROM(GT24P64AL-2CSLI-TR) I2C address: (Write:0xA2 Read:0xA3)

Note : I2C Spec. : Standard mode (100 kbps) and Fast mode (400 kbps), Fast mode Plus (1 Mbps) and High-speed mode (3.4 Mbps)

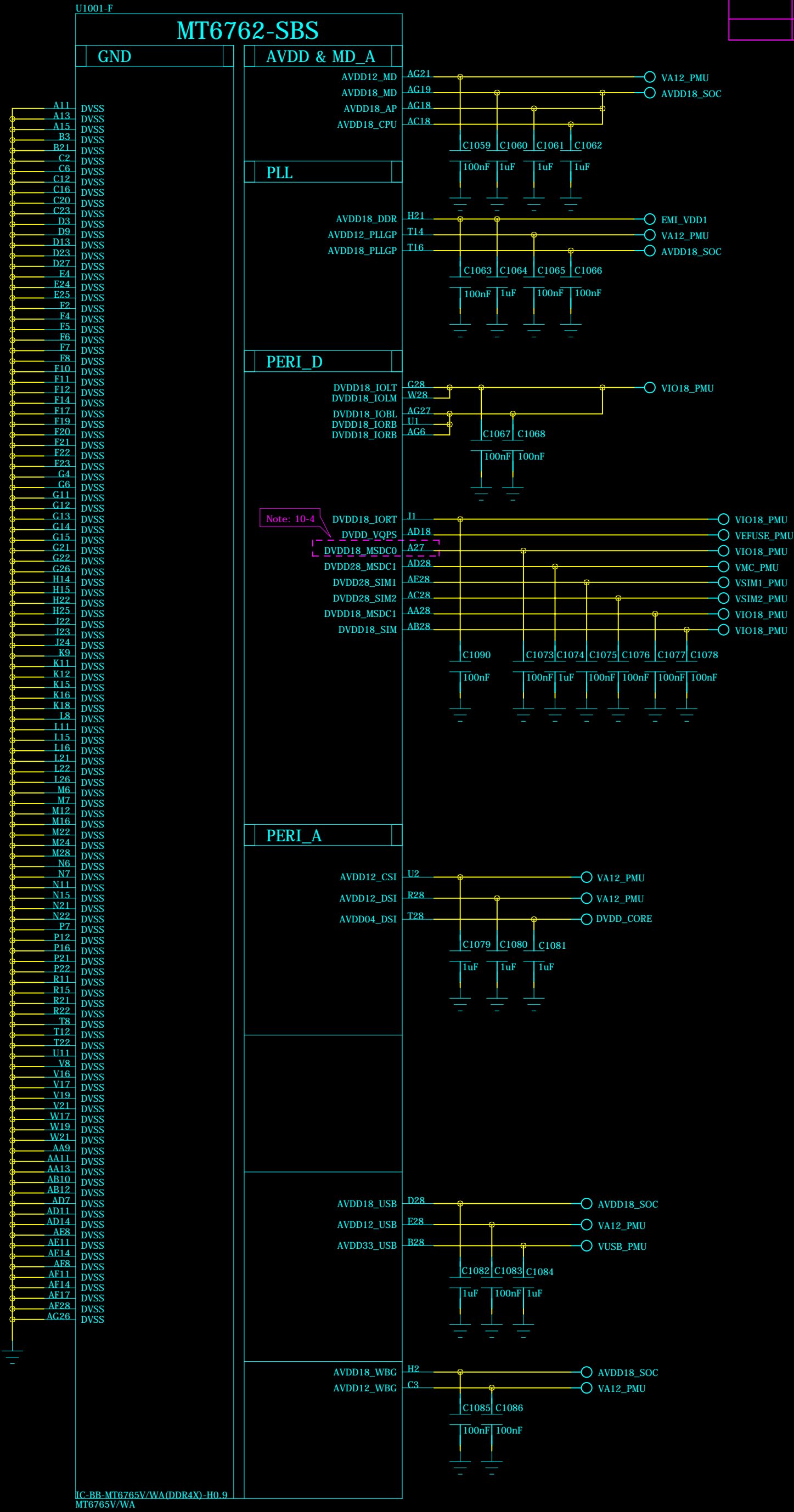
COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 01_I2C_ID_OVERVIEW		VERSION: V1.0	SHEET: 2 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

BB_POWER

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

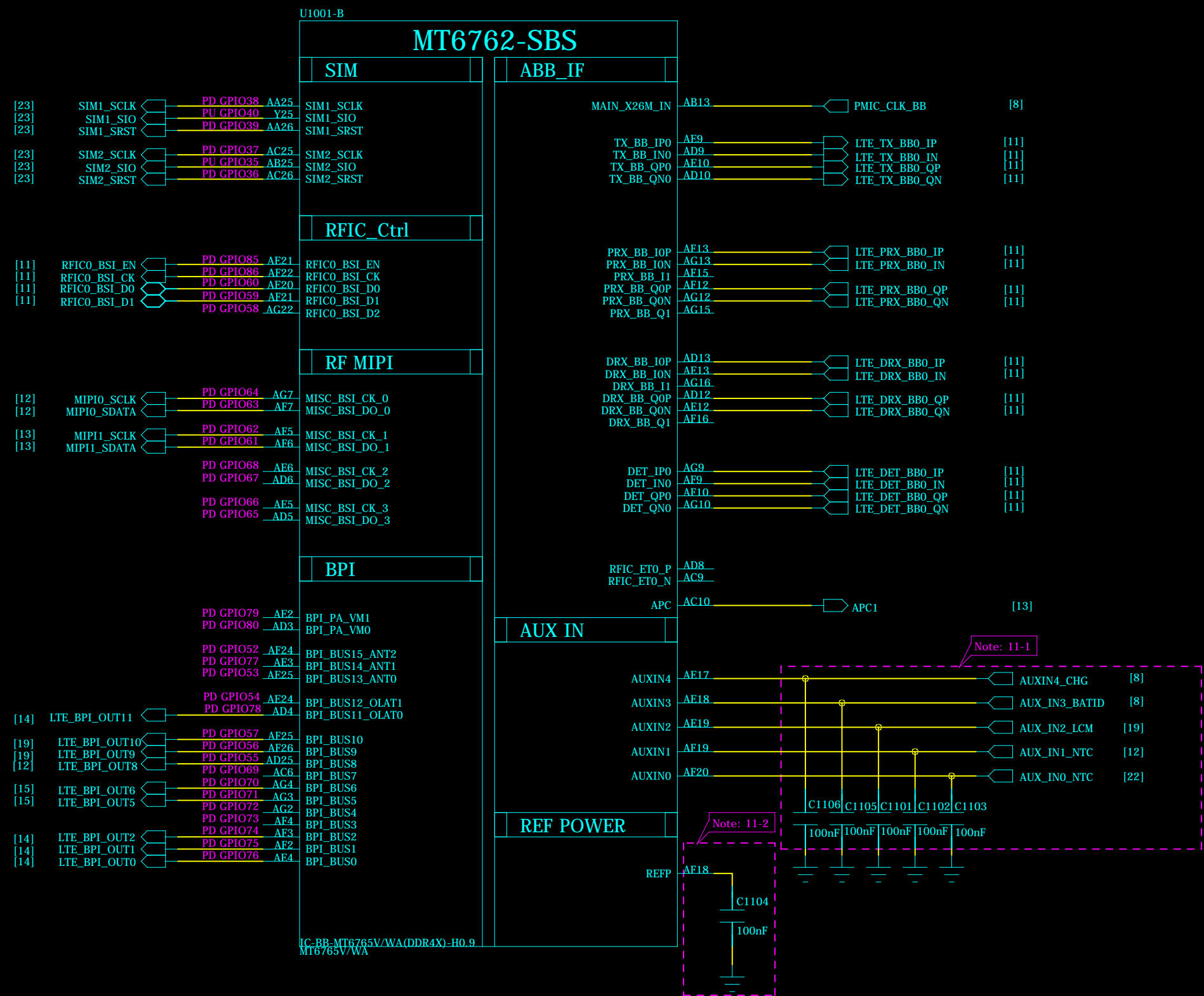


- Schematic design notice of "10_BB_POWER" page.
- Note 10-1: Differential pair of DVDD_MODEM remote sense must be close to BB's ball. Remote sense trace with GND shielding to PMIC (Differential)
- Note 10-2: Differential pair of DVDD_DVFS remote sense must be close to BB's ball. Remote sense trace with GND shielding to PMIC (Differential)
- Note 10-3: Differential pair of DVDD_CORE remote sense must be close to BB's ball. Remote sense trace with GND shielding to PMIC (Differential)
- Note 10-4: C1073 closed DVDD18_MSDC0 4mm
- Note 10-5: C1074 closed DVDD28_MSDC0 4mm
- Note 10-6: C1077 closed DVDD18_MSDC0 4mm



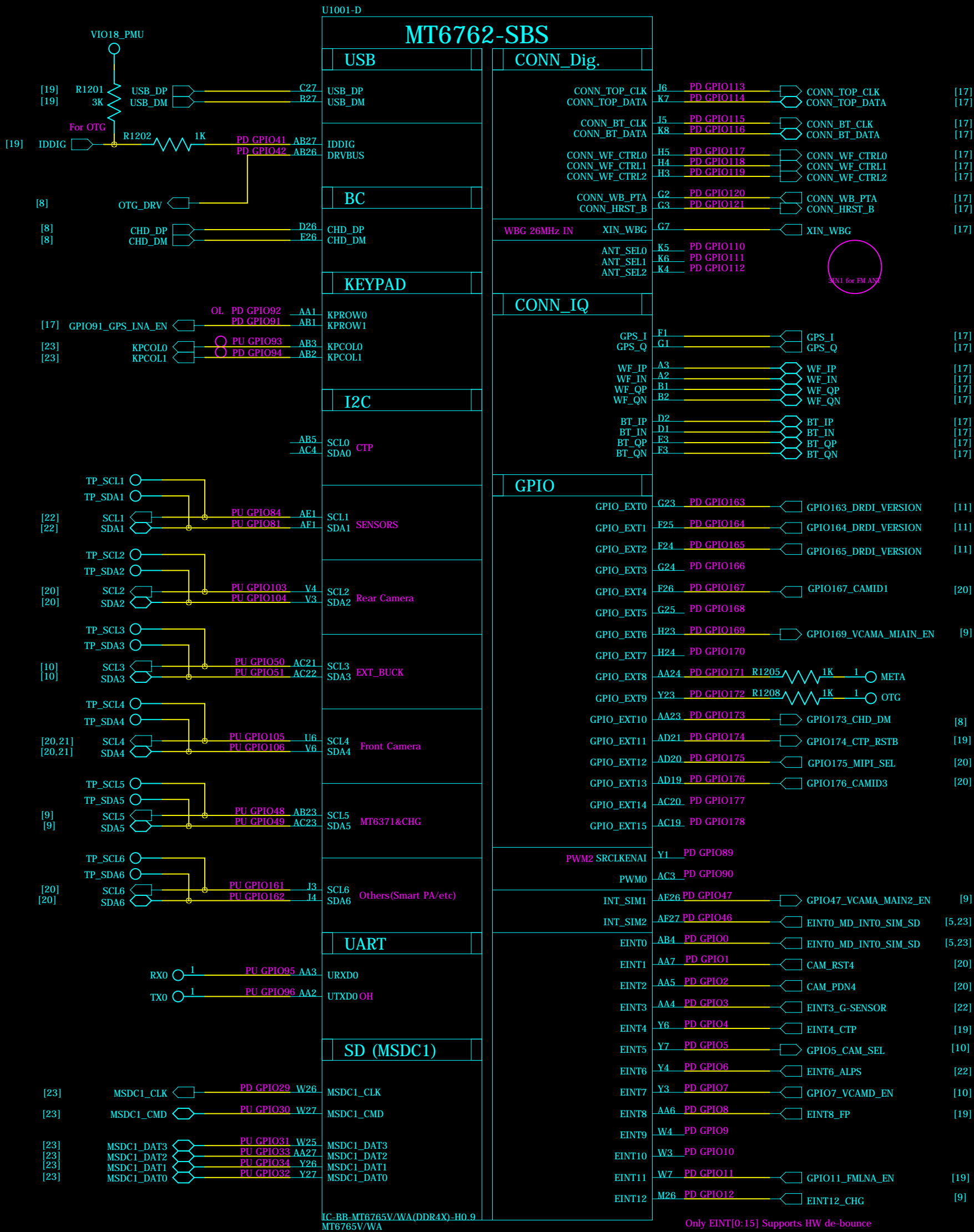
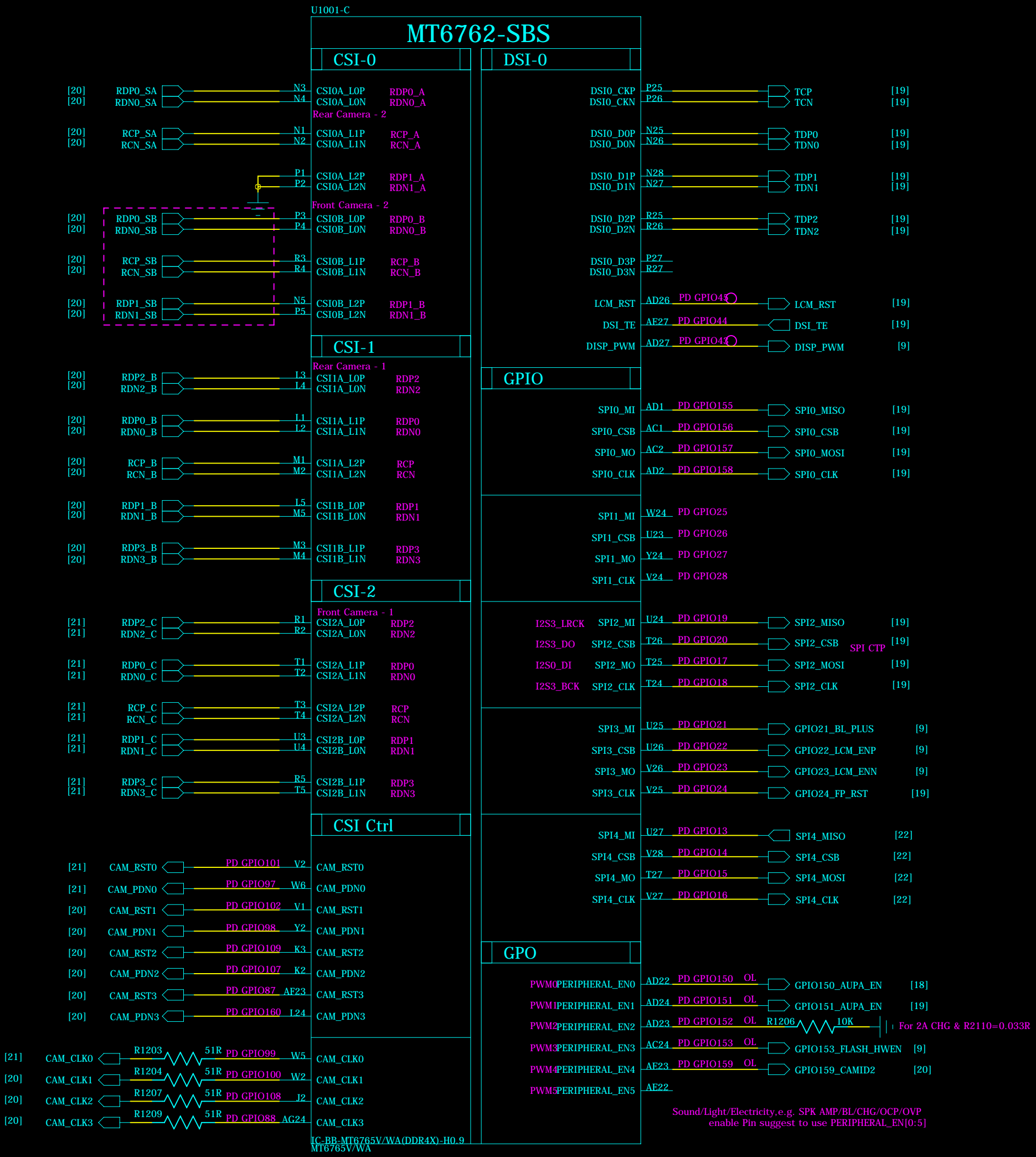
SHIELDING&LOGO&SN&MARK											
SH1002	SH1003	SH1004	SH1009	TL1001	WPL1001	MARK1001	MARK1002	MARK1003	MARK1004	SH1005	SH1006
SH-RF-H6116	SH-OTG-H6116	SH-4C-H6116	SH-JT-H6116	TRANSION-2.5H		MARK_1.0	MARK_1.0	MARK_1.0	MARK_1.0	SH-WCN-H6116	SH-FZ-H6116
SH1007	SH1008	SH1009	SH1010	SH1011	SH1012	SH1013	SH1014	SH1015	SH1016	SH1017	SH1018
SH-PMU-H6116	SH-BB-H6118	SH-PMU-H6116	SH-BB-H6118	SH-PMU-H6116	SH-BB-H6118	SH-PMU-H6116	SH-BB-H6118	SH-PMU-H6116	SH-BB-H6118	SH-PMU-H6116	SH-BB-H6118

COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 10_BB_POWER		VERSION: V1.0	SHEET: 3 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality		CONFIDENTIAL	



COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 11_BB_I		VERSION: V1.0	SHEET: 4 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



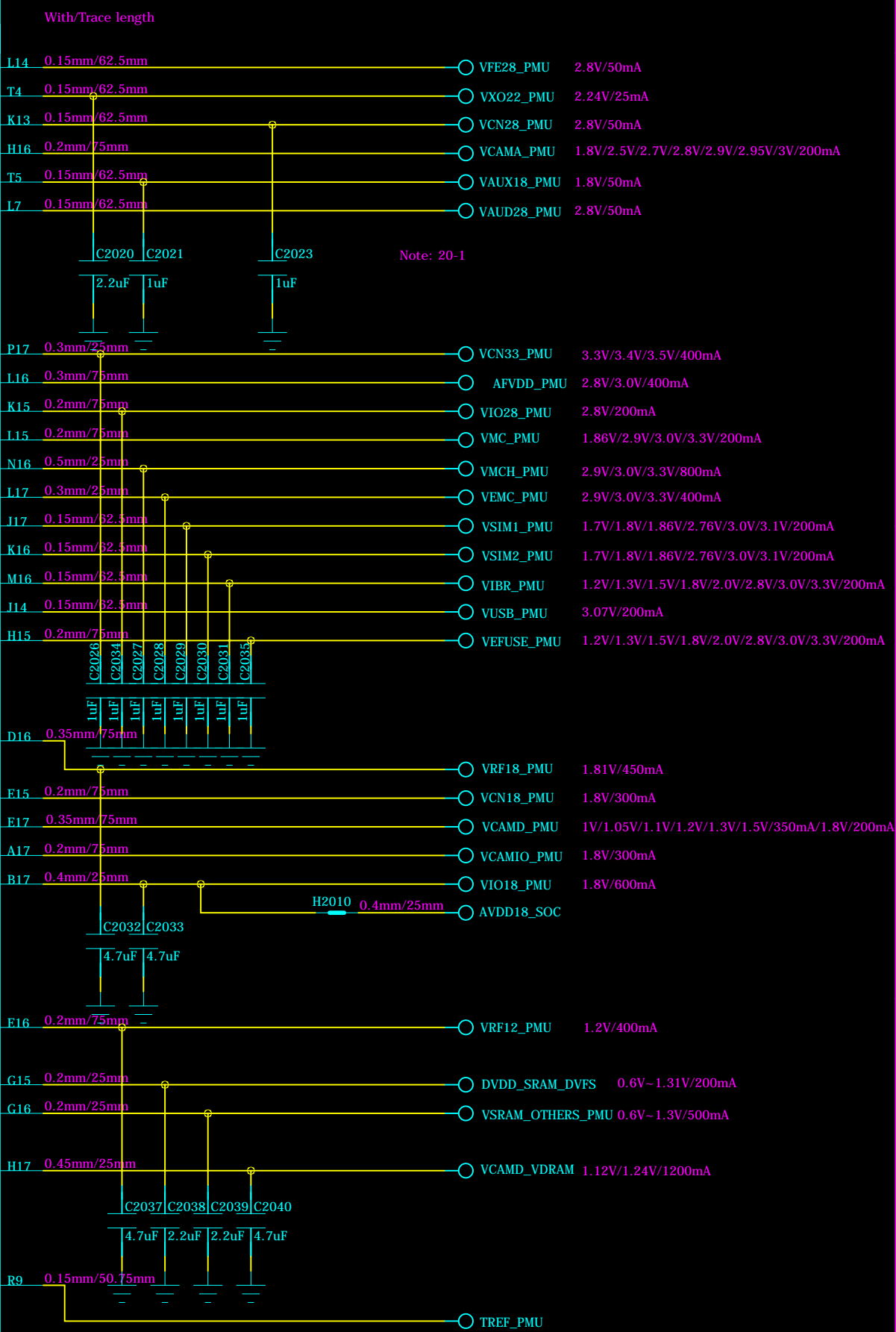
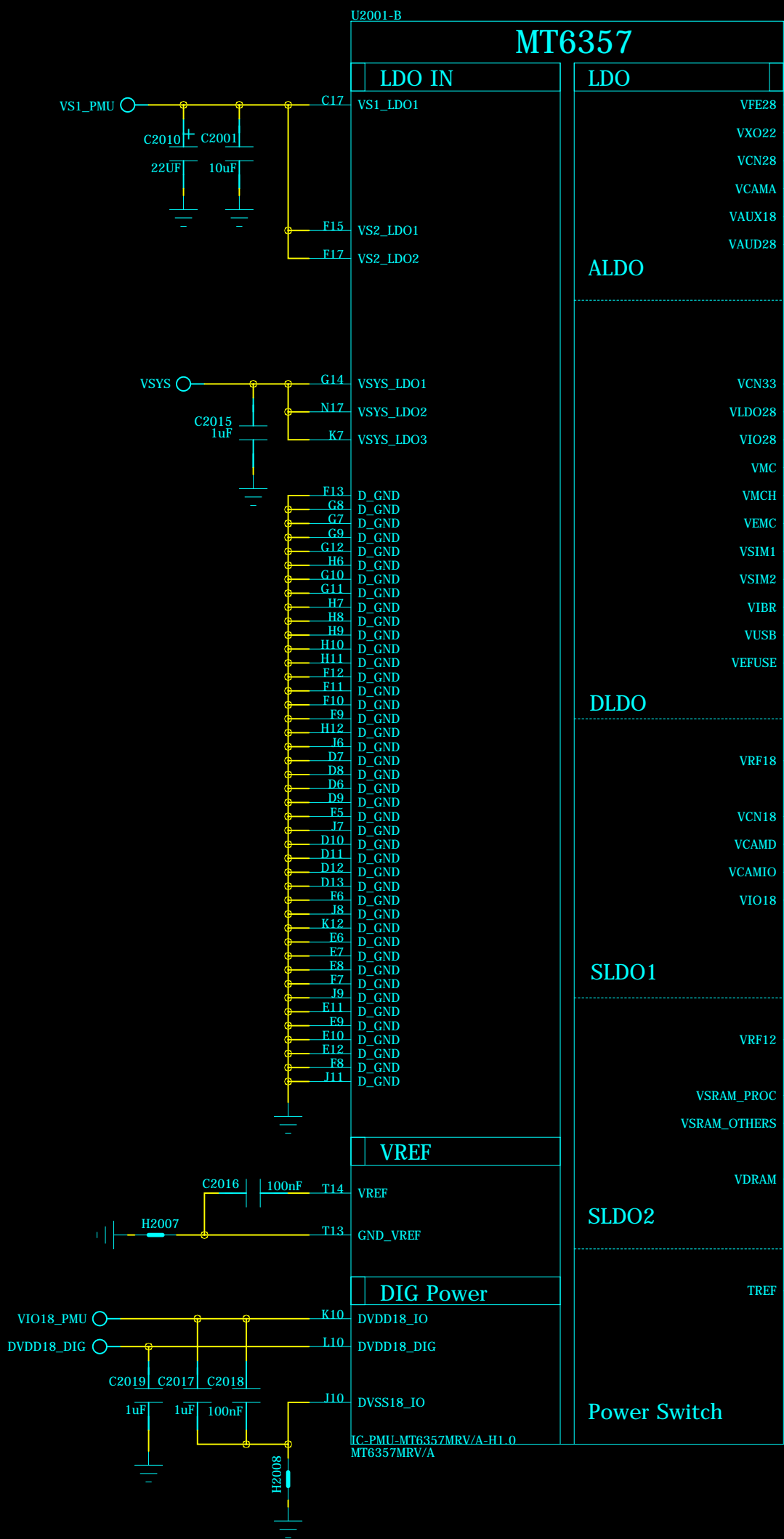
COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 12_BB_II		VERSION: V1.0	SHEET: 5 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

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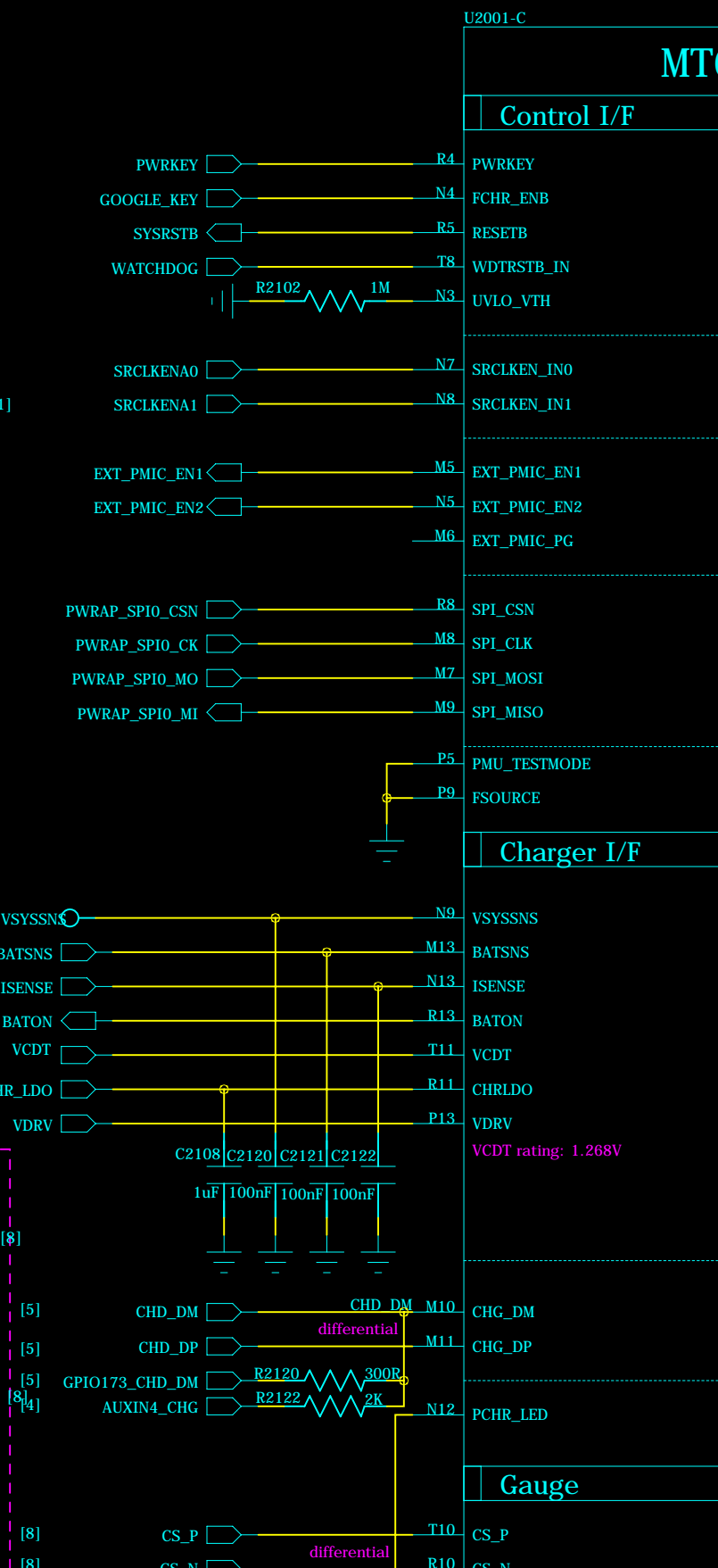
COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 13_BB_III		VERSION: V1.0	SHEET: 6 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

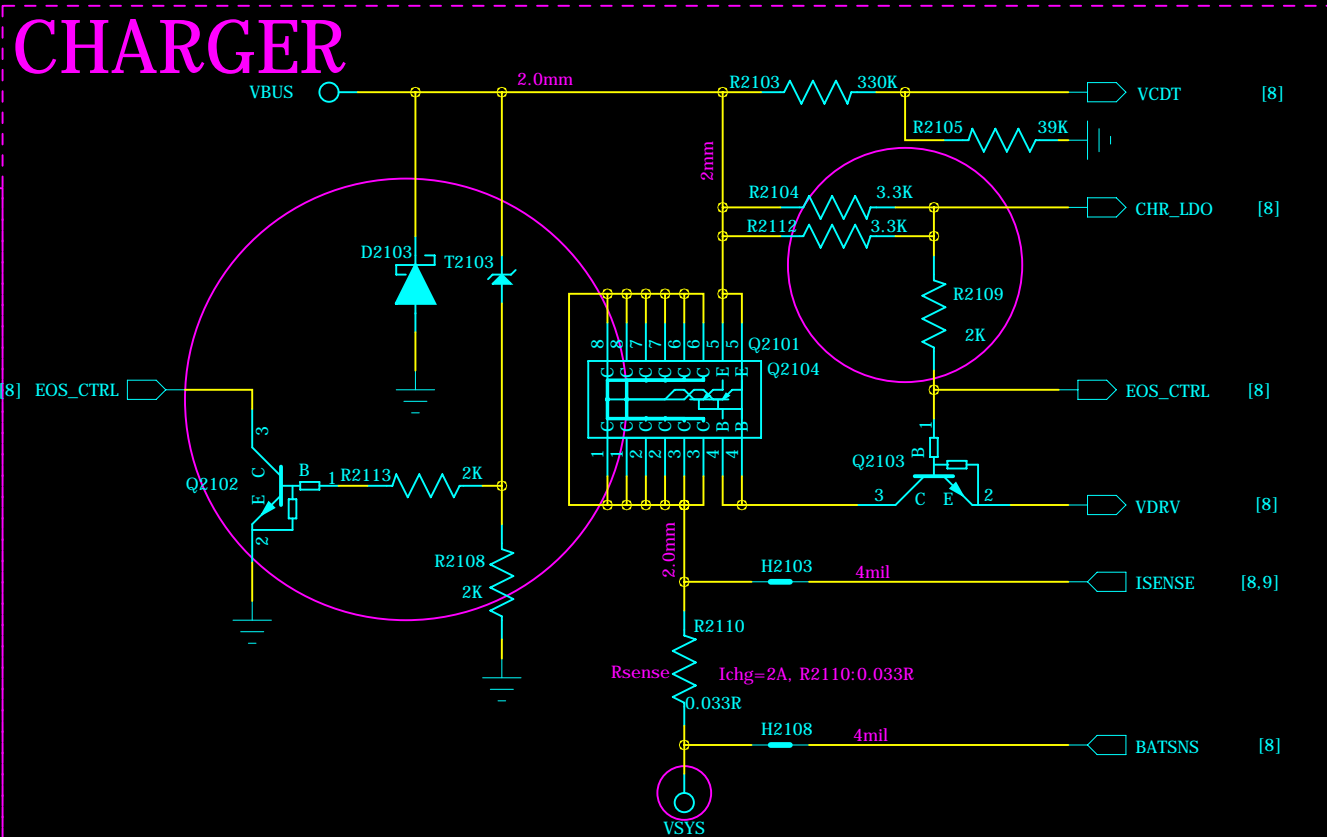
PMU_LDO



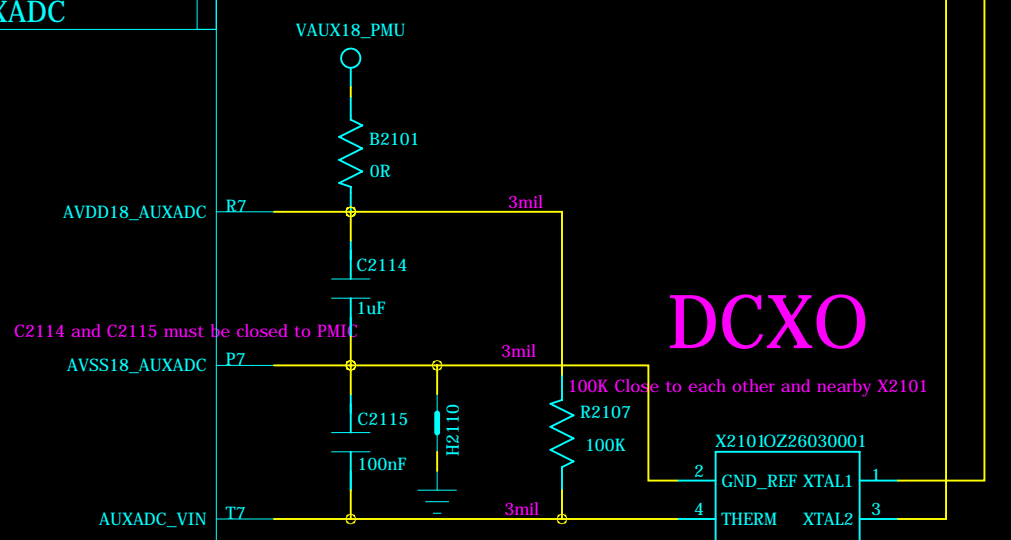
PMU_CLK



CHARGER



DCXO



Route AVDD18_AUXADC/AUXADC_VIN as differential trace (3 mil each) with well GND shielding and route AVSS18_AUXADC with 3mil trace width under
AVDD18_AUXADC/AUXADC_VIN trace to provide return current path.

Connect PMIC pin AVDD18_AUXADC to AUXADC_GND first, and then connect it to main GND by a inner-layer short and a via.

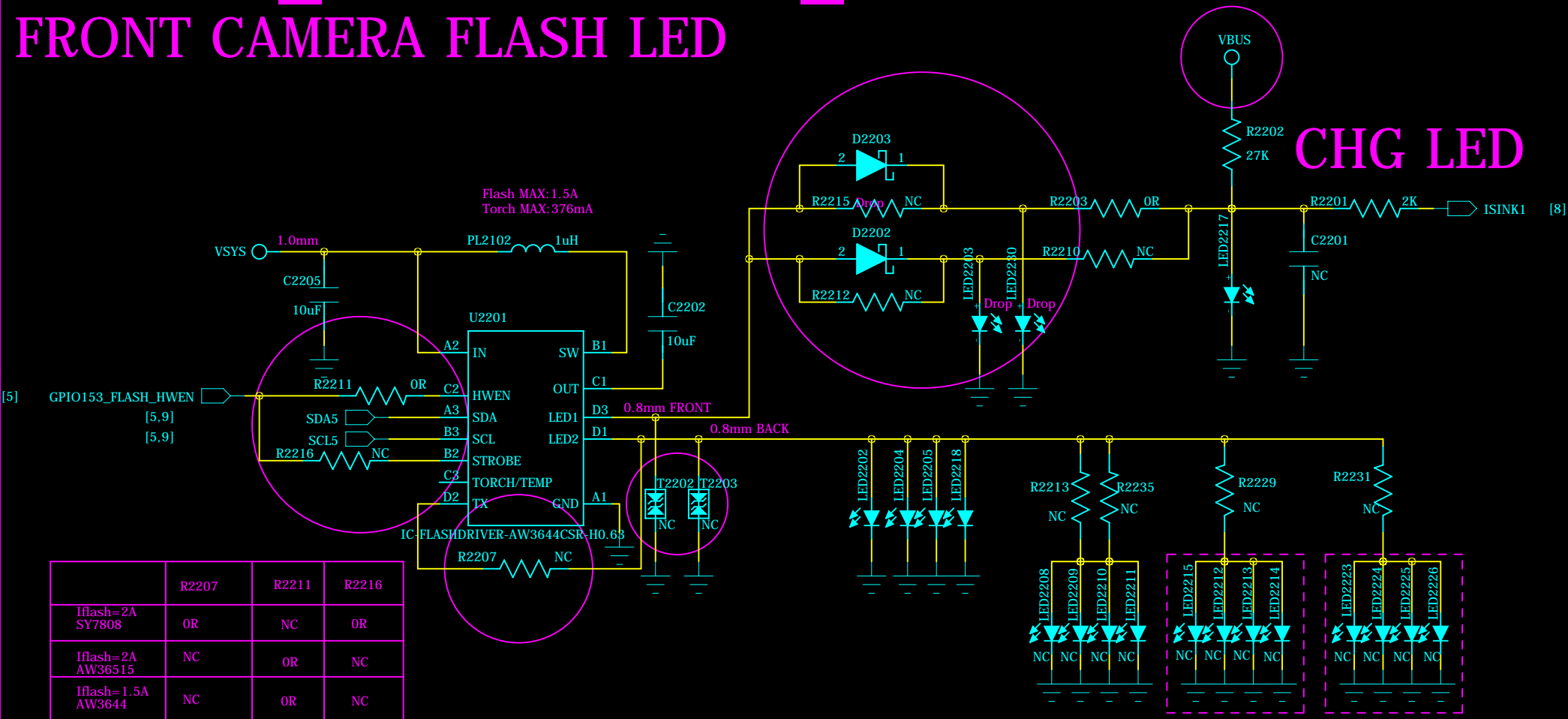
Connect VAUX18_PMI to C2114 first, and then connect to 100kOhm and AUXAD18_AUXADC. Keep the crystal >4mm away from PMIC, and XTAL1 and XTAL2 length <10mm for signal quality consideration. Keep-out all crystal components >0.25mm away from surrounding metal.

Place the crystal inside the differential cable and keep >1mm away from the PCB edge and keep >1mm away from the PCB edge, and >20mm away from USB_headphones,vibrators

VCDT rating: 1.268V
R2209 close to battery connector. (<10mm)
Charging path should be 40mil or wider
Star connection from R2110 to BAT connector

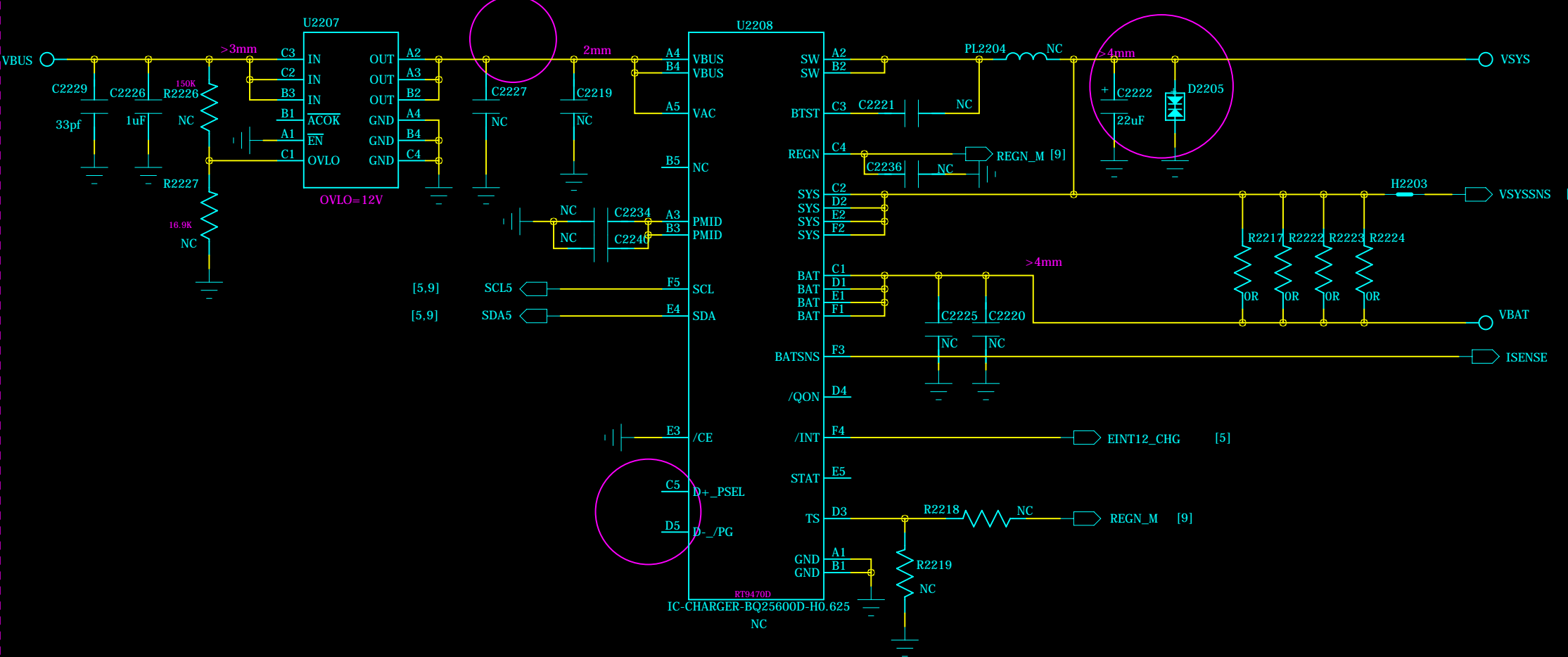
COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 21_POWER_MT6357_II		VERSION: V1.0	SHEET: 8 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

POWER_THIRD-PARTY_I
FRONT CAMERA FLASH LED



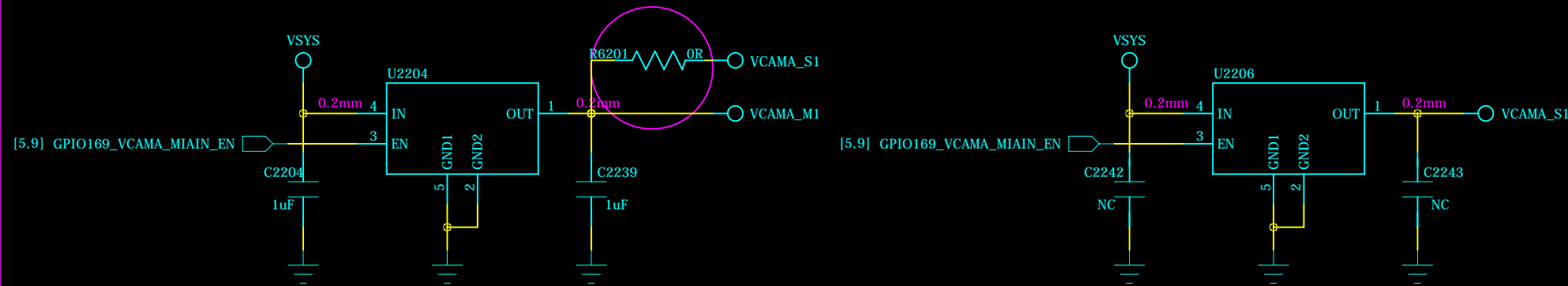
OVP

CHARGER

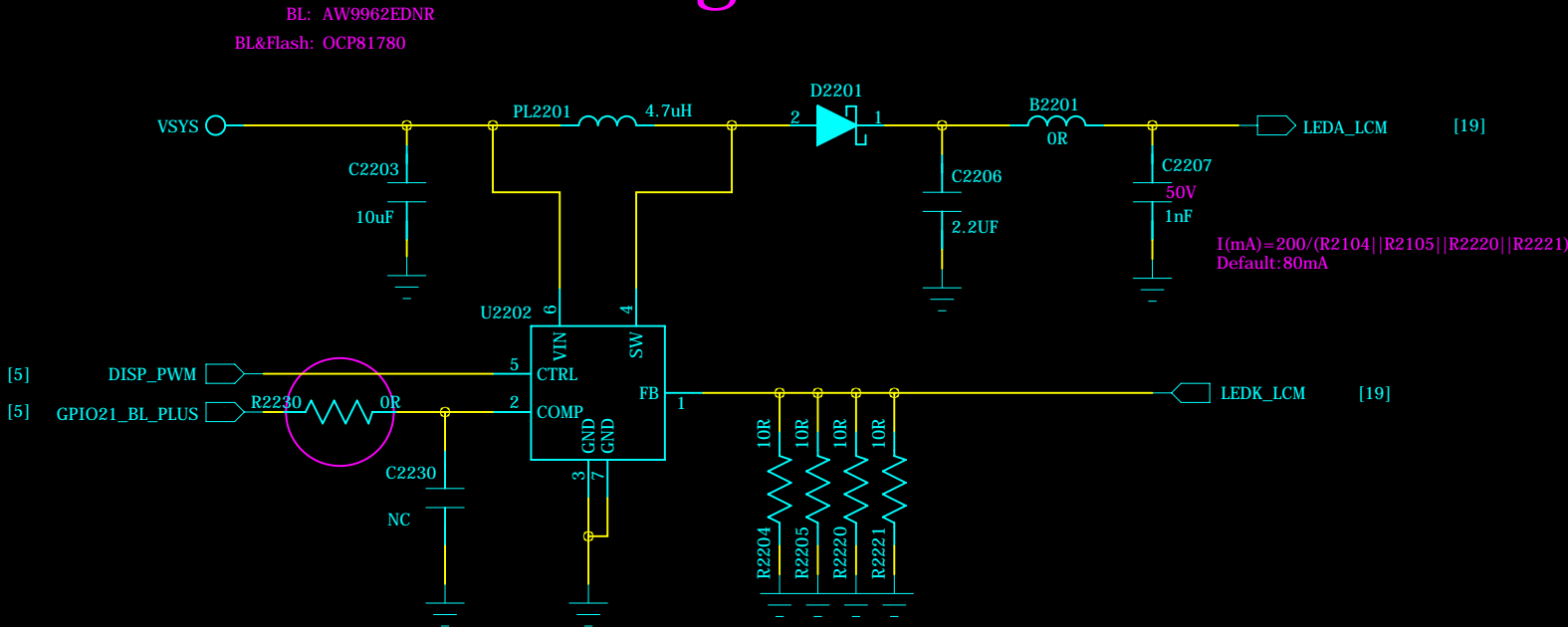


VCAMA_MAIN_1(2.8V)

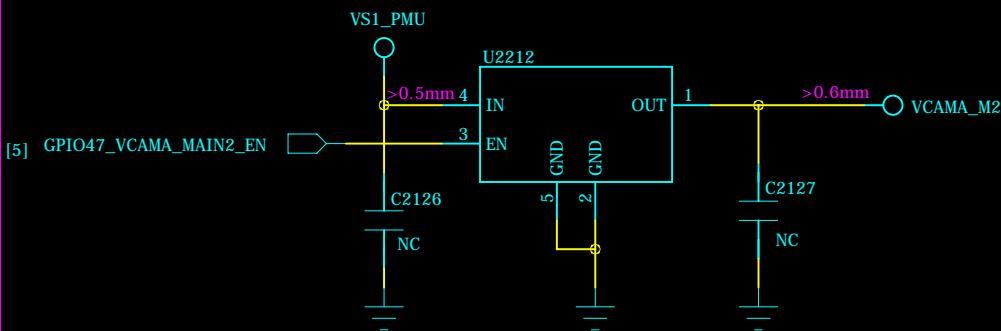
VCAMA_SUB_1(2.8V)



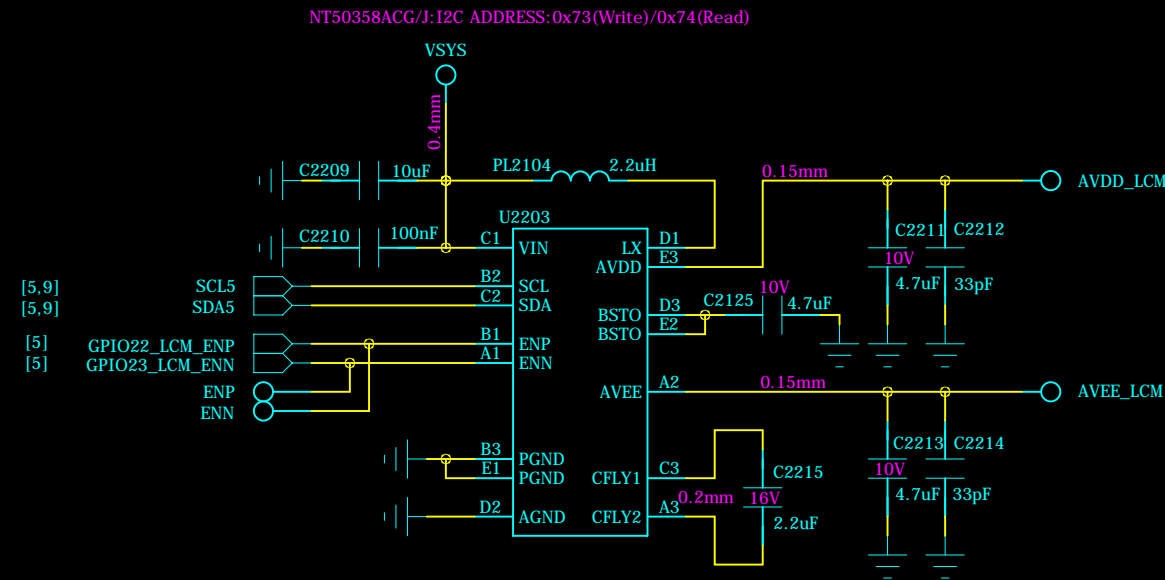
LCM Backlight LED Driver



VCAMA_MAIN_2 (1.8V)



LCM BIAS



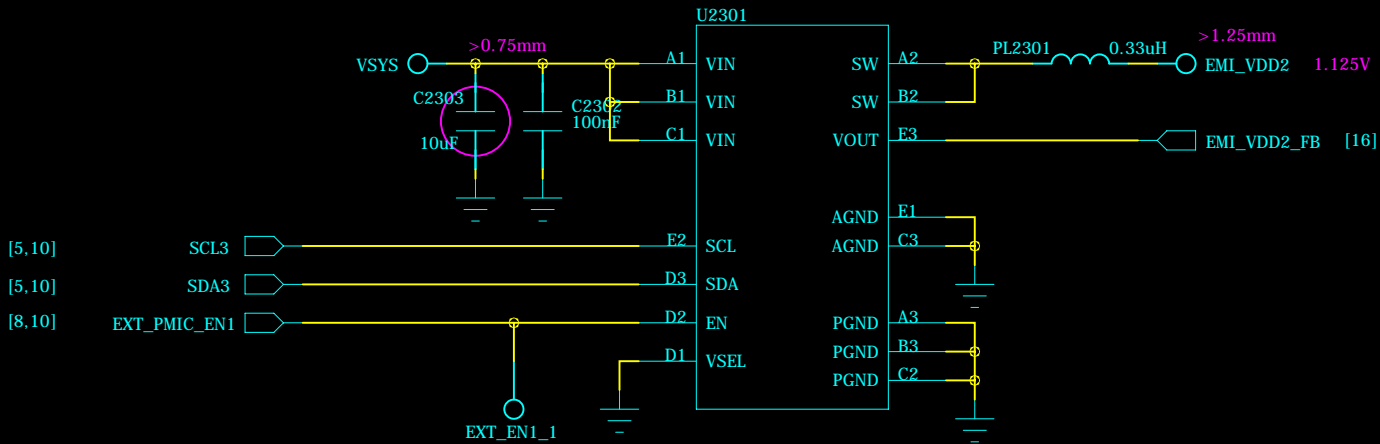
COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 22_POWER_THIRD-PARTY_I		VERSION: V1.0	SHEET: 9 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

POWER_THIRD-PARTY_II

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

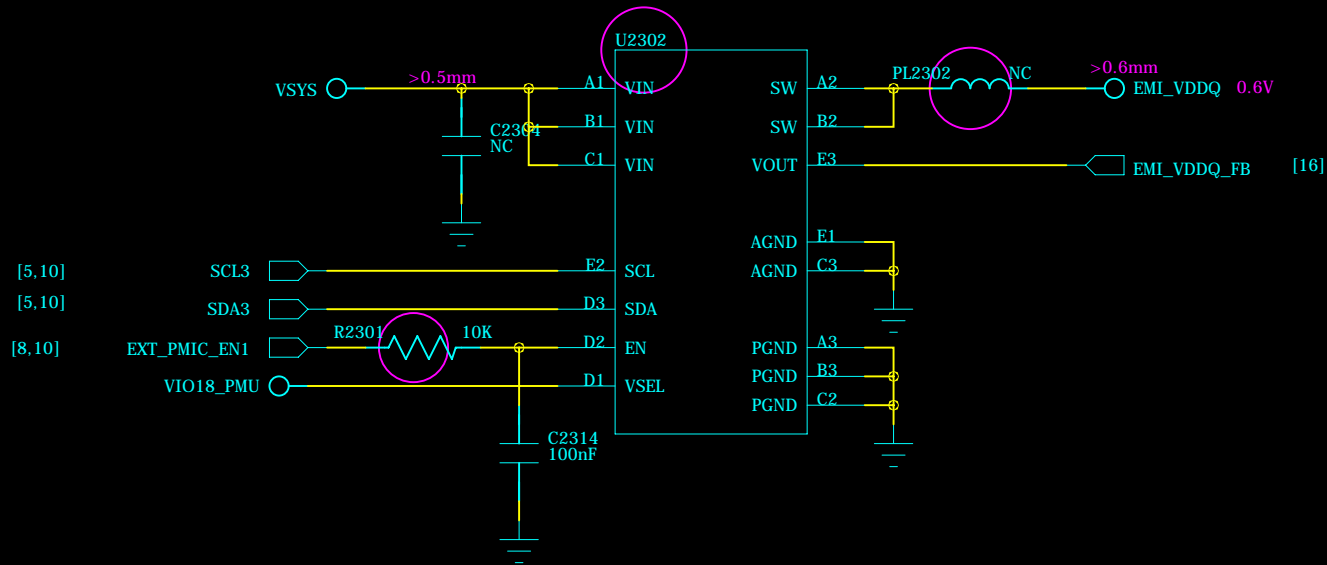
Ext. buck LP4X VDRAM(VDD2)

MT6691OOP/A:12C ADDRESS:0X57 (Write:0xAE, Read:0xAF)



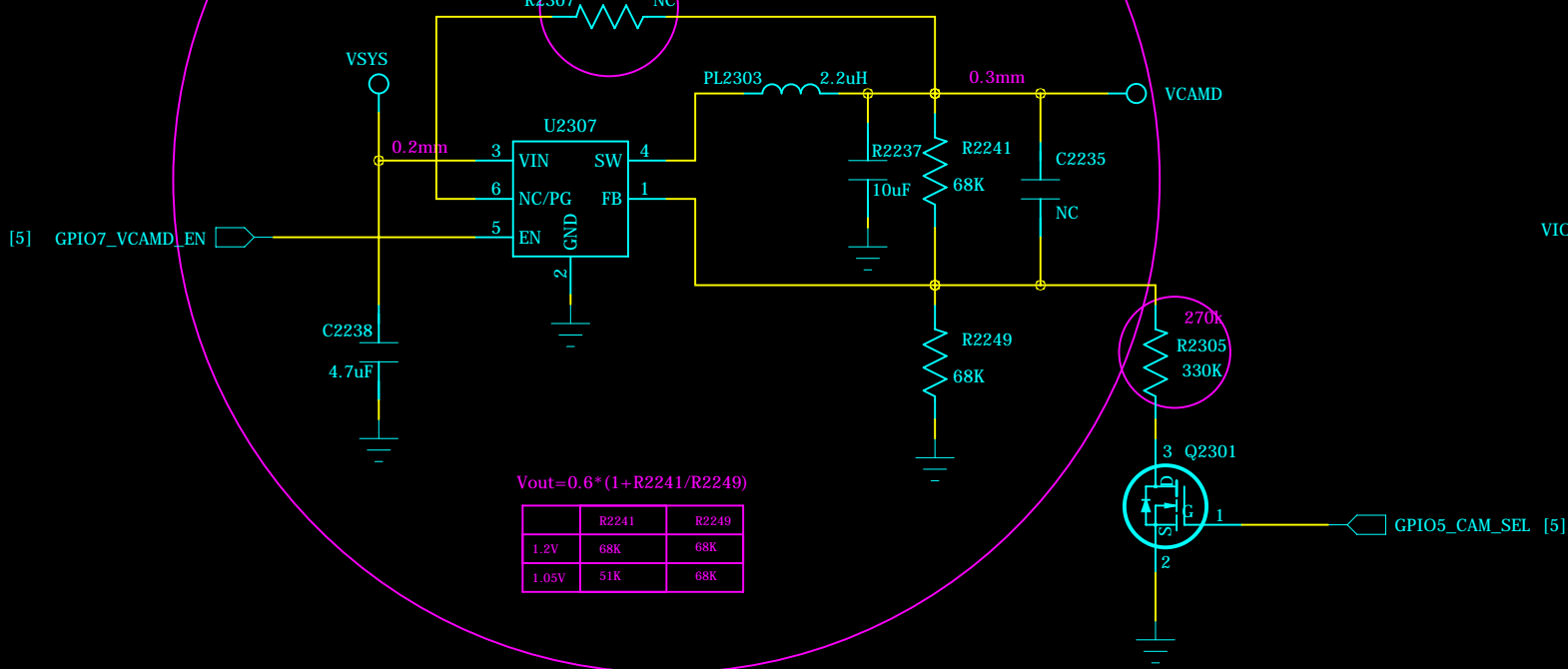
Ext. buck LP4X VDDQ

MT6691ZXP/A:12C ADDRESS:0X50 (Write:0xA0, Read:0xA1)

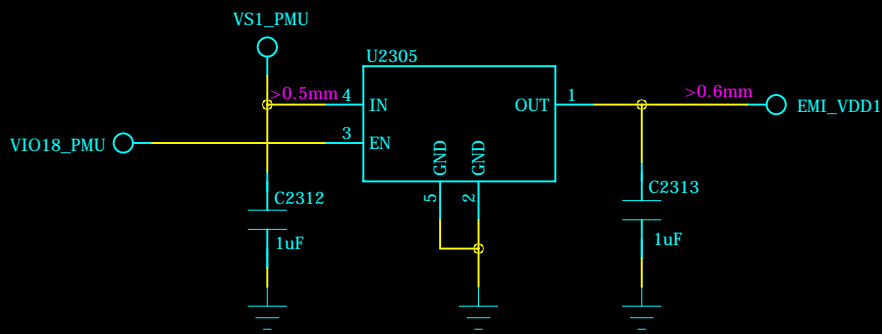


DVDD_Under_LCM_FP (3.3V)

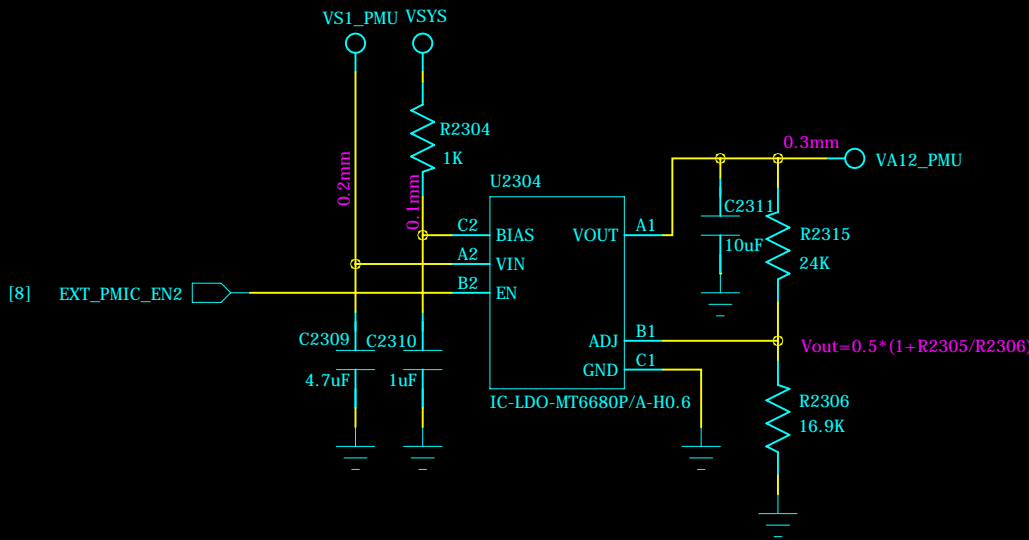
VCAMD (1.05V/1.2V)



LPDDR4X VDD1 1.8V LDO

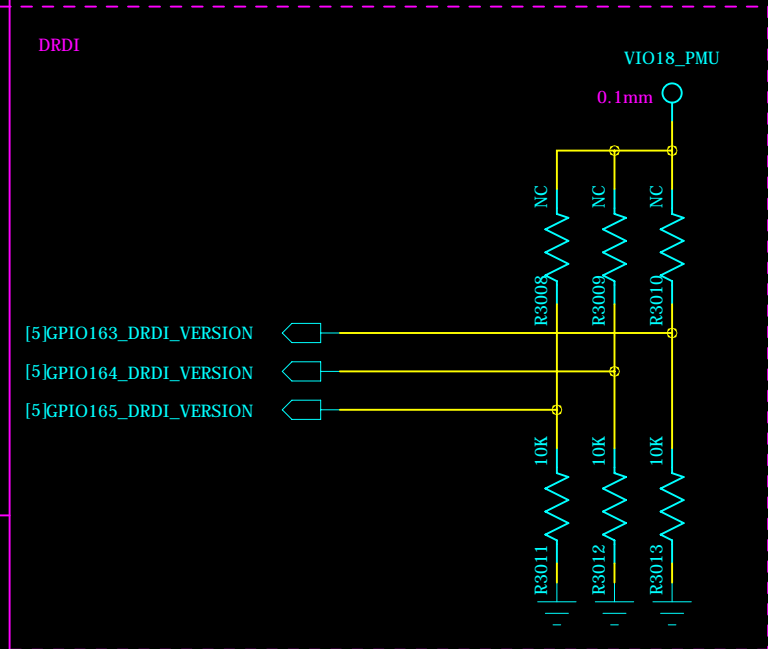


LDO 1.2V for VA12



LDO for VCN33

COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 23_POWER_THIRD-PARTY_II		VERSION: V1.0	SHEET: 10 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		



0.2mm

0.5mm

0.45mm

apply star connection to each Pin

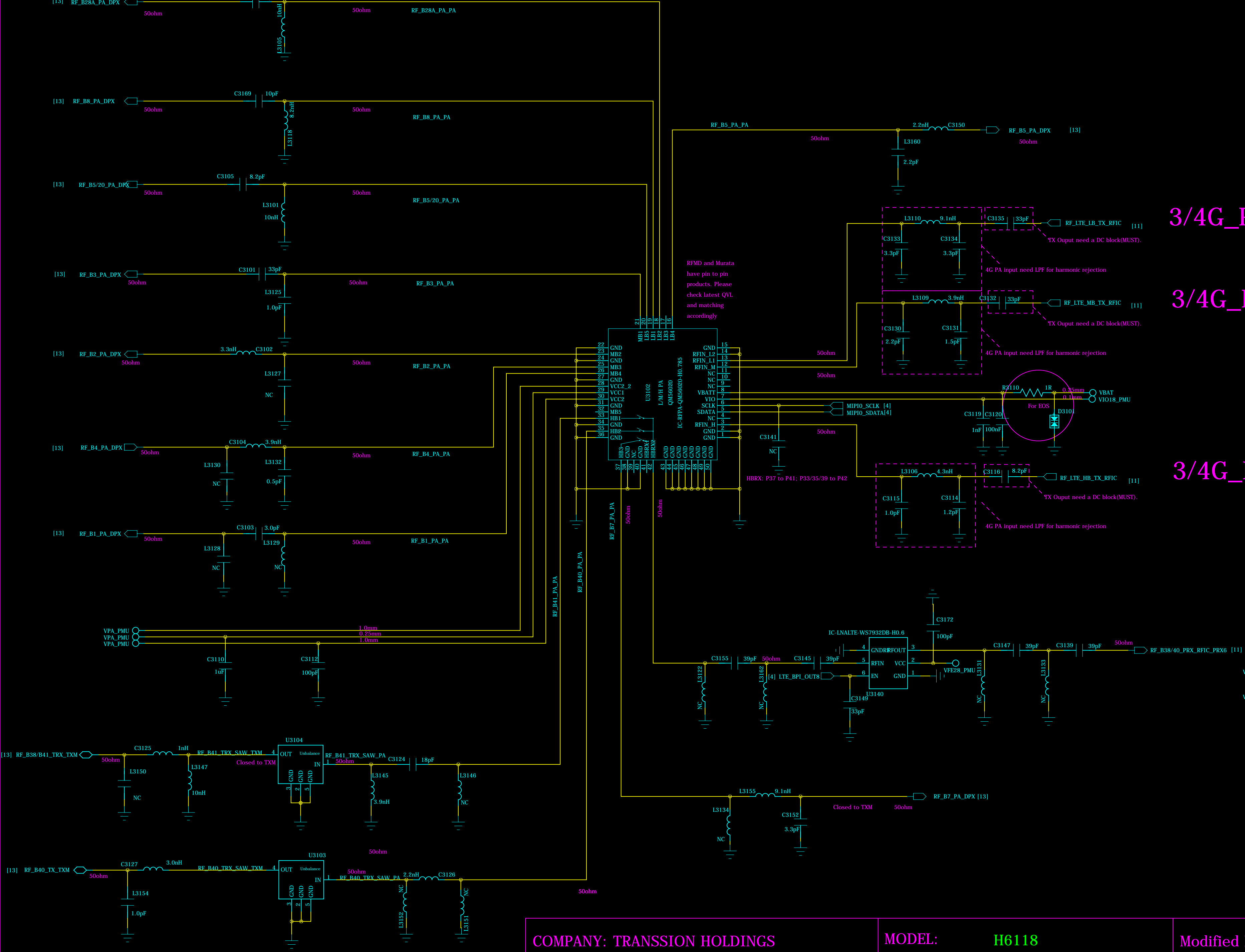
4.7uF

4.7uF

COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 30_RF_MT6177M_PIN_OUT		VERSION: V1.0	SHEET: 11 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

RF_MT6176_RF_TX

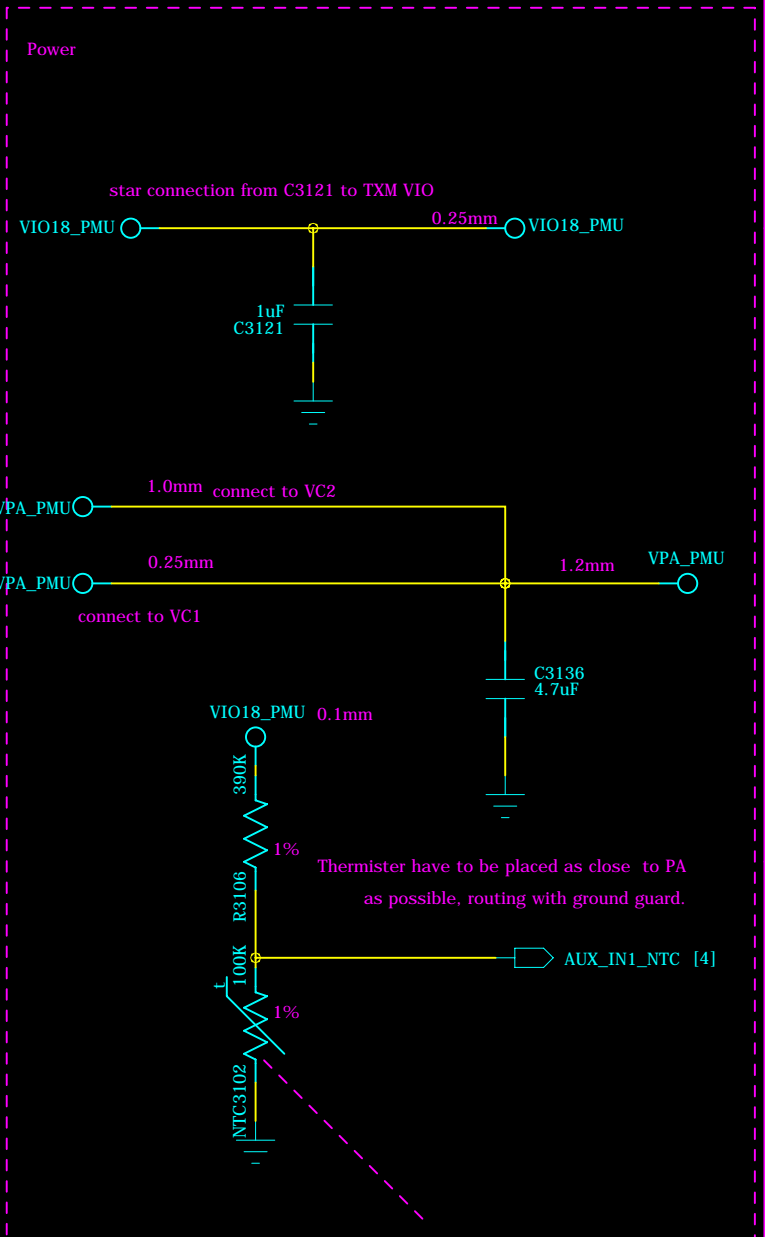
REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:



3/4G_PAIN_LB

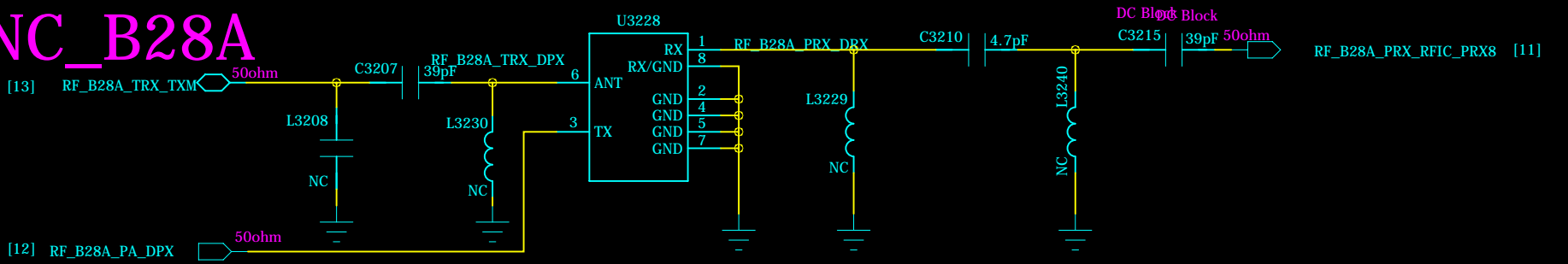
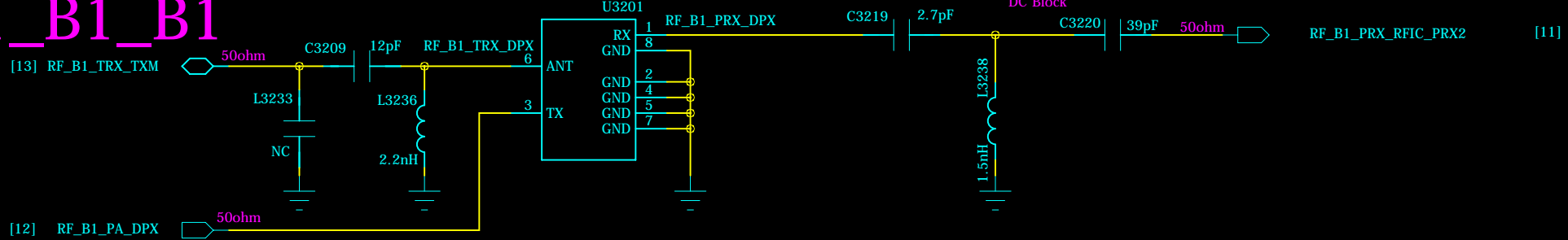
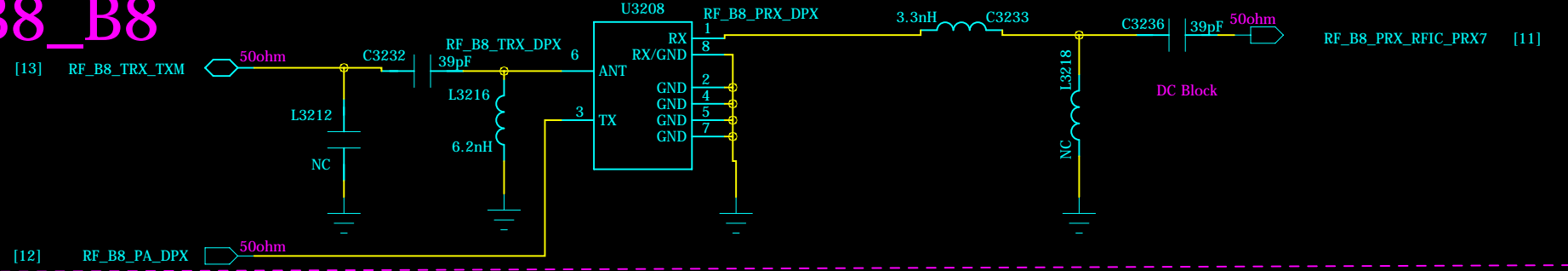
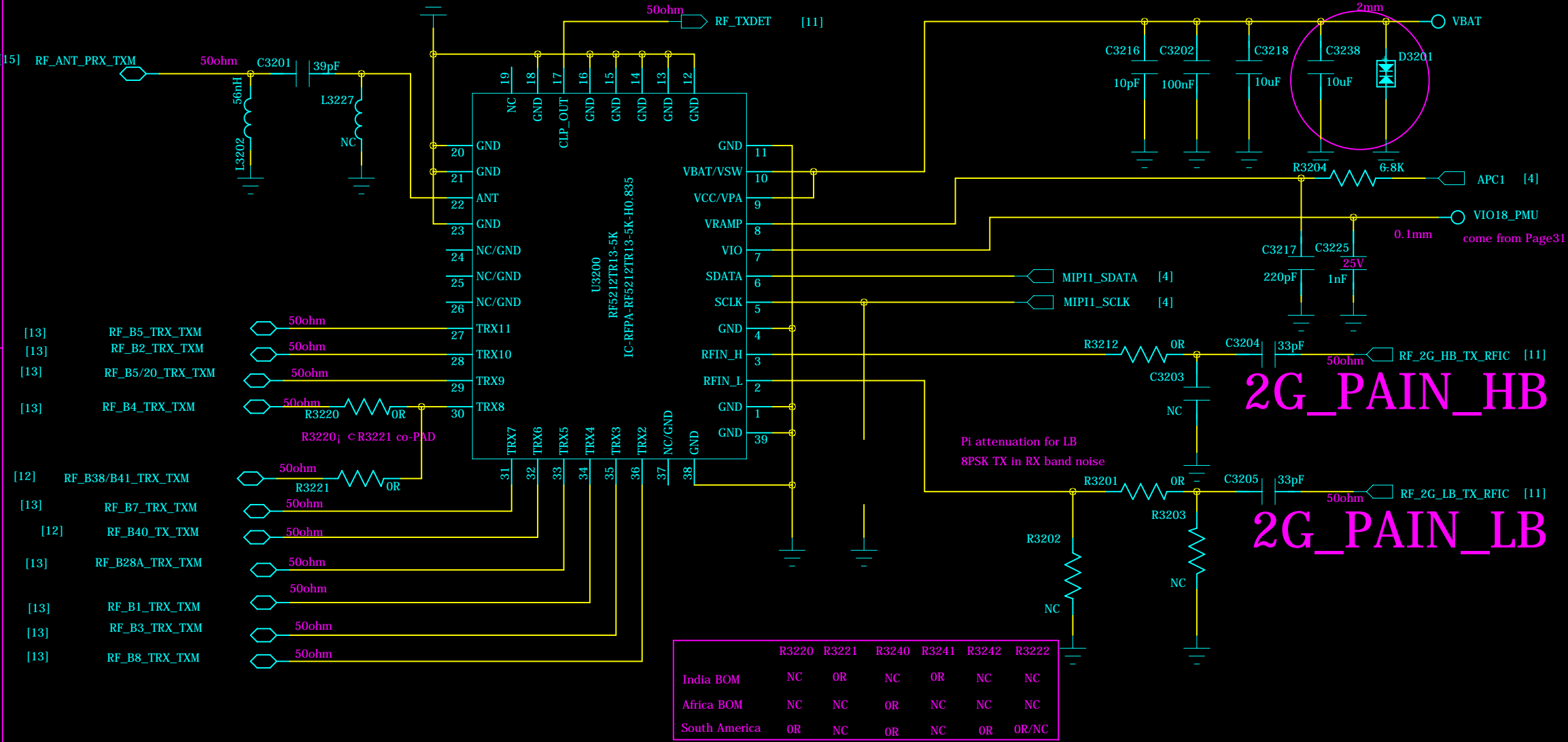
3/4G_PAIN_MB

3/4G_PAIN_HB



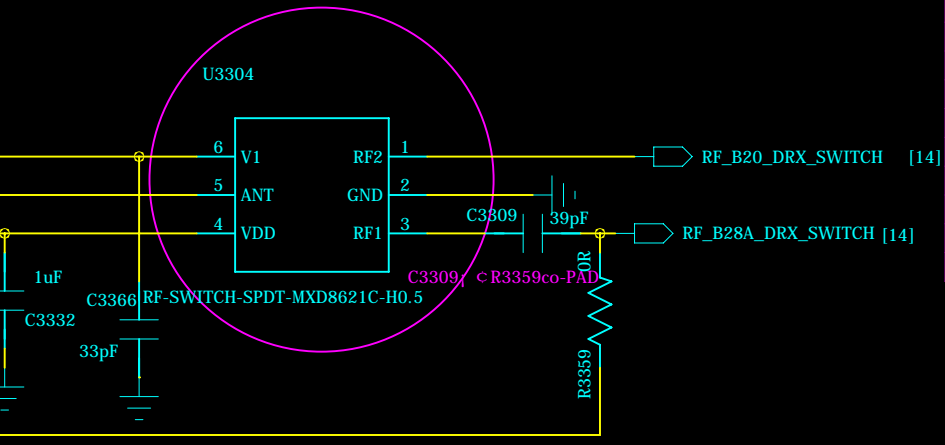
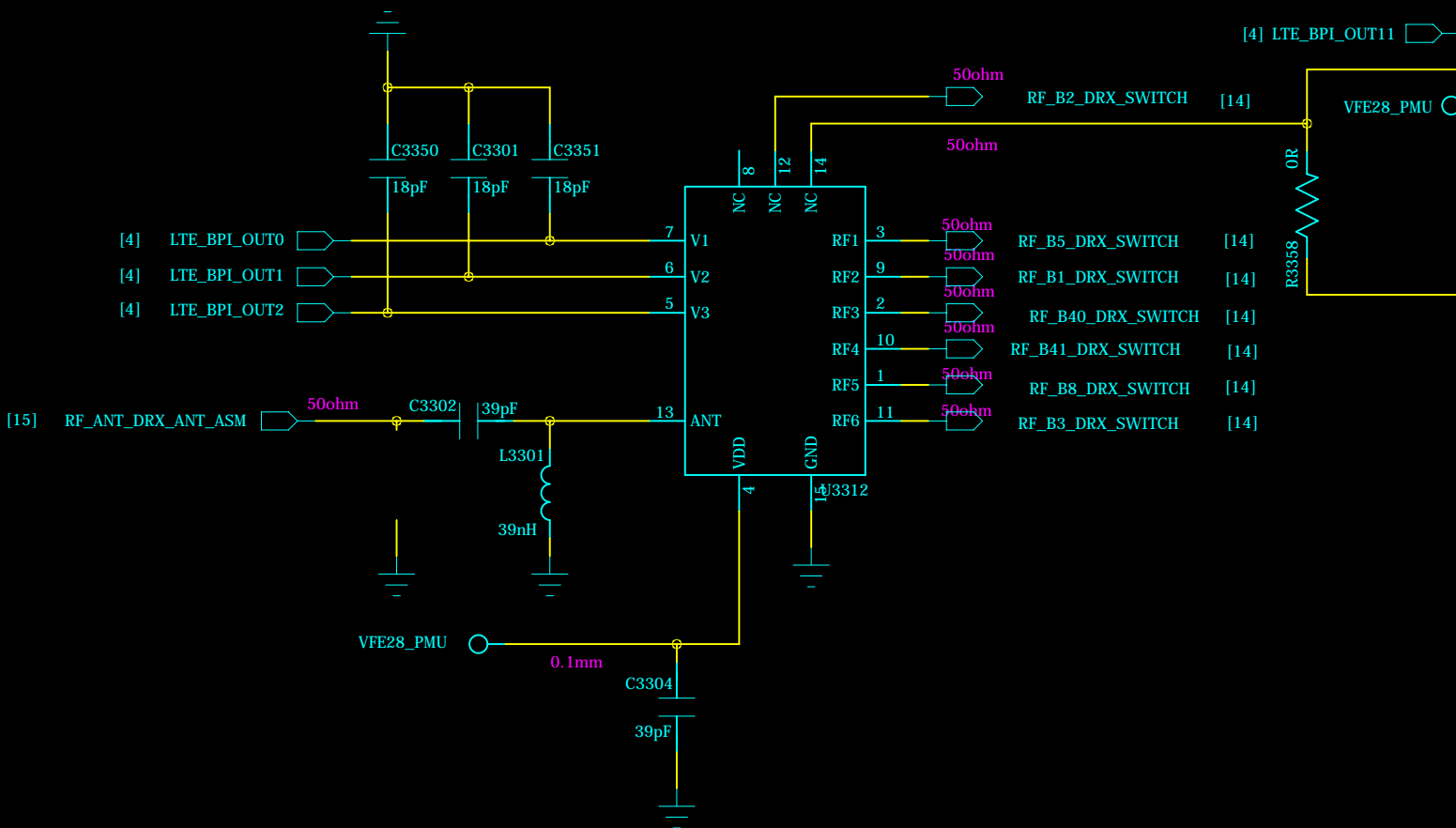
COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 31_RF_MT6177M_RF_TRX		VERSION: V1.0	SHEET: 12 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

RF_MT6177M_RF_PRX



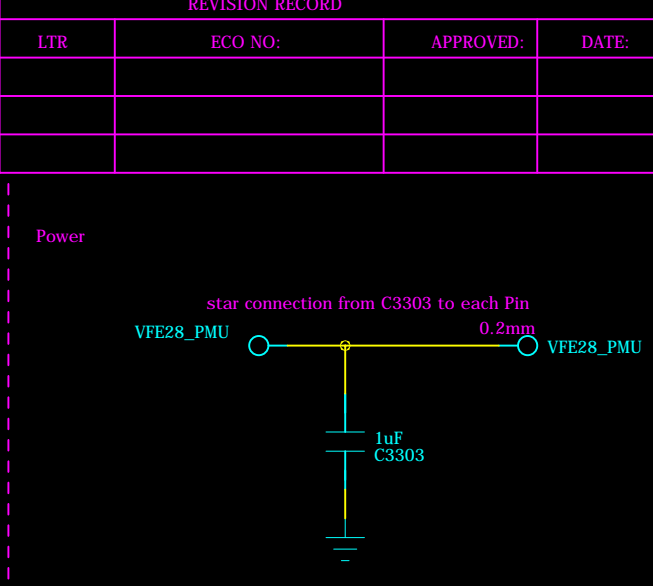
RF_MT6177M_RF_DRX

SP8T

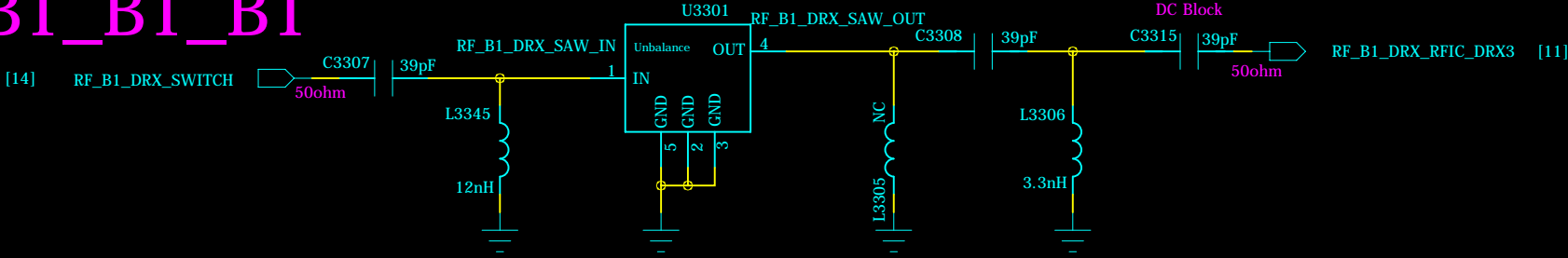


SP8T control logic							
V1	V2	V3	RF1	RF2	RF3	RF4	RF5
L	L	L	Y	N	N	N	N
L	L	H	N	Y	N	N	N
L	H	L	N	N	Y	N	N
L	H	H	N	N	N	Y	N
H	L	L	N	N	N	N	Y
H	L	H	N	N	N	N	Y
H	H	L	N	N	N	N	Y

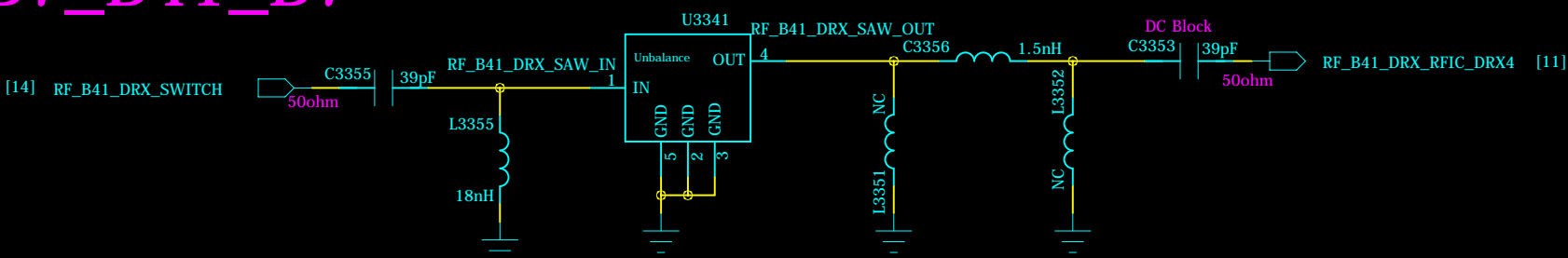
VC1613 control logic				
VC1	VC2	RF1	RF2	RF3
H	L	Y	N	N
H	H	N	Y	N
N	H	N	N	Y



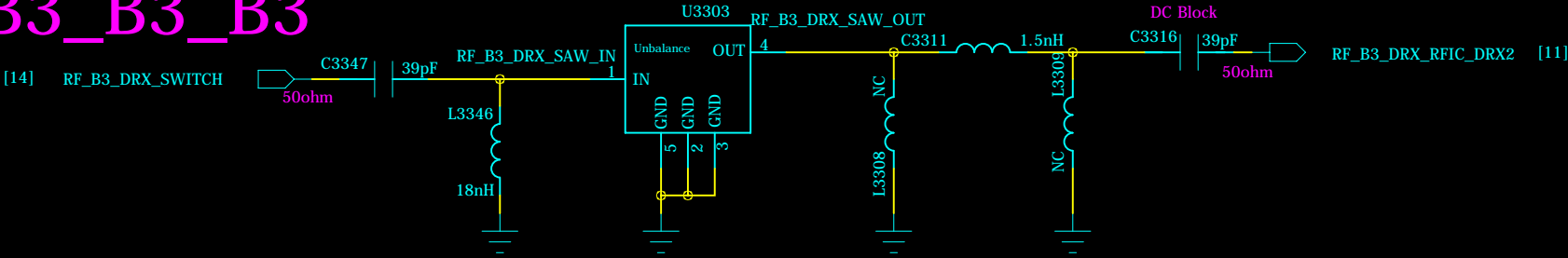
B1_B1_B1



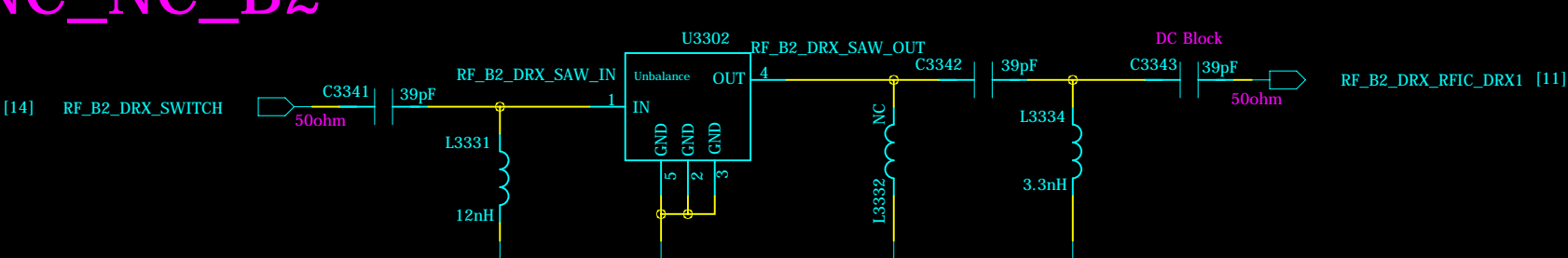
B7_B41_B7



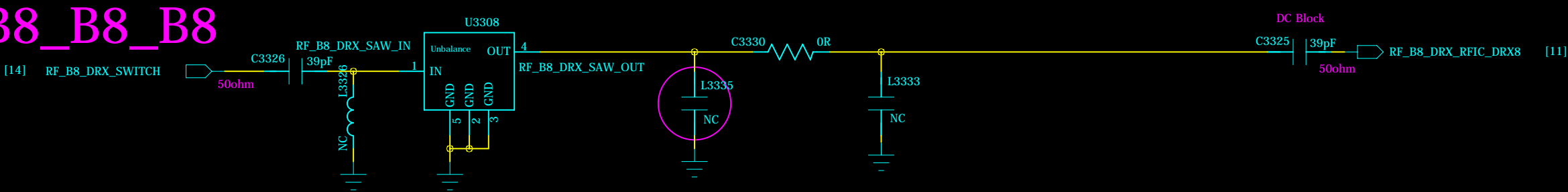
B3_B3_B3



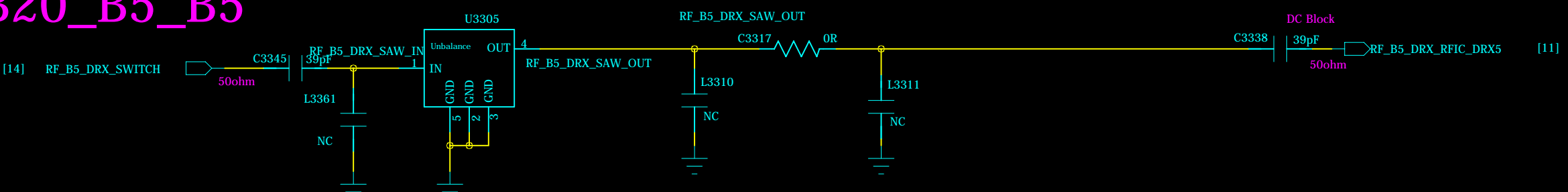
NC_NC_B2



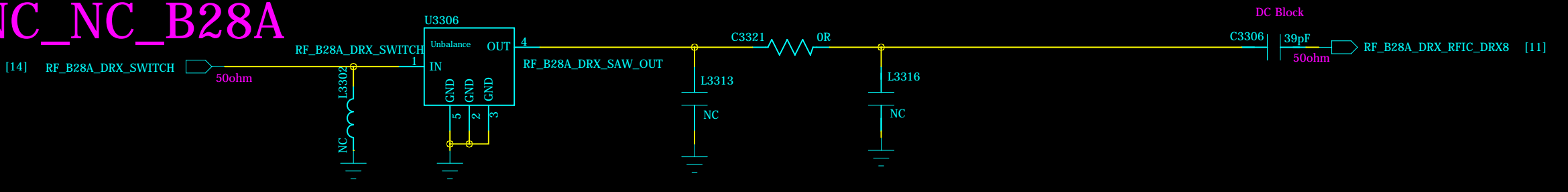
B8_B8_B8



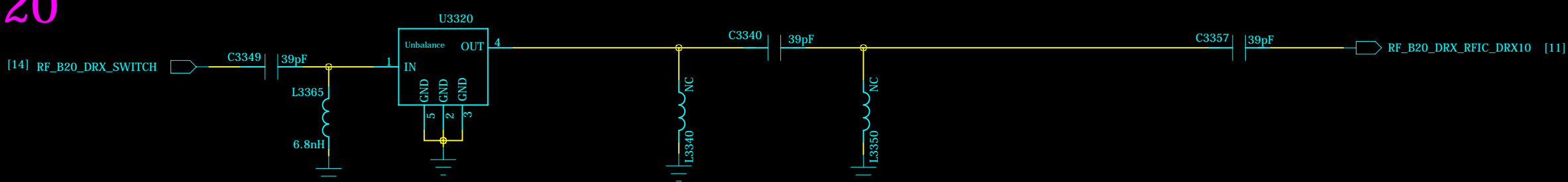
B20_B5_B5



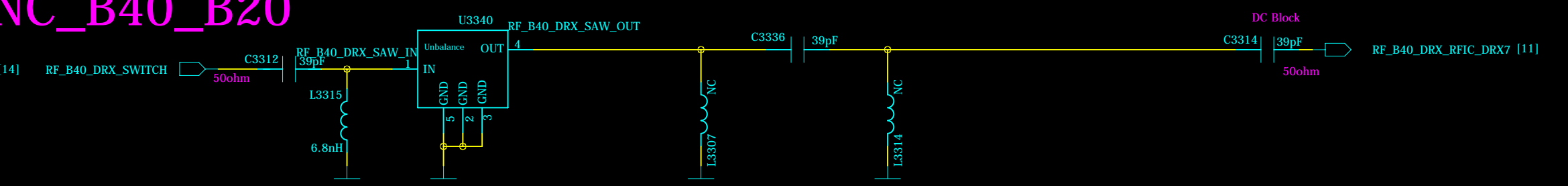
NC_NC_B28A



B20



NC_B40_B20

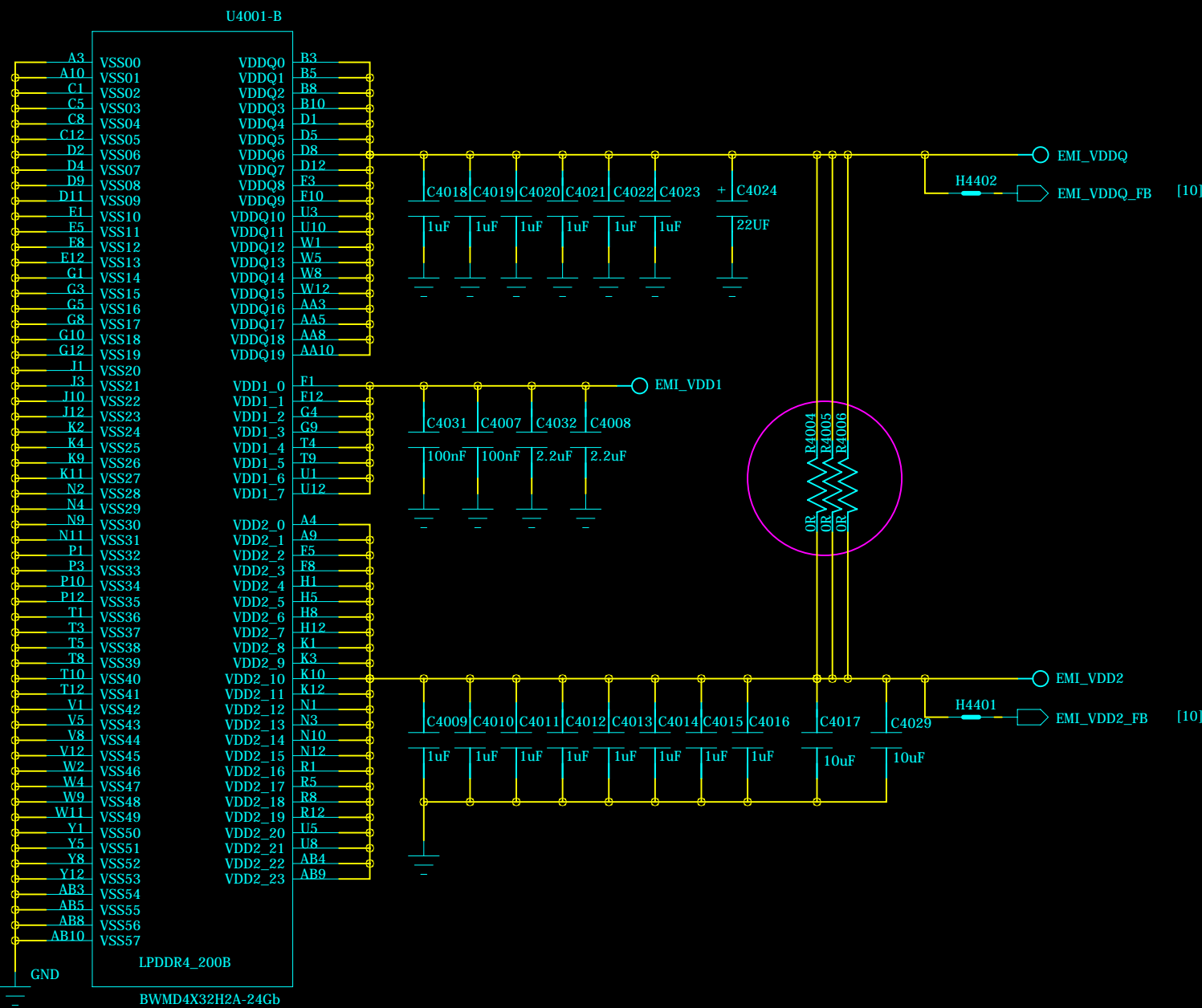
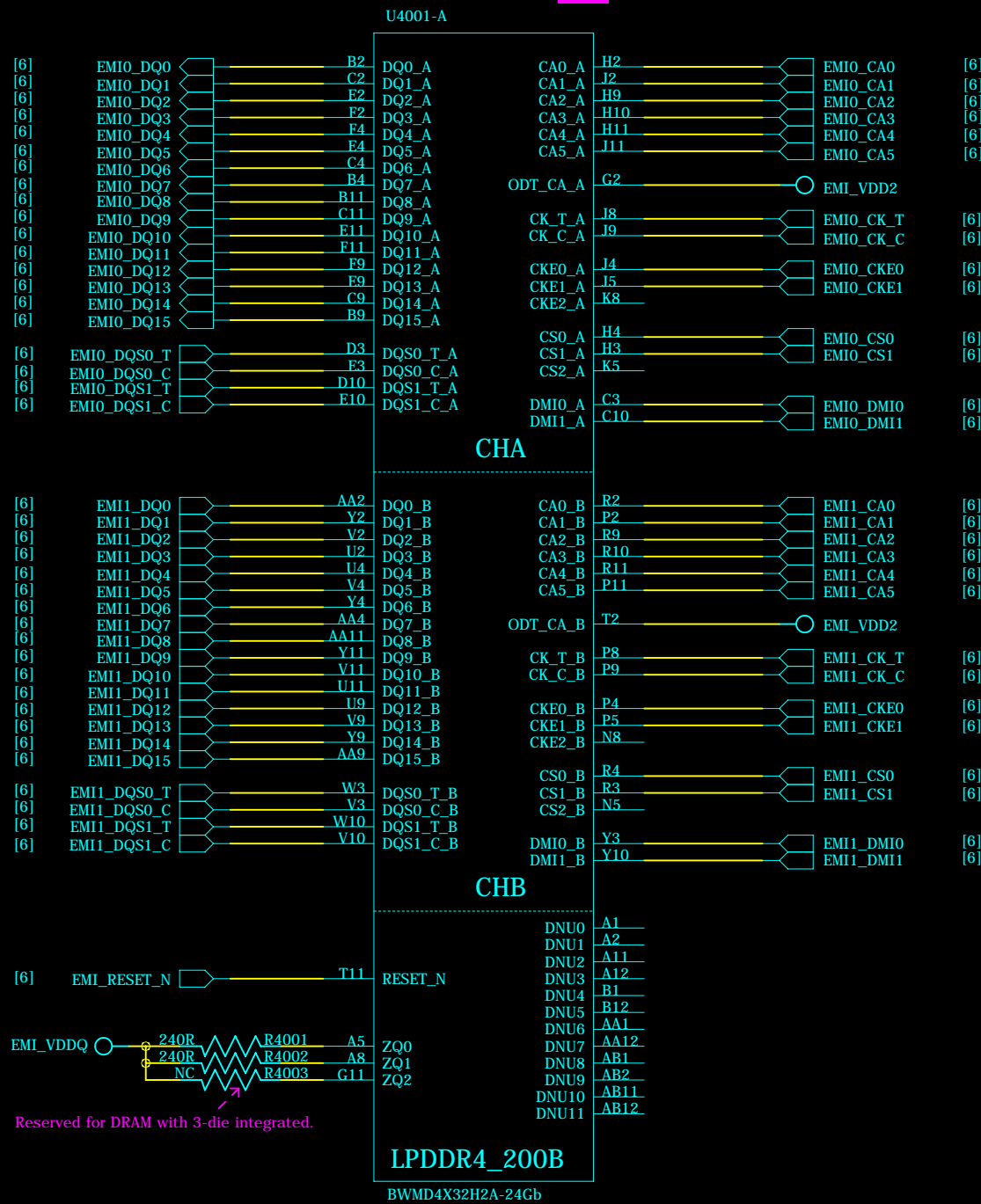
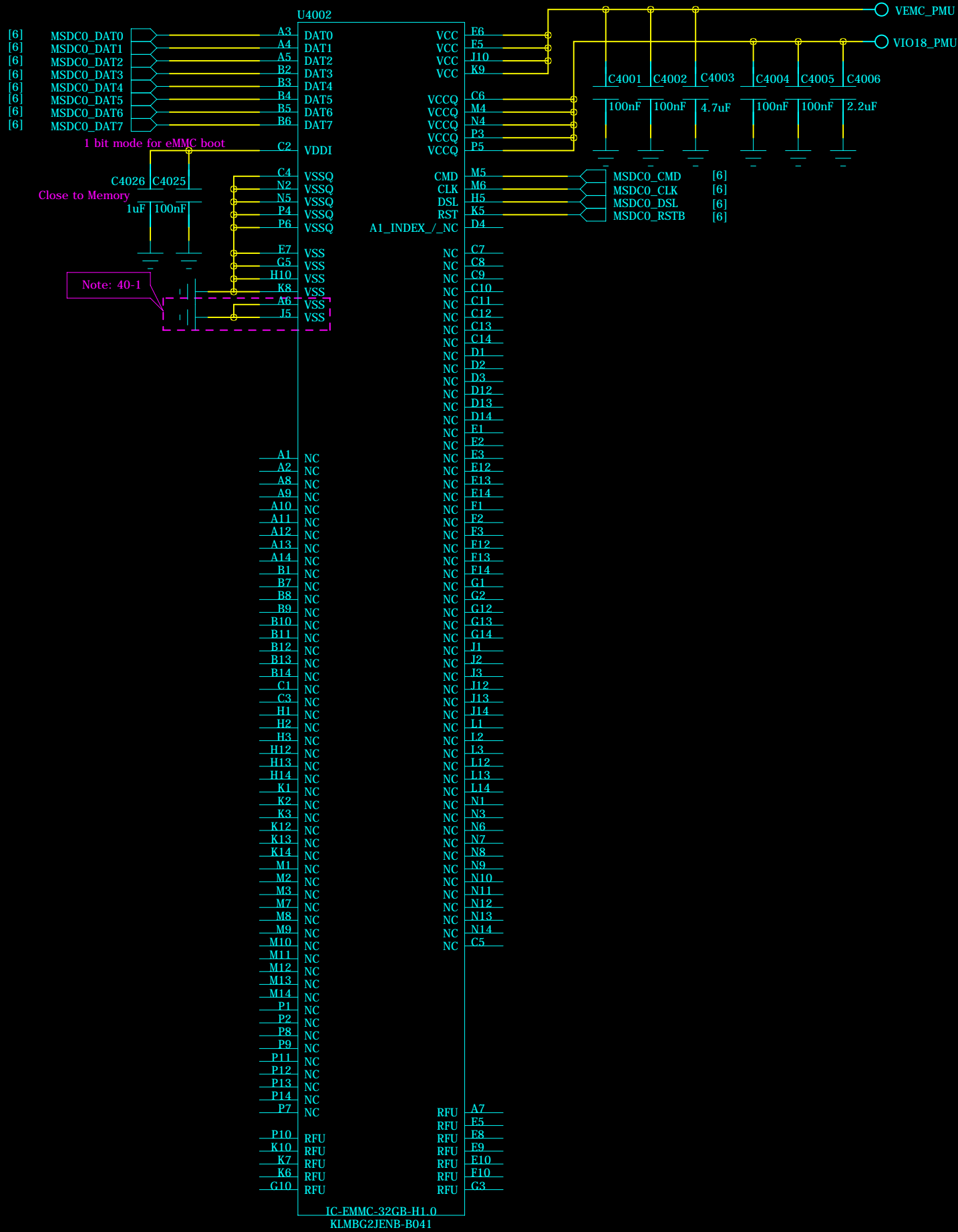


COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 33_RF_MT6177M_RF_DRX		VERSION: V1.0	SHEET: 14 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

MEMORY_LPDDR4

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

153 ball eMMC



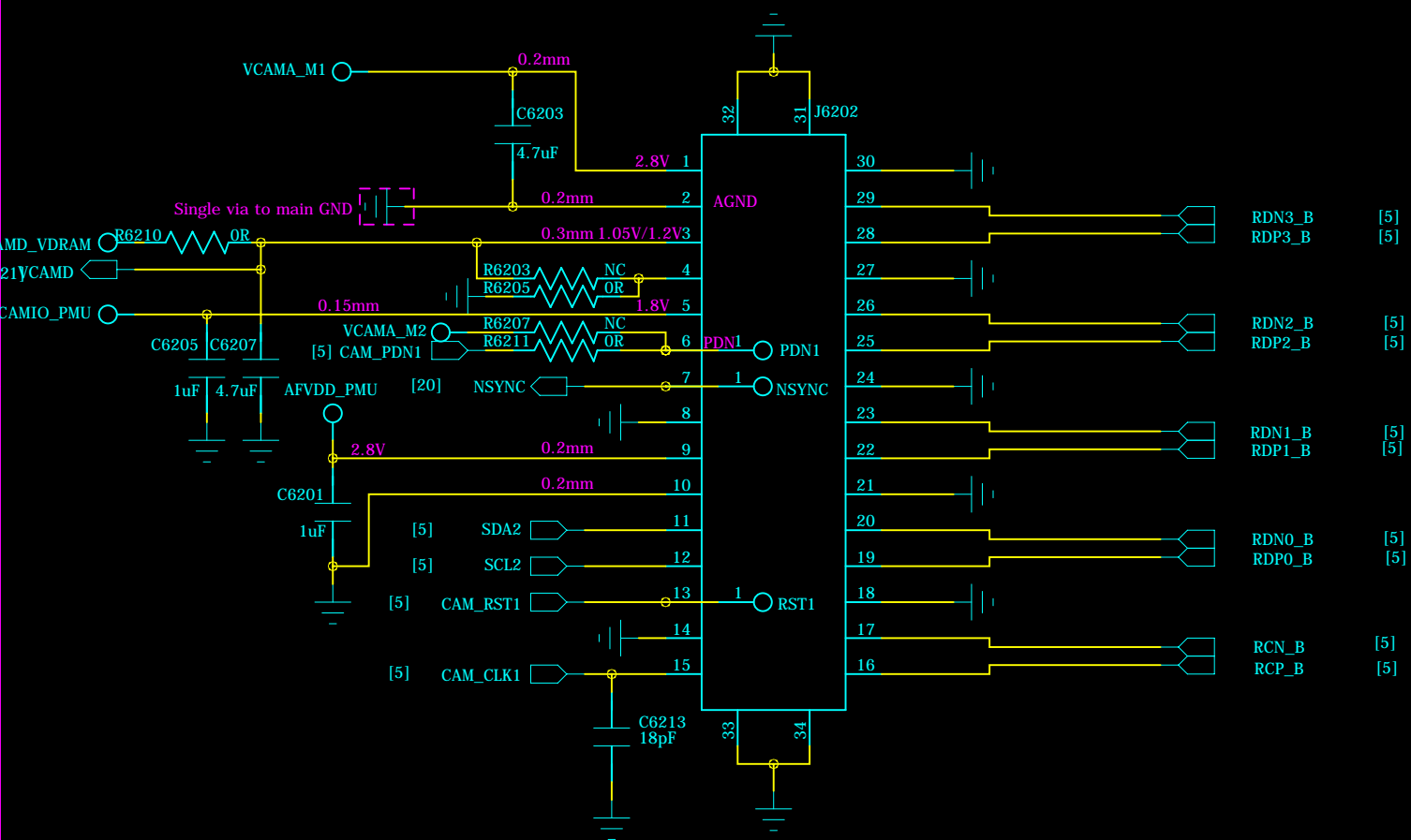
COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: <40_MEMORY_EMMC_LPDDR3>		VERSION: V1.0	SHEET: 16 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

The schematic diagram illustrates the PCB layout for the T6105 module, showing the connection of various components to the T6105 IC. The diagram includes a top layer layout with components like R6106, R6107, R6108, R6109, R6110, R6111, R6112, R6113, R6101, R6115, R6102, R6104, R6103, R6105, R6114, R6100, H6101, R6104, R6103, and R6105. It also shows the connection of the T6105 IC to the T6105 module. The diagram is labeled with component values, pin numbers, and connection points.

[illegible]

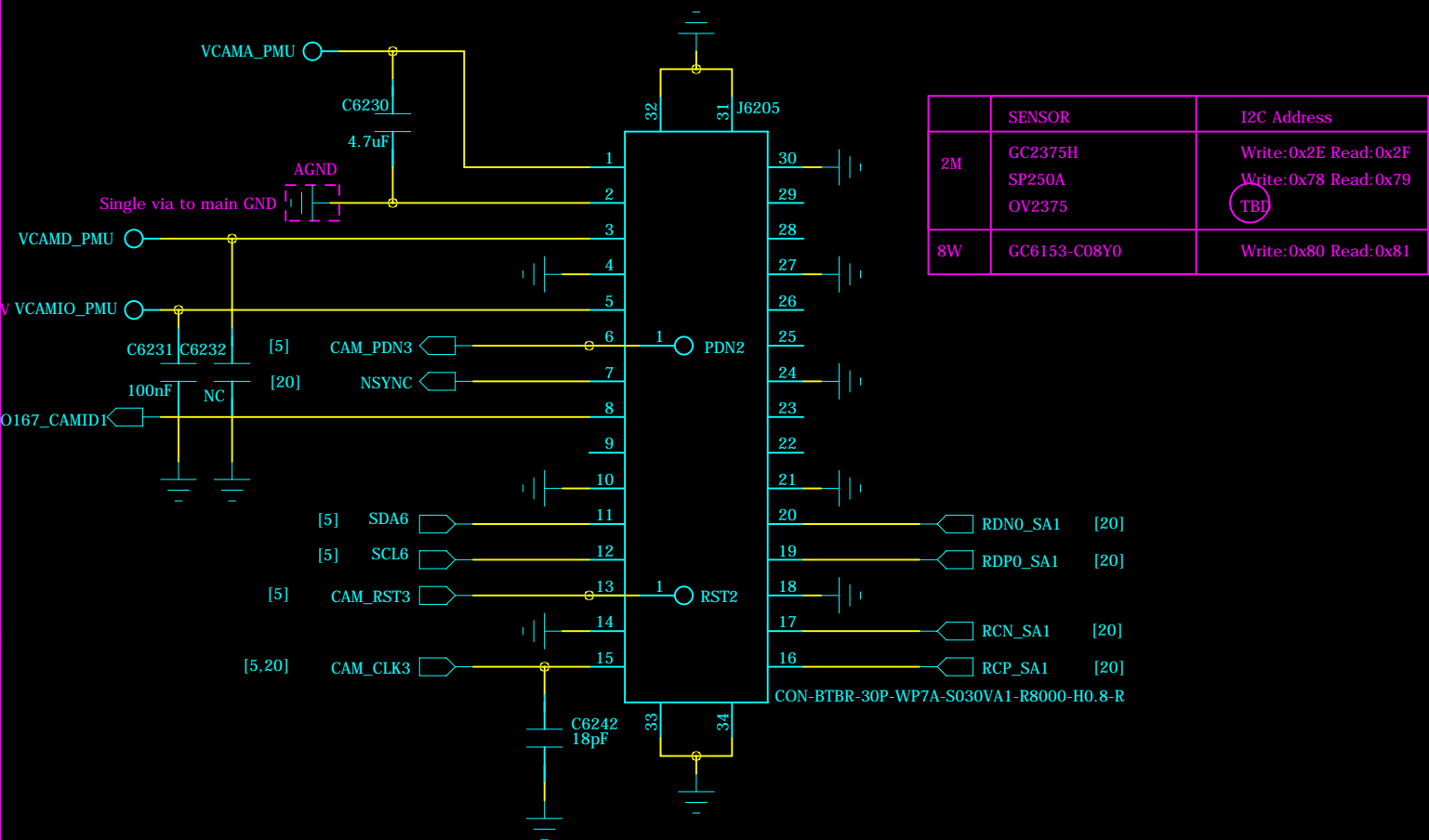
PERI_CAMERA

REAR CAMERA_I (48/16M+AF)



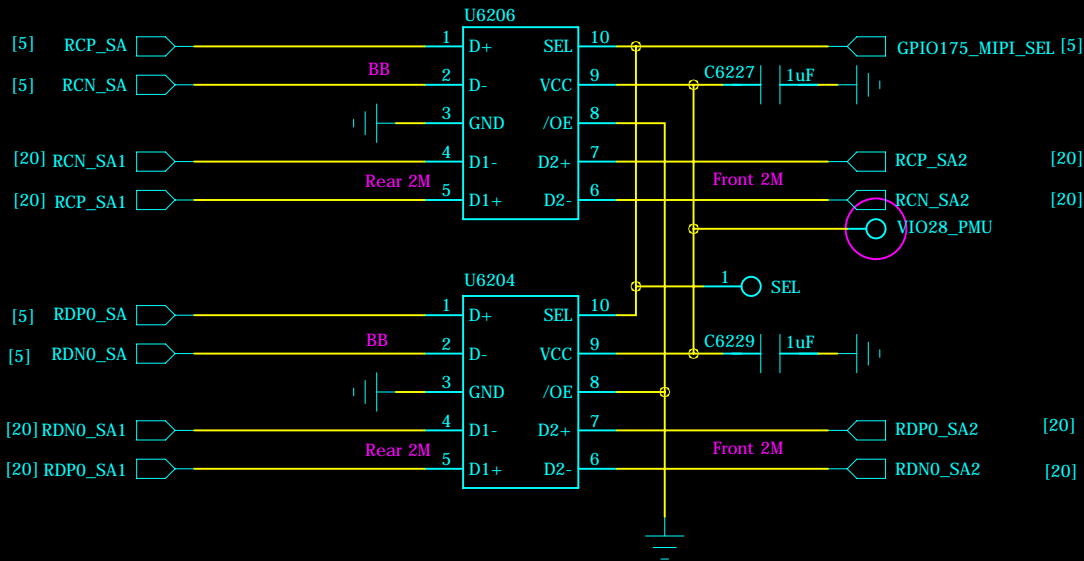
	SENSOR	I2C Address
8M	GC8034 S5K4H7YX03-FCX9	Write:0x6E, Read:0x6F Write:0x20, Read:0x21
13M	S5K3L6XX03-FCX9	Write:0x20 Read:0x21
16M	S5K3P9SX04-FCX09	Write:0x20 Read:0x21
48M	OV48B2Q-GA5A-Z	Write:0x20 Read:0x21
64M	IMX686-AAJH5-C	Write:0x20 Read:0x21

REAR CAMERA_II (2M)

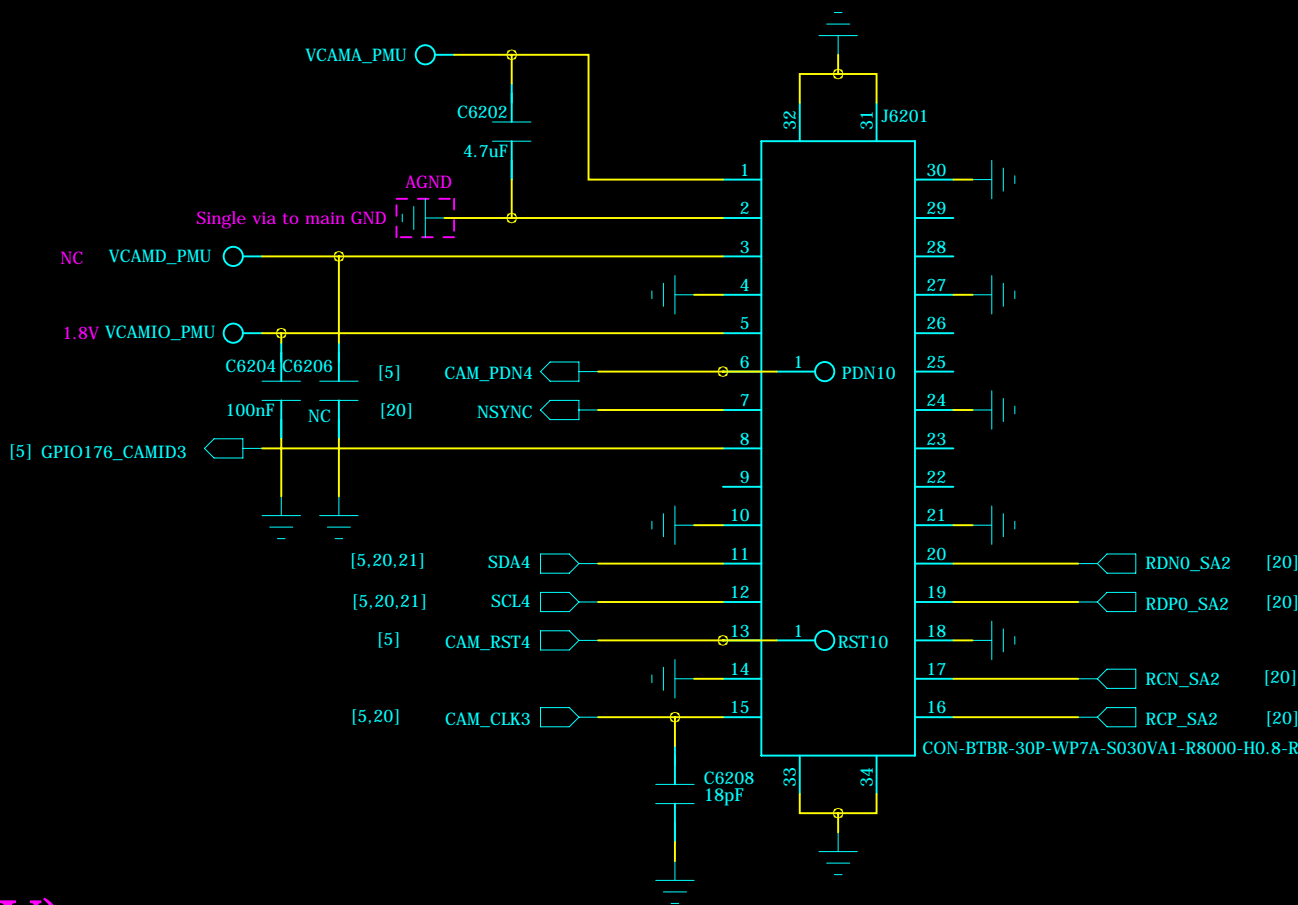


	SENSOR	I2C Address
2M	GC2375H SP250A OV2375	Write:0x2E Read:0x2F Write:0x78 Read:0x79 (TBI)
8W	GC6153-C08Y0	Write:0x80 Read:0x81

MIPI SWITCH

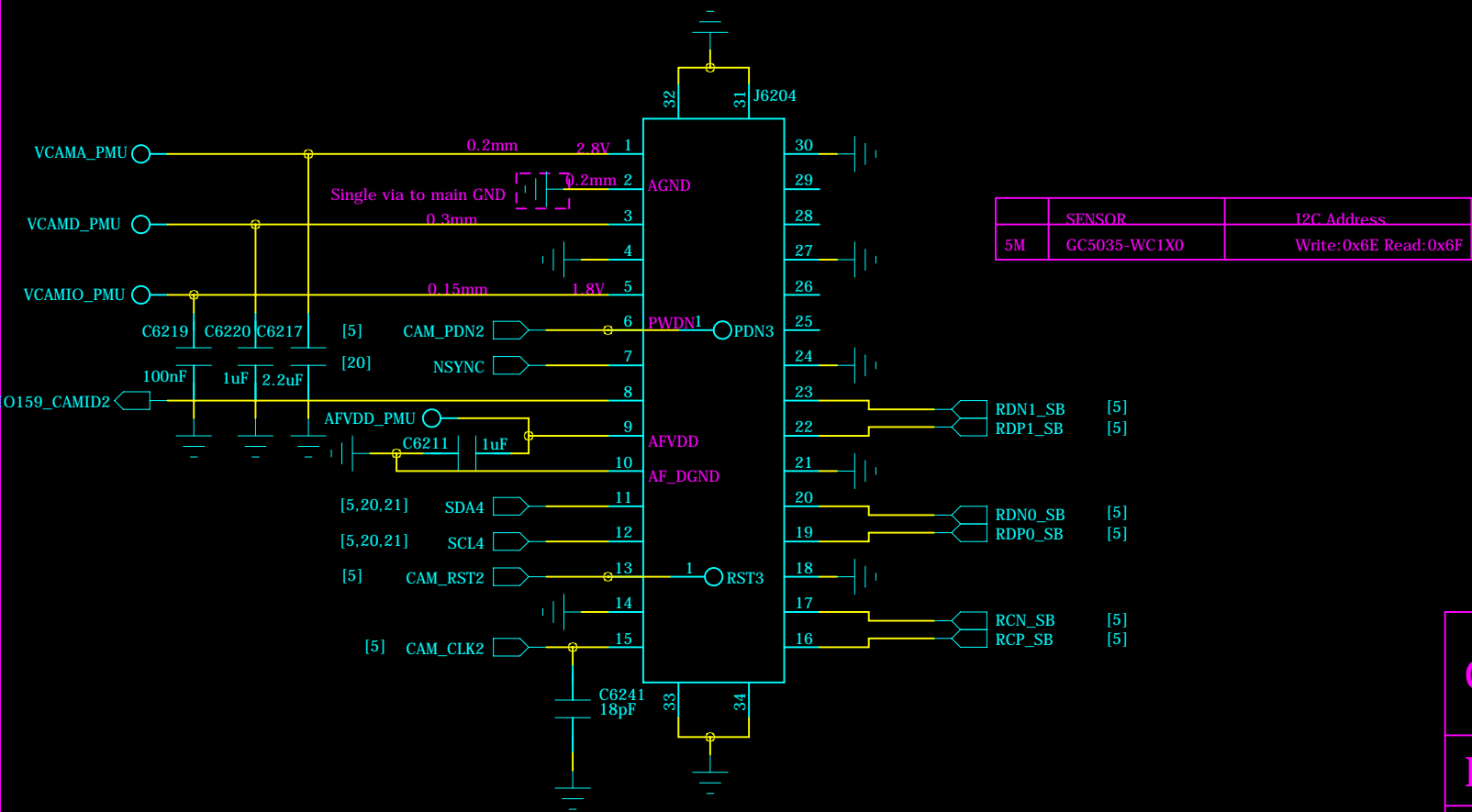


REAR CAMERA_III (2M/8W)



	SENSOR	I2C Address
2M	GC2375H SP250A OV2375	Write:0x2E Read:0x2F Write:0x78 Read:0x79 (TBI)
8W	GC6153-C08Y0	Write:0x80 Read:0x81

REAR CAMERA_III (5M)

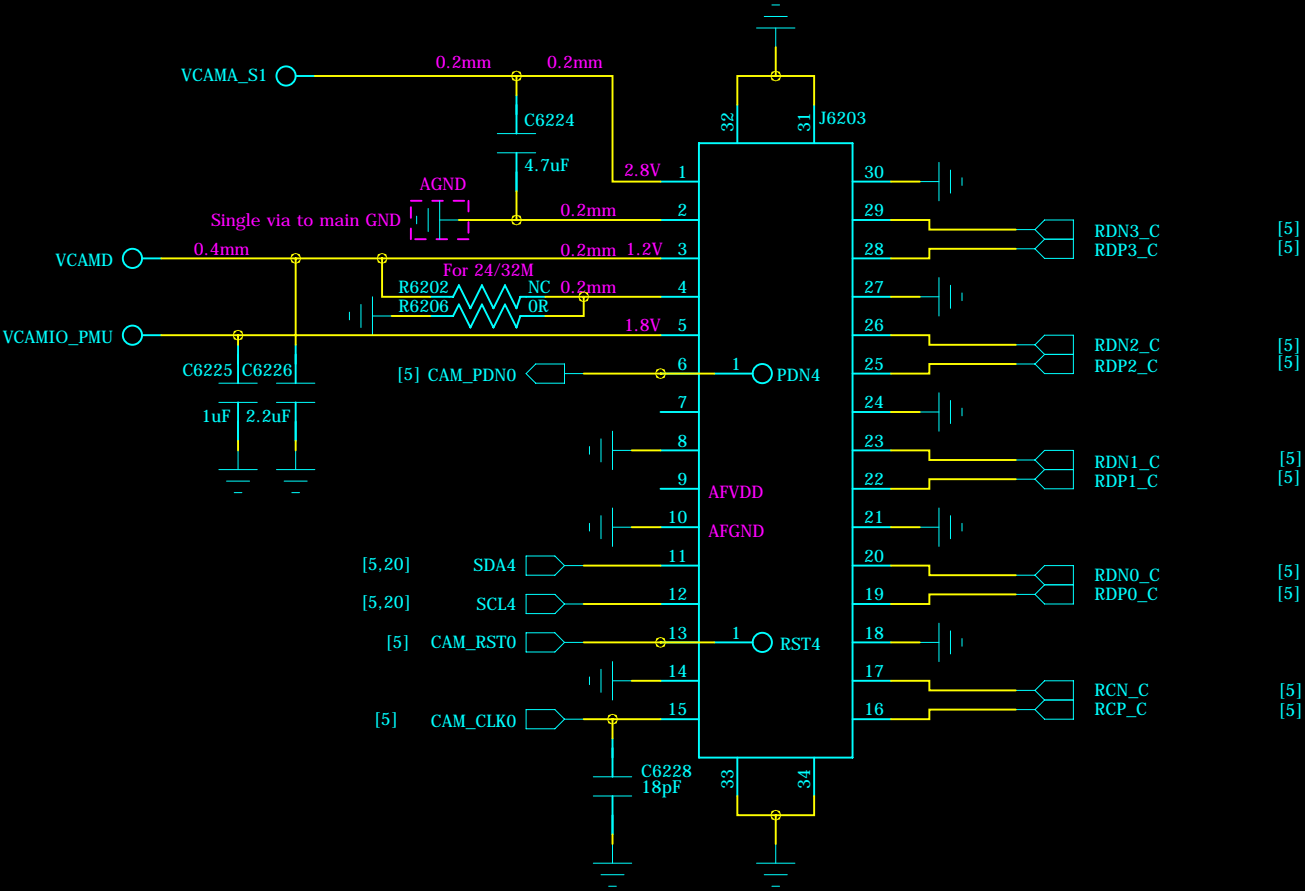


	SENSOR	I2C Address
5M	GC5035-WC1X0	Write:0x6E Read:0x6F

COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 62_PERI_CAMERA-1		VERSION: V1.0	SHEET: 20 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

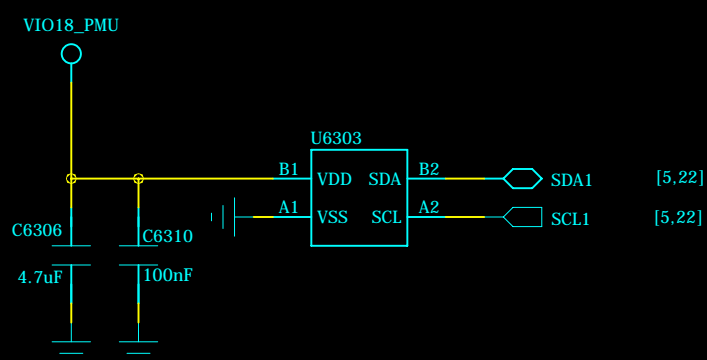
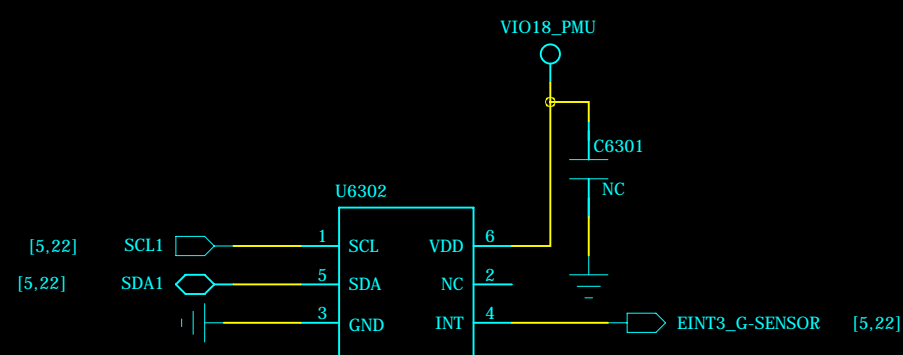
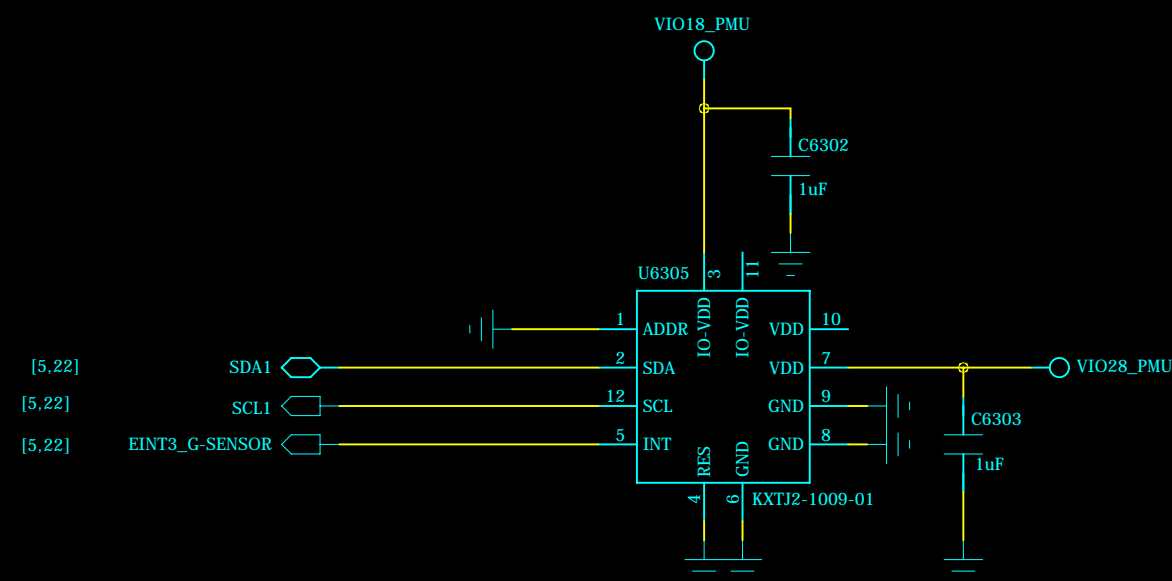
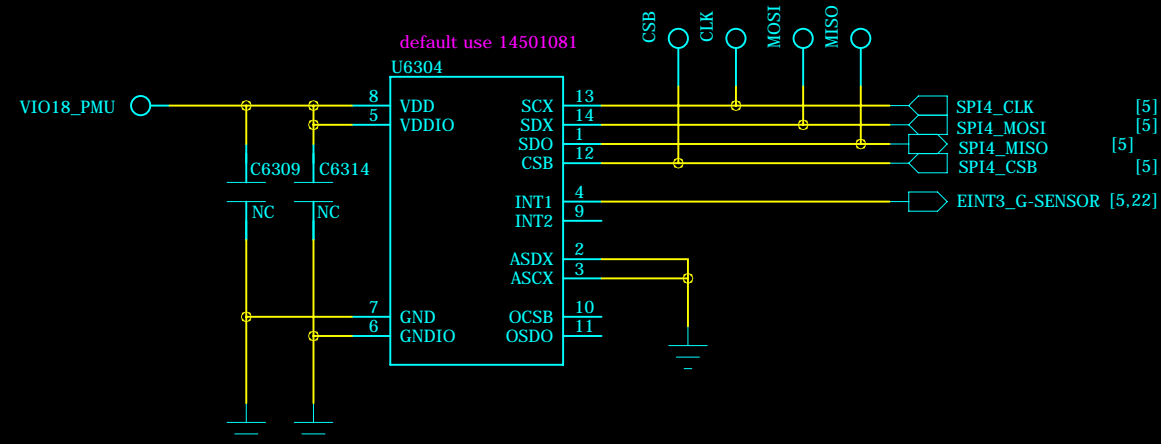
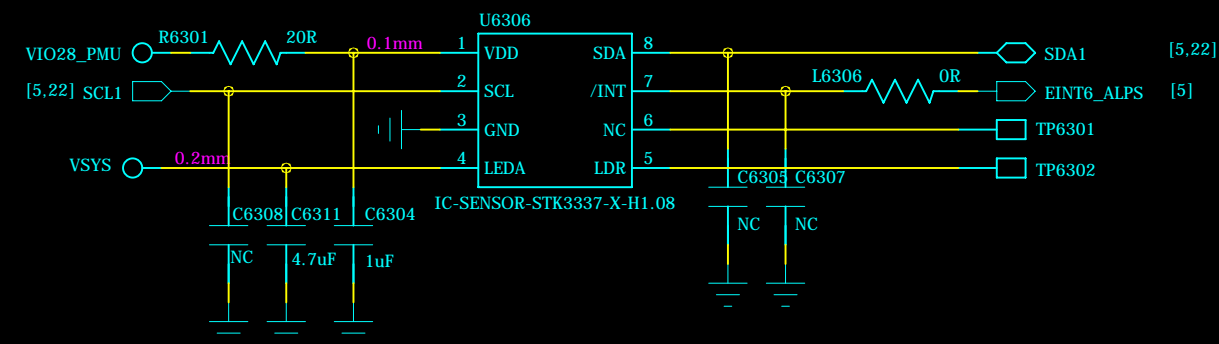
FRONT CAMERA_I (40/32/16/8M)



	SENSOR	I2C Address
8M	GC8034	Write:0x6E, Read:0x6F
16M	S5K3P9SP04-FGX9	Write:0x5A Read:0x5B
32M	S5KGD1SP03-F1X9	Write:0x5A Read:0x5B
40M	S5KGH1SP03-FGX9	Write:0x5A Read:0x5B
13M	S5K3L6XX03-FGX9	Write:0x5A Read:0x5B

COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 62_PERI_CAMERA-2		VERSION: V1.0	SHEET: 21 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

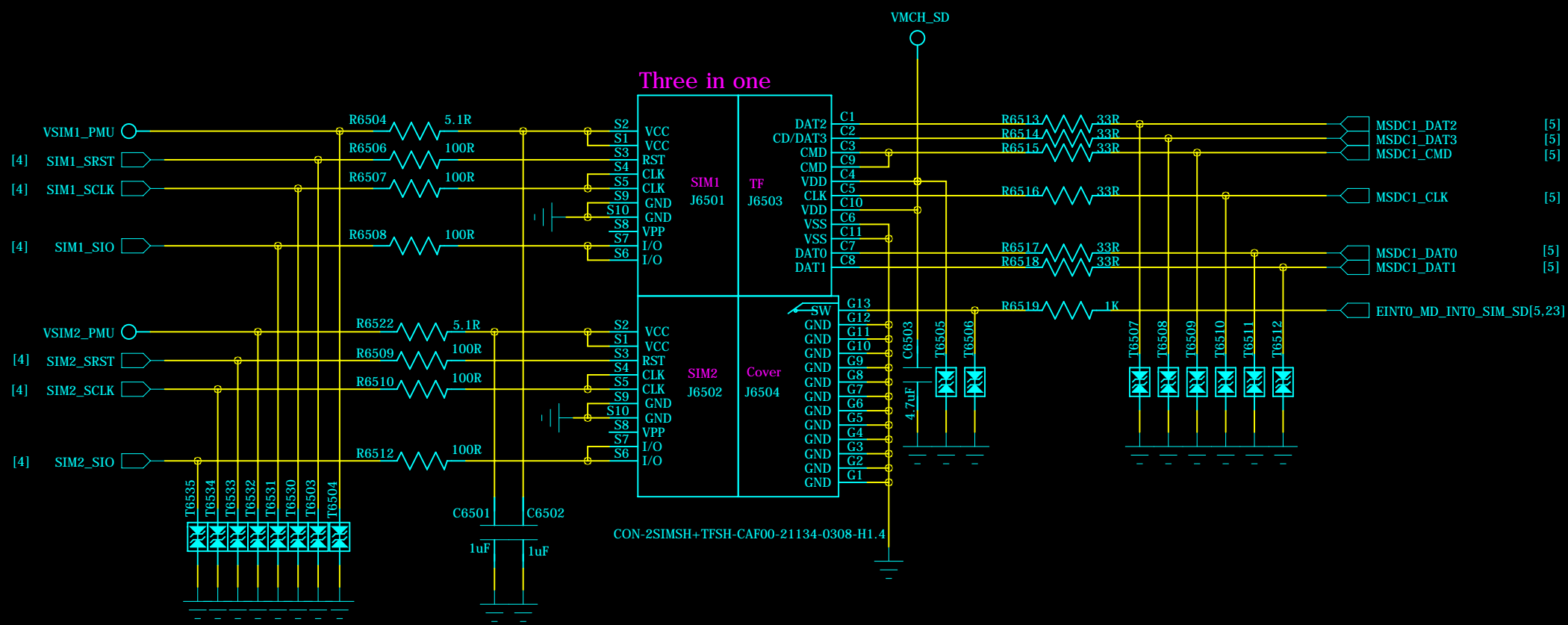
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



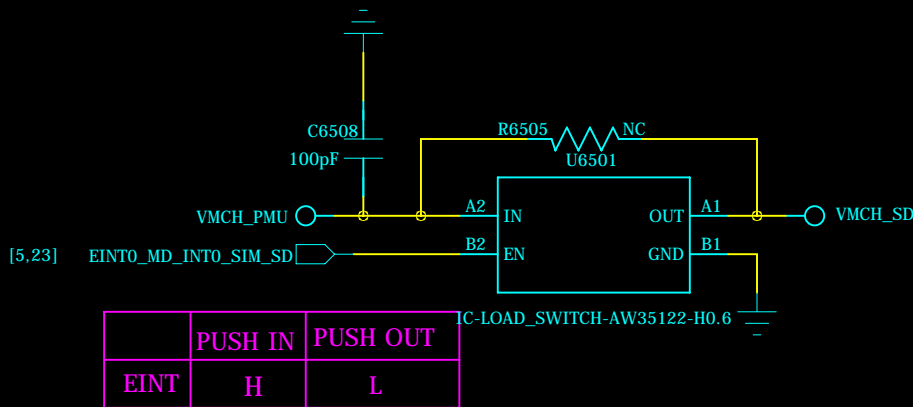
PERI_SIM_SD_KEYPAD

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

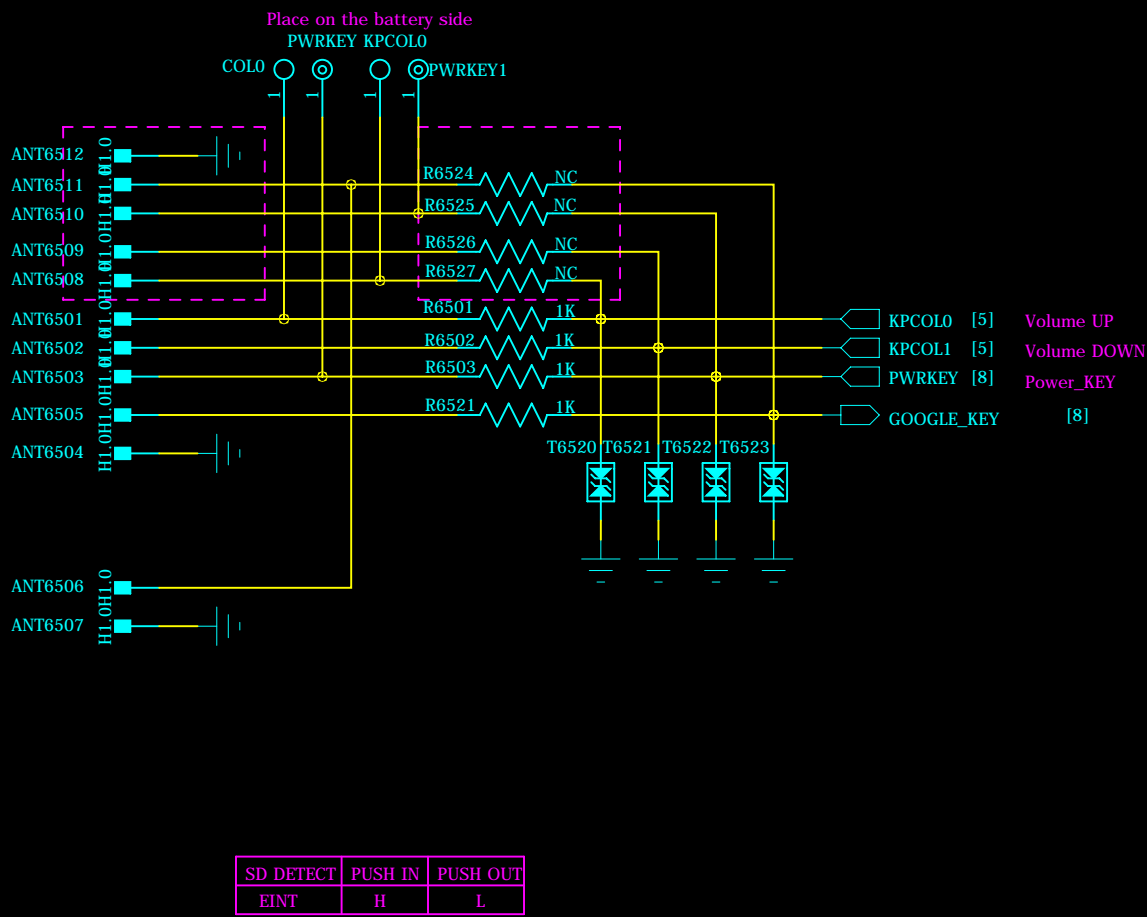
SIM1/2-SD-CARD



SD POWER CONTROL



SIDEKEY AI-KEY



COMPANY: TRANSSION HOLDINGS				MODEL: H6118		Modified Date: 2020/9/11	
DRAWN	WYF	DATED	20200911	TITLE: 65_PERI_SIM_SD_KEYPAD		VERSION: V1.0	SHEET: 23 OF 23
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		