

6

5

4

3

2

1

20210824

1jCJ5605 Battery NTCÉÎÀ-μçÔ´ÓÉVDDRF_1V8±ä³ÉVDD1V85

20210903

2; cÖ0ÐÂÁ¬¼ÓÉÏL6200ÍøÂç

20210910

3;ç0ö¼0R2519;çR2517A½,öI2C6²âE0μã

4;çR2517;çR2518;çR2519ËÀ-μÇ'ÓÉVDDRF_1V8±ä³ÉVDD1V85

20210913

5 ; çÖÖÐÂÁ-¼ÓËÏC6502 ; çL6106ÍøÂç

2021125

6; c0ö¼0R3522; cR3520; cR3519; cR5008

7;ç,ü»»G-sensorİ^aSTK8321

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

D

C

C

B

B

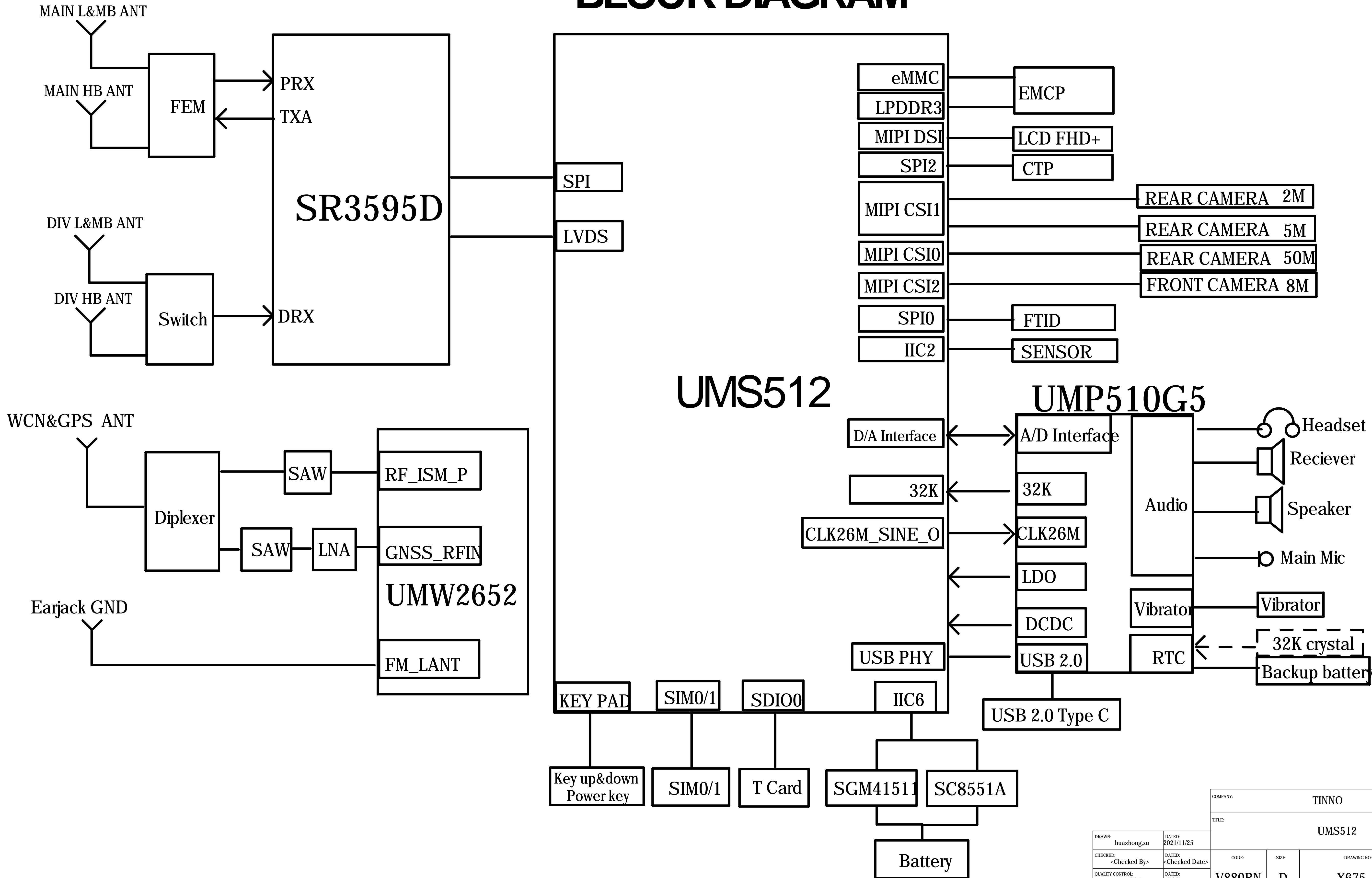
A

A

		COMPANY: TINNO			
		TITLE: UMS512			
DRAWN: huazhong.xu	DATED: 2021/11/25				
CHECKED: <Checked By>	DATED: <Checked Date>	CODE:	SIZE:	DRAWING NO:	REV:
QUALITY CONTROL: <QC By>	DATED: <QC Date>	V880BN	D	X675	V1.0
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>			SHEET: of 31

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

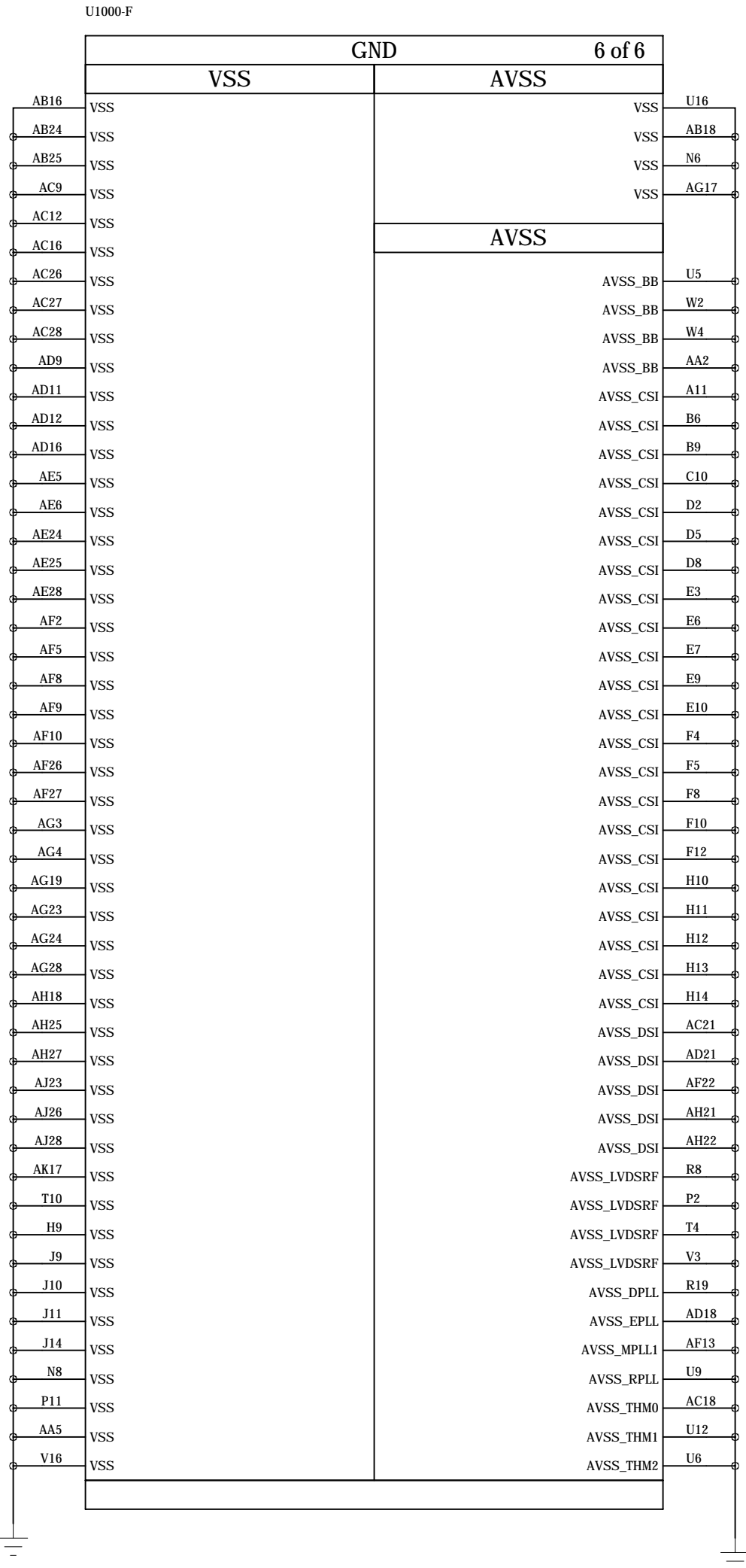
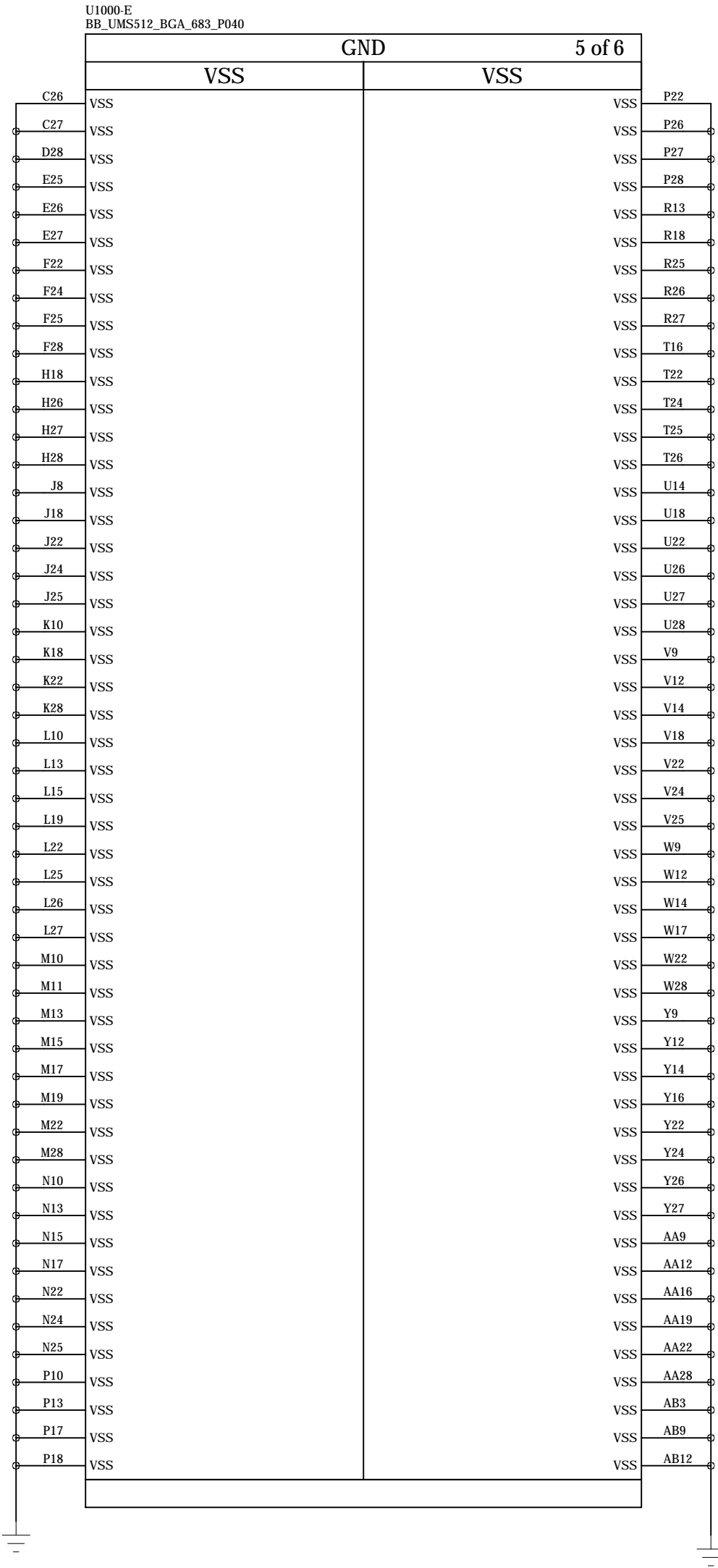
BLOCK DIAGRAM



DRAWN:	huazhong.xu	DATED:	2021/11/25
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>

COMPANY:		TINNO	
TITLE:			
UMS512			
CODE:	SIZE:	DRAWING NO:	REV:
V880BN	D	X675	V1.0
SCALE: <Scale>		SHEET: 2F	31

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:



DRAWN: huazhong.xu	DATED: 2021/11/25
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

COMPANY: TINNO			
TITLE: UMS512			
CODE: V880BN	SIZE: D	DRAWING NO: X675	REV: V1.0
SCALE: <Scale>		SHEET: 3f 31	

6

5

4

3

2

1

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

DIGITAL		1 of 6	
RF SPI 0 (VIO1V8)		KEYPAD (VIO1V8)	
PCC_RFSCK<[21]>	K5	(H) DNU	KEYNO
PCC_RFSDA<[21]>	M5	(H) DNU	KEYN1
PCC_RFSEN<[21]>	L5	KEYN2/EXTINT4/GPIO126	CHG_INT_M [H]
RF SPI 1 (VIO1V8)		SPI 0 (VIO1V8)	
RFSCCK1/GPIO13	J4	KEYOUT0/EXTINT11/CM4_GPIO5G0/GPIO121	FTD_SPL_DI
RFSDA1/GPIO12	K4	KEYOUT1/EXTINT12/CM4_GPIO6G0/GPIO122	FTD_SPL_DO
RFSEN1/GPIO14	K3	KEYOUT2/PWMB/CM4_GPIO7G0/GPIO123	FTD_SPL_CLK
RF CTRL (VSD3/VIO1V8)		SPI 2 (VIO1V8)	
FOR LTE/WCDMA/TD/GSM		SPI0_DO/EXTINT8/GCGPIO91	FTD_SPL_CS
RFCTL0/GPIO19	A12	SPI0_DI/EXTINT7/GCGPIO92	SE2_SPL_MOSI
RFCTL1/GPIO20	A12	SPI0_CLK/EXTINT8/GCGPIO93	SE2_SPL_MISO
RFCTL2/GPIO21	AG2	SPI0_CS/EXTINT3/GCGPIO90	SE2_SPL_CLK
RFCTL3/GPIO22	AG1	SDIO 0 (VSD0)	SE2_SPL_CS_N
RFCTL4/GPIO23	AF1		TF_SD0_CMD
RFCTL5/GPIO24	AC2	SD0_CMD/AUD_DSP_TCK/GPIO150	TF_SD0_D0
RFCTL6/GPIO25	AC1	SD0_D0/AUD_DSP_TMS/GPIO151	TF_SD0_D1
RFCTL7/GPIO26	AB4	SD0_D1/AUD_DSP_RTCK/GPIO152	TF_SD0_D2
RFCTL8/GPIO27	AB5	SD0_D2/AUD_DSP_TDO/GPIO149	TF_SD0_D3
RFCTL9	AB2	SD0_D3/AUD_DSP_TDO/GPIO148	
RFCTL10	P4	SDIO 1 (VIO1V8)	
RFCTL11	P1	SD1_CLK/GPIO64	WF_SD1_CLK
FOR LTE		SD1_CMD/GPIO65	WF_SD1_CMD
RFCTL16/CM4_GPIO1/GPIO7	N3	SD1_D0/GPIO66	WF_SD1_D0
RFCTL17/WIFI_COEXIST/GPIO8	N4	SD1_D1/GPIO67	WF_SD1_D1
WB_WCL2_RXD<[26]>	M4	SD1_D2/GPIO68	WF_SD1_D2
WB_WCL2_TXD<[26]>	M4	SD1_D3/GPIO69	WF_SD1_D3
GPIO (VIO1V8)		SDIO 2 (VSD2)	
SIM0_DET [17]	N5	SD2_CLK/SPI1/CLK/GPIO134	
WB_TX_REQ<[26]>	N2	SD2_CMD/SPI1/DI/GPIO135	
GPIO_FCAM_VCAMA_EN [18]	N1	SD2_D0/SPI1/DO/GPIO136	
SIM1_DET [17]	L2	SD2_D1/SE_GPIO7/GPIO137	
PCC_RFFE_SCK0<[22]>	L1	SD2_D2/SE_GPIO8/GPIO138	
PCC_RFFE_SDA0<[22]>	K1	SD2_D3/SPI1/CSN/GPIO139	
2M/CM4_VCAMD_EN [16]	K2	SIM 0 (VSIM0)	
VDDCAMA_EN [16]	J2	SIMCLK0/GPIO157	SIM0_CLK
for Sensor		SIMDAT0/GPIO158	SIM0_DA
IC2_SCL [17]	E18	SIMRST0/GPIO159	SIM0_RST
IC2_SDA [17]	D18	SIM 1 (VSIM1)	
IC2C_SCL [H]	H2	SIMCLK1/GPIO160	SIM1_CLK
IC2C_SDA [H]	H1	SIMDAT1/GPIO161	SIM1_DA
IC2C_SCL3 [17]	B28	SIMRST1/GPIO162	SIM1_RST
IC2C_SDA3 [17]	B27	SIM 2 (VSIM2)	
CTP_INT [17]	C22	SIMCLK2/CLASE_GPIO11/GPIO154	IC24_SCL
CTP_RST [17]	E22	SIMDAT2/SDA4/SE_GPIO12/GPIO155	IC24_SDA
SD_CARD_DET_N [17]	F16	SIMRST2/CLK_AUX/SE_GPIO13/GPIO156	CHG_INT
BUA_BAT_DET [17]	F17	IIS 0 (VIO1V8)	
EXTINT (VIO1V8)		IIS0I2C/GPIO56	BT_PCM_OUT
ARM_JTAG (VIO1V8)		IIS0DO/GPIO57	BT_PCM_IN
DMIC (VIO1V8)		IIS0CLK/GPIO58	BT_PCM_CLK
		IIS0LRCK/GPIO59	BT_PCM_SYNC
		IIS 1 (VIO1V8)	
		IIS1DO/SE_GPIO0/EXTINT13/GPIO130	FTD_INT
		IIS1DO/SE_GPIO1/CM4_GPIO3G0/GPIO131	FTD_RSTN
		IIS1CLK/SE_GPIO2/CM4_GPIO4G1/GPIO132	LCM_SOURCE_AVDEN
		IIS1LRCK/SE_GPIO3/EXT_INT_EN1/GPIO133	LCM_SOURCE_AVEEN
		UART 0 (VIO1V8)	
		U0TXD/EXT_INT_EN2/GPIO60	WCN_U0RXD
		U0RXD/EXT_INT_EN3/GPIO61	WCN_U0TXD
		UART 4 (VIO1V8)	
		U4TXD/GPIO17	GPS_U1RXD
		U4RXD/GPIO18	GPS_U1TXD
		U4CTS/GPIO34	GPS_U1RTS
		U4RTS/GPIO35	GPS_U1CTS
		UART 5 (VIO1V8)	
		U5TXD/SCLS/GPIO38	BB_U5TXD
		U5RXD/SDA5/GPIO39	BB_U5RXD
		SYSTEM (VIO1V8)	
		CLK_AUX/PROBE_CLK/GPIO129	CON_S2K_IN
		DNS For PRO (VIO1V8)	
		DNS_D0/DNS_PGA_LV_L/GPIO110	LCM_BL_PWM
		DNS_D1/DNS_PGA_LV_R/GPIO111	
NOTE:EXTINT4 mode. GPIO marked (H) will be set HIGH after power-on.			

LK0719

LK0709

LK0720

LK0709

LK0709

LK0714

LK0709

INTERFACE		3 of 6	
A TO D I/F (VIO1V8)		LVDSRF	
ADL_SCLK [17]	AG12	LVDSRF0_UL_P	R3
ADL_D [17]	AJ13	LVDSRF0_UL_N	R4
AUD_SCLK [17]	AJ16	LVDSRF0_DL_PRI_P	U1
AUD_ADSYNC [17]	AK14	LVDSRF0_DL_PRI_N	T1
AUD_ADD0 [17]	AJ15	LVDSRF0_DL_DIV_P	U2
AUD_DASYNC [17]	AJ14	LVDSRF0_DL_DIV_N	V2
AUD_DAD0 [17]	AH12	LVDSRF1_UL_P	B2
AUD_DAD1 [17]	AG13	LVDSRF1_UL_N	T2
VDDARM0_EN [17]	AK15	LVDSRF1_DL_PRI_P	U3
VDDARM1_EN [17]	AJ12	LVDSRF1_DL_PRI_N	U4
EXT_RST_B [17]	AK11	LVDSRF1_DL_DIV_P	T3
XTL_BUF_EN0 [17]	AK12	LVDSRF1_DL_DIV_N	R5
XTL_BUF_EN1 [17]	AK14	CLK_S2K	
ANA_INT [17]	AF15	D TO A CLK26M	
CHP_SLEEP [17]	AH14	CLK26M_OUT	AF16
CLK_S2K [17]		CSI 1	
PMU_26M_IN [17]	AA1	CSI1_DP0	
		CSI1_DN0	
[16] MCS11_DATA0_P	A10	CSI1_DP1	
[16] MCS11_DATA0_N	B10	CSI1_DN1	
		CSI1_CKP	
[16] MCS11_CLK_P	D9	CSI1_CKN	
[16] MCS11_CLK_N	C9	CSI 2 M	
		CSI2_DP0	
[16] MCS12M_DATA0_P	B2	CSI2_DN0	
[16] MCS12M_DATA0_N	B3	CSI2_DP1	
[16] MCS12M_DATA1_P	C4	CSI2_DN1	
[16] MCS12M_DATA1_N	D4	CSI2_CKP0	
[16] MCS12M_CLK_P	D3	CSI2_CKN0	
[16] MCS12M_CLK_N	C3	CSI 2 S	
		CSI2_DP2	
[16] MCS12S_DATA0_P	E2	CSI2_DN2	
[16] MCS12S_DATA1_P	C2	CSI2_DP3	
[16] MCS12S_DATA1_N	C1	CSI2_DN3	
[16] MCS12S_CLK_P	E5	CSI2_CKP1	
[16] MCS12S_CLK_N	E4	CSI2_CKN1	
CSI 0		CSI CTRL (VIO1V8)	
MCS0_DATA0_P [17]	C6	CMCLK0/CLK_AUX2	DNU
MCS0_DATA0_N [17]	D6	CMCLK1/DTDO/GPIO43	
MCS0_DATA1_P [17]	B4	CMCLK2/EXTINT7/G1/GPIO143	
MCS0_DATA1_N [17]	A4	CMPO0/DTCK/GPIO46	
MCS0_CLK_P [17]	A5	CMPO1/DTCK/GPIO47	
MCS0_CLK_N [17]	B5	CMPO2/KEYIN3/GPIO40	
MCS0_DATA2_P [17]	C7	CMRST0/DTDM/GPIO44	
MCS0_DATA2_N [17]	D7	CMRST1/DTMS/GPIO45	
MCS0_DATA3_P [17]	A7	CMRST2/KEYIN4/GPIO41	
MCS0_DATA3_N [17]	B7	SCLO/GPIO48	(H)
		SDA0/GPIO49	(H)
		SDA1/EXTINT15G0/KEYIN6/GPIO74	(H)
		SDA1/EXTINT15G0/KEYIN6/GPIO75	(H)
		DSI CTRL (VIO1V8)	
		DSI_DP0	AG21
		DSI_DN0	AG22
		DSI_DP1	AK22
		DSI_DN1	AK23
		DSI_DP2	AG20
		DSI_DN2	AH20
		DSI_DP3	AJ20
		DSI_DN3	AK20
		DSI_CKP	AJ21
		DSI_CKN	AJ22
		DSI_TE/GPIO51	
		LCM_RSTN/GPIO50	AJ17

close to BB

LVDS_ULP_P_RFA

LVDS_ULP_N_RFA

LVDS_ULP_P_RFA

LVDS_ULP_N_RFA

LVDS_ULP_P_RFA

LVDS_ULP_N_RFA

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

LVDS_ULP_P_RFB

LVDS_ULP_N_RFB

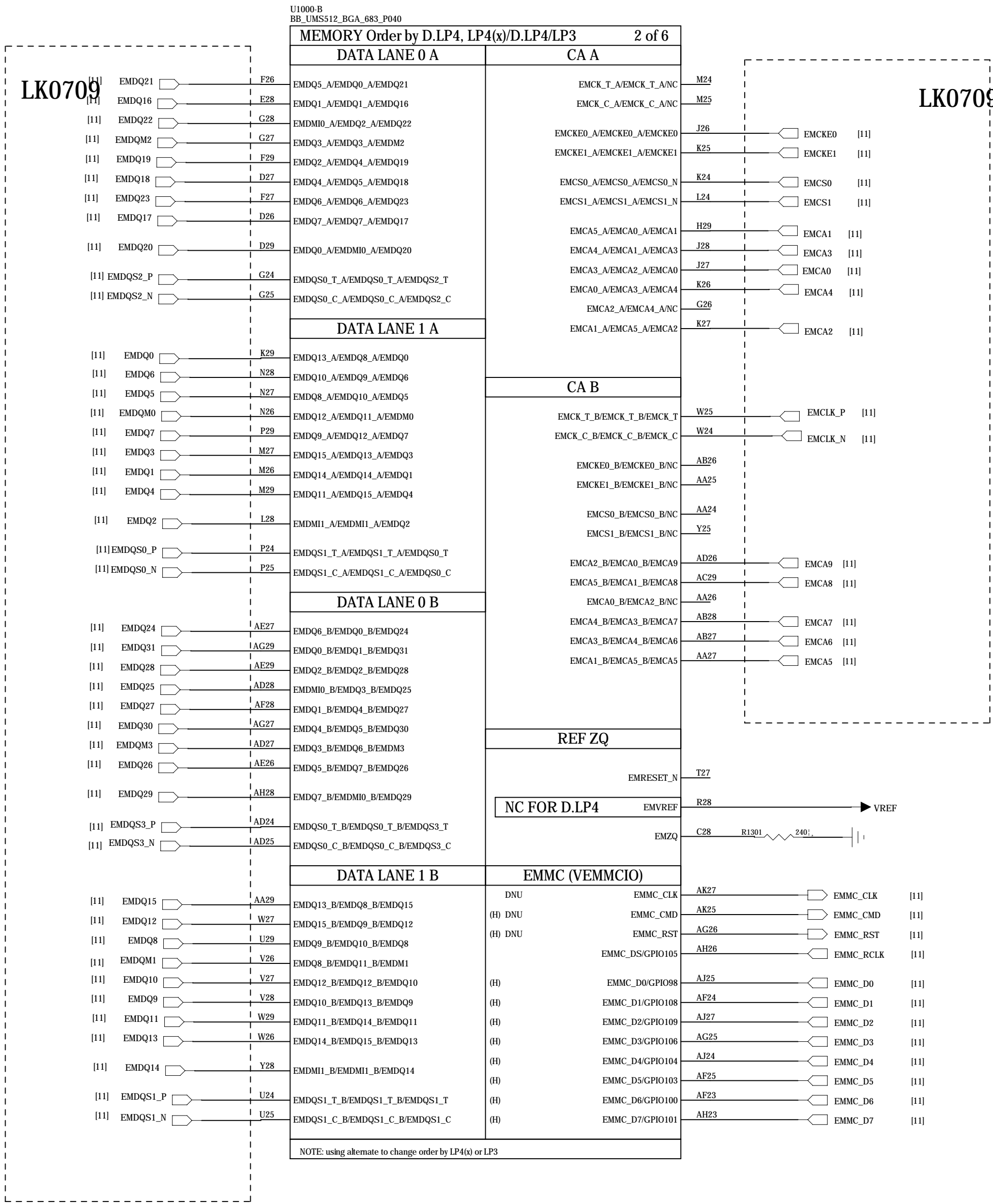
TINNO

UMS512

DRAWN:	huazhong.xu	DATED:	2021/11/25
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>

CODE:	SIZE:	DRAWING NO:	REV:
V880BN	D	X675	V1.0
SCALE: <Scale>		SHEET: 4f	31

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:



DRAWN:	huazhong.xu	DATED:	2021/11/25
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>

COMPANY:		TINNO	
TITLE:		UMS512	
CODE:	SIZE:	DRAWING NO:	REV:
V880BN	D	X675	V1.0
SCALE: <Scale>		SHEET: 6f	31

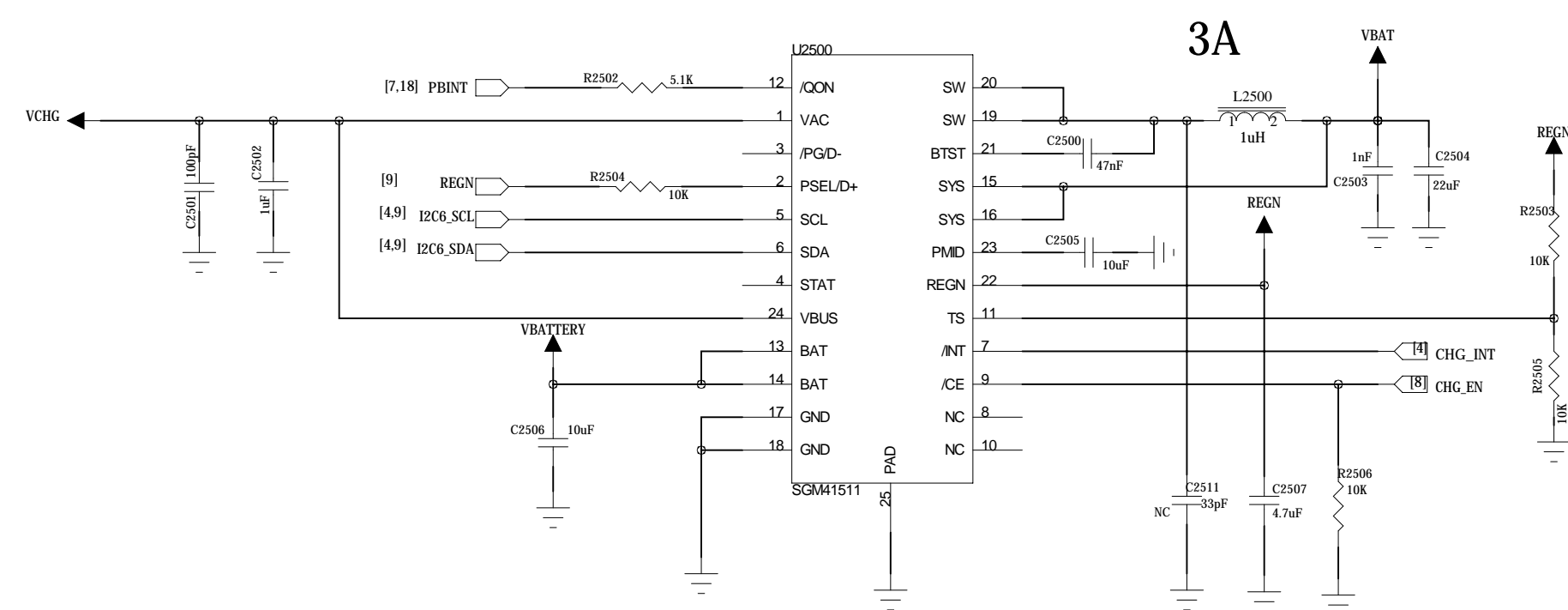
D

A

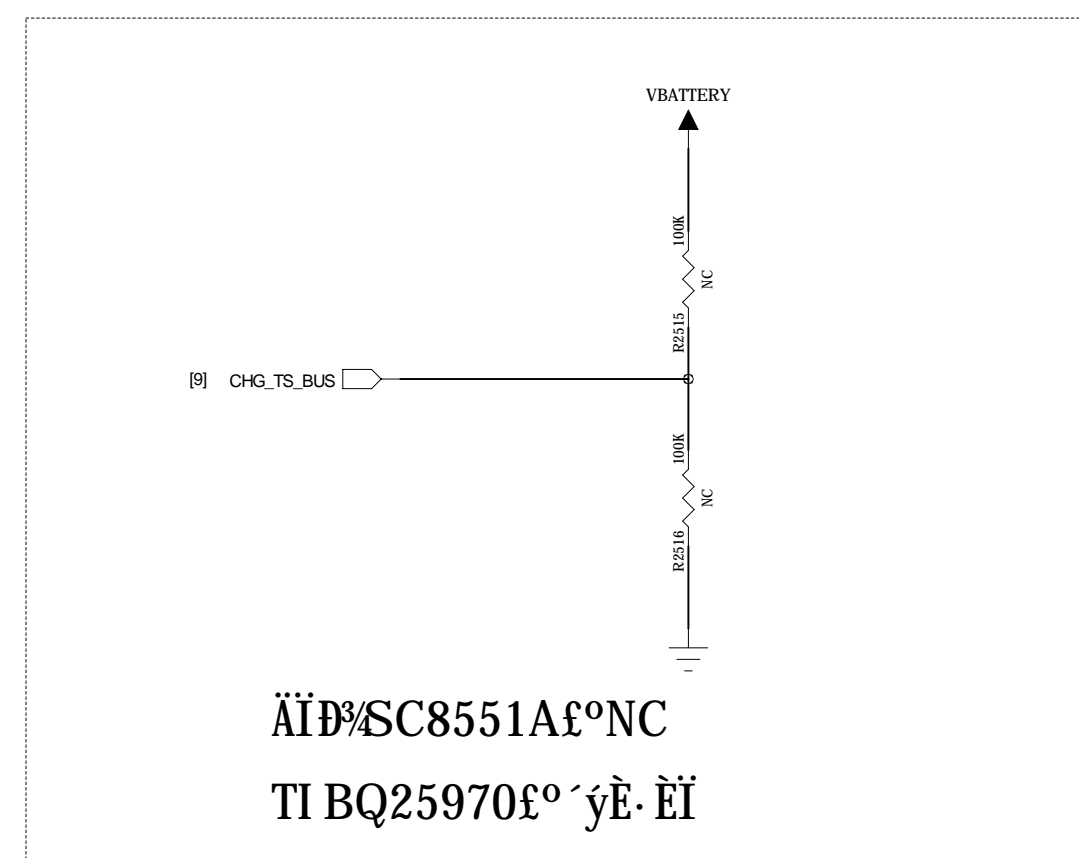
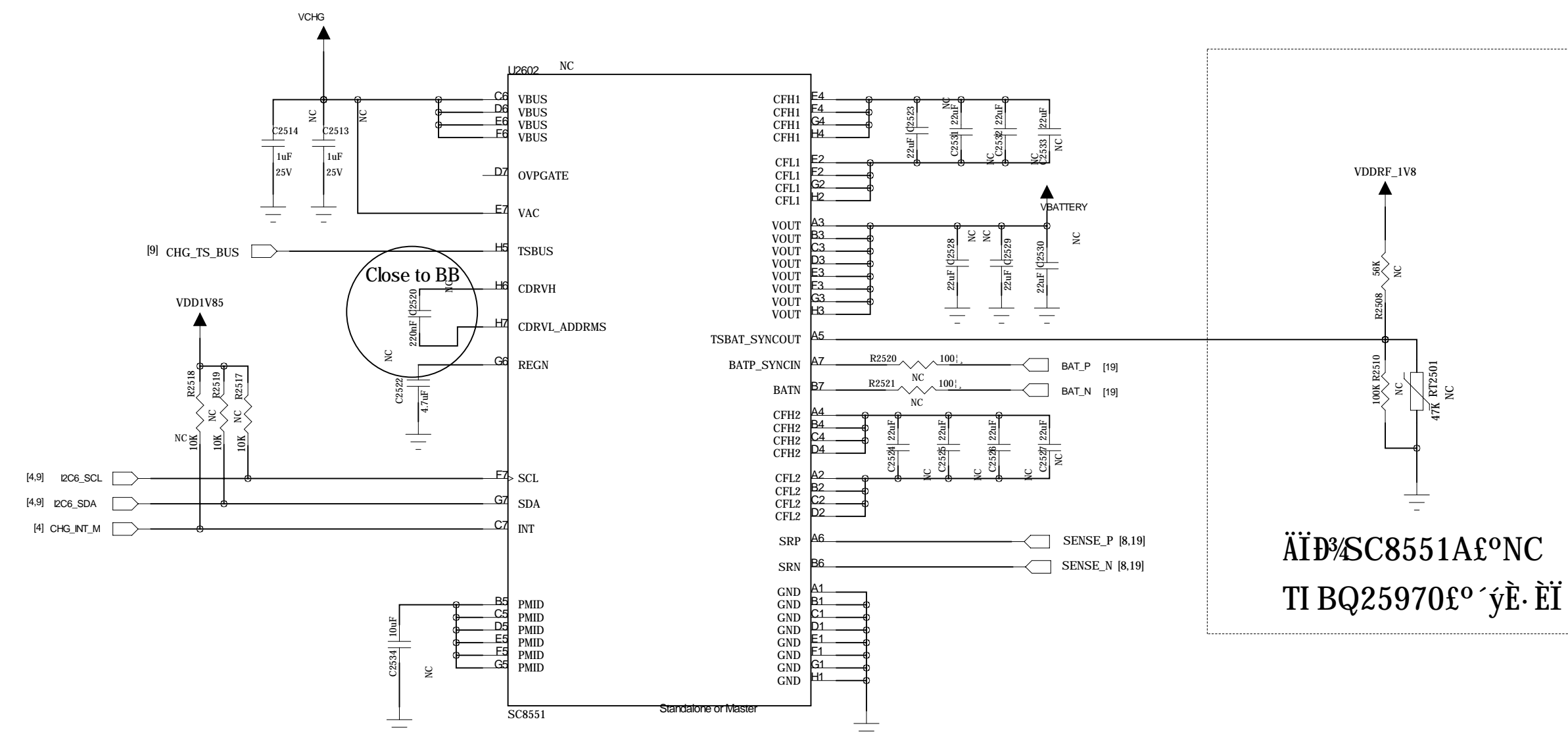
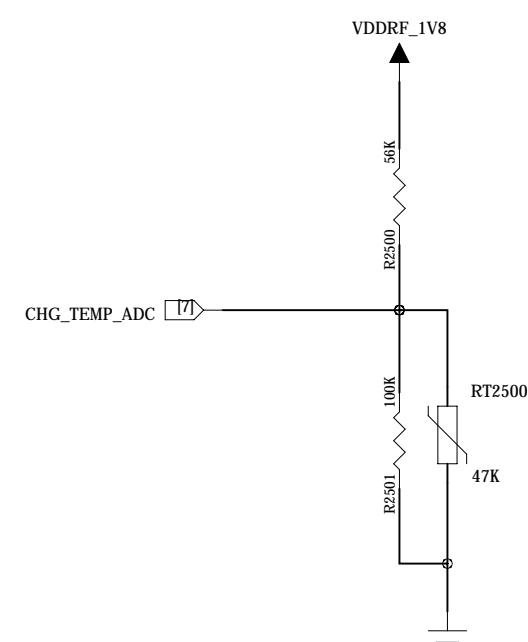
DRAWN: huazhong.xu	DATED: 2021/11/25
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

CHARGE

Ö÷³ äµçÐ¾⁄⁄



I2C ADDR:



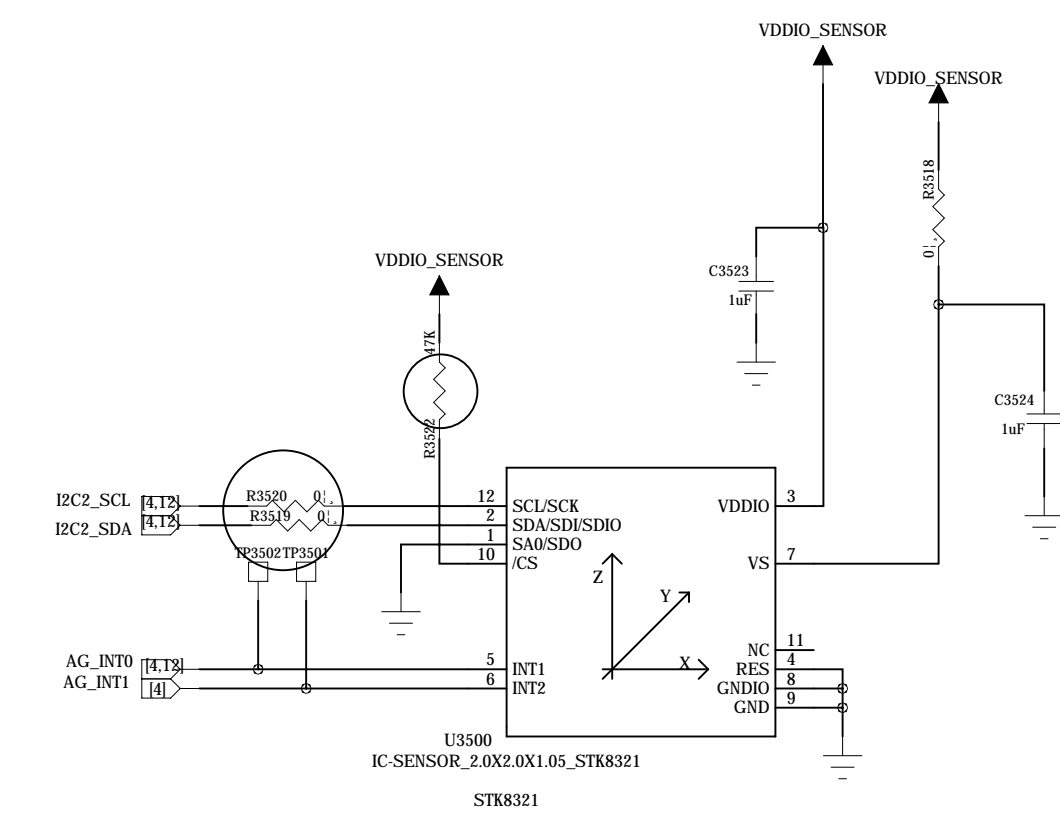
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

DRAWN: huazhong.xu	DATED: 2021/11/25
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

COMPANY:		TINNO	
TITLE:		UMS512	
CODE:	SIZE:	DRAWING NO:	REV:
V880BN	D	X675	V1.0
SCALE: <Scale>		SHEET: 0f	31

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

Accelerometer

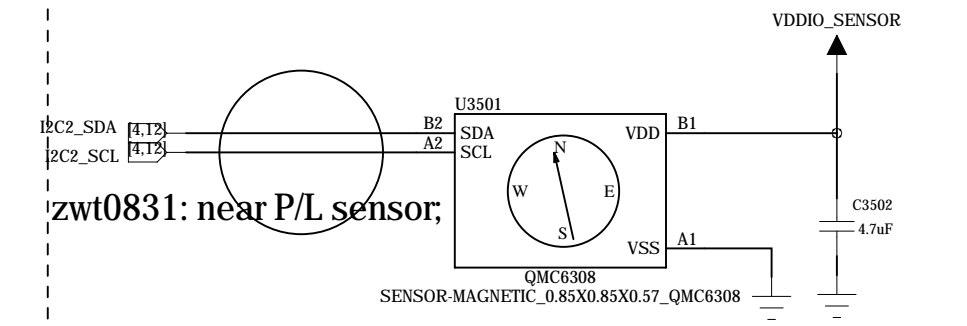


	SA0=0	SA0=1
STK8321	0X0F	0X1F
MC3416-P	0x98W;0x99R	0xD8W;0xD9R

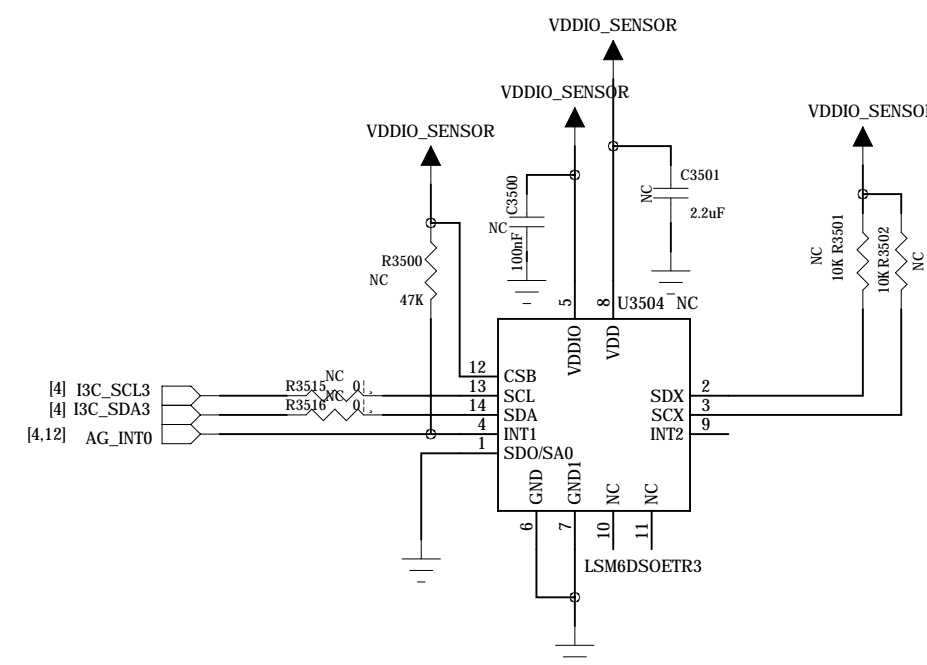


Note: 77-1

```
| For debugging
```

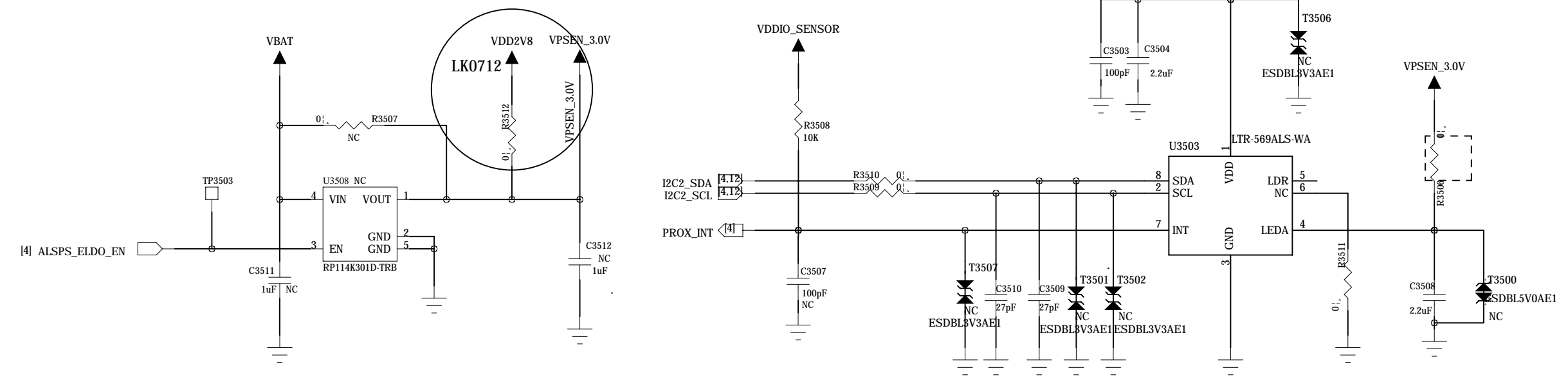


G+Gyro Sensor Reserved NC



PL Sensor

	I2C addr
STK33562	
MN78911D	

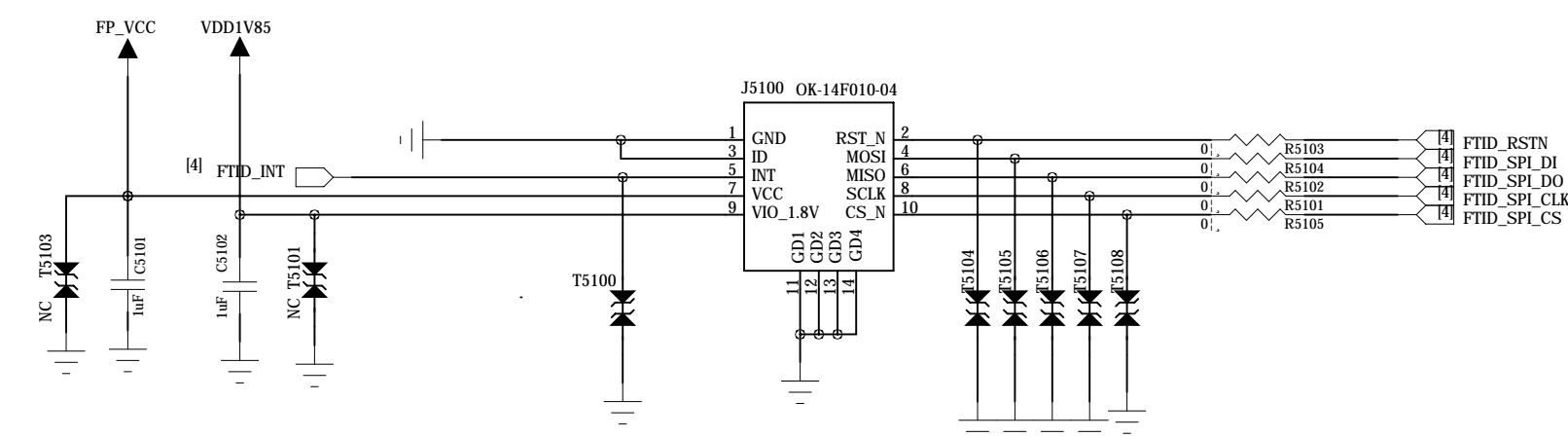


DRAWN: huazhong.xu	DATED: 2021/11/25
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

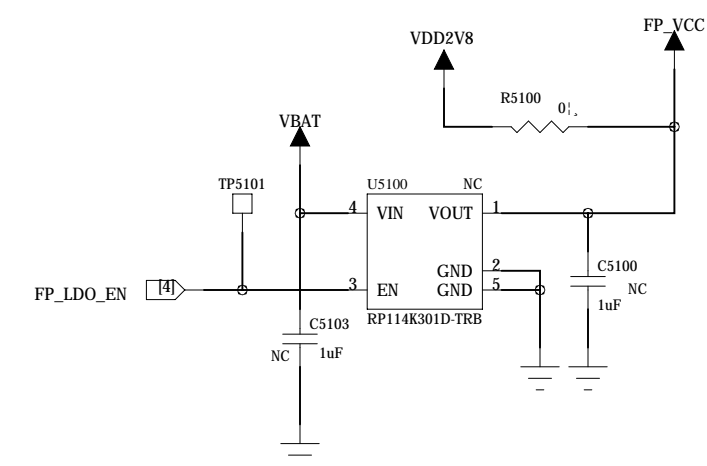
COMPANY:		TINNO	
TITLE:		UMS512	
CODE:	SIZE:	DRAWING NO:	REV:
V880BN	D	X675	V1.0
SCALE: <Scale>		SHEET: 42	31

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

Fingerprint Connector



1	GND
2	RST_N
3	GND
4	MOSI
5	INT
6	MISO
7	VCC:3.0V
8	SCLK
9	DVDD1V8
10	CS_N



		COMPANY: TINNO			
		TITLE: UMS512			
DRAWN: huazhong.xu	DATED: 2021/11/25				
CHECKED: <Checked By>	DATED: <Checked Date>	CODE:	SIZE:	DRAWING NO:	REV:
QUALITY CONTROL: <QC By>	DATED: <QC Date>	V880BN	D	X675	V1.0
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>			SHEET: 46 31

Main-CAMERA

50M: 4 lane

AFVDD	2.8V
AVDD	2.8V
DVDD	1.1V
IOVDD	1.8V
addr	

F-CAMERA

8M: 2 lane

AVDD	2.8V
DVDD	1.2V
IOVDD	1.8V
addr	

Depth-CAMERA

2M: 2 lane

AVDD	2.8V
DVDD	1.8V
IOVDD	1.8V
addr	

MIAN-FLASH

Wide-CAMERA

5M: 2 lane

AVDD	2.8V
VDDCORE1	1.2V
IOVDD	1.8V
addr	

FCAM CAM AVDD 2.8v

2MCAM CAM AVDD 2.8v

Front/Wide CAM DVDD 1.05v

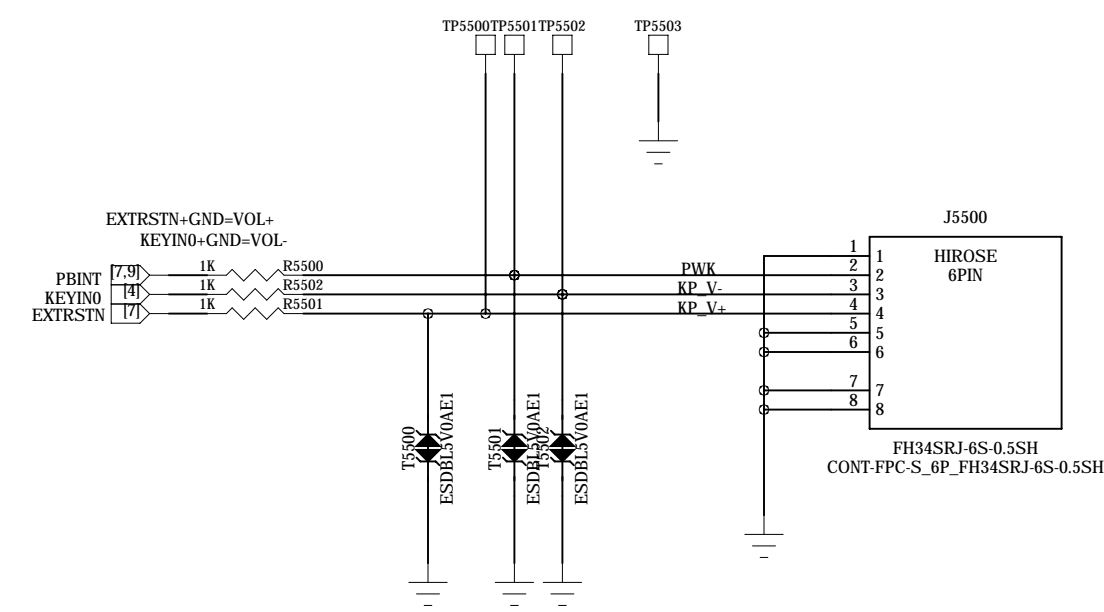
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

DRAWN:	huazhong.xu	DATED:	2021/11/25
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>

COMPANY: TINNO			
TITLE: UMS512			
CODE: V880BN	SIZE: D	DRAWING NO: X675	REV: V1.0
SCALE: <Scale>		SHEET: 46	31

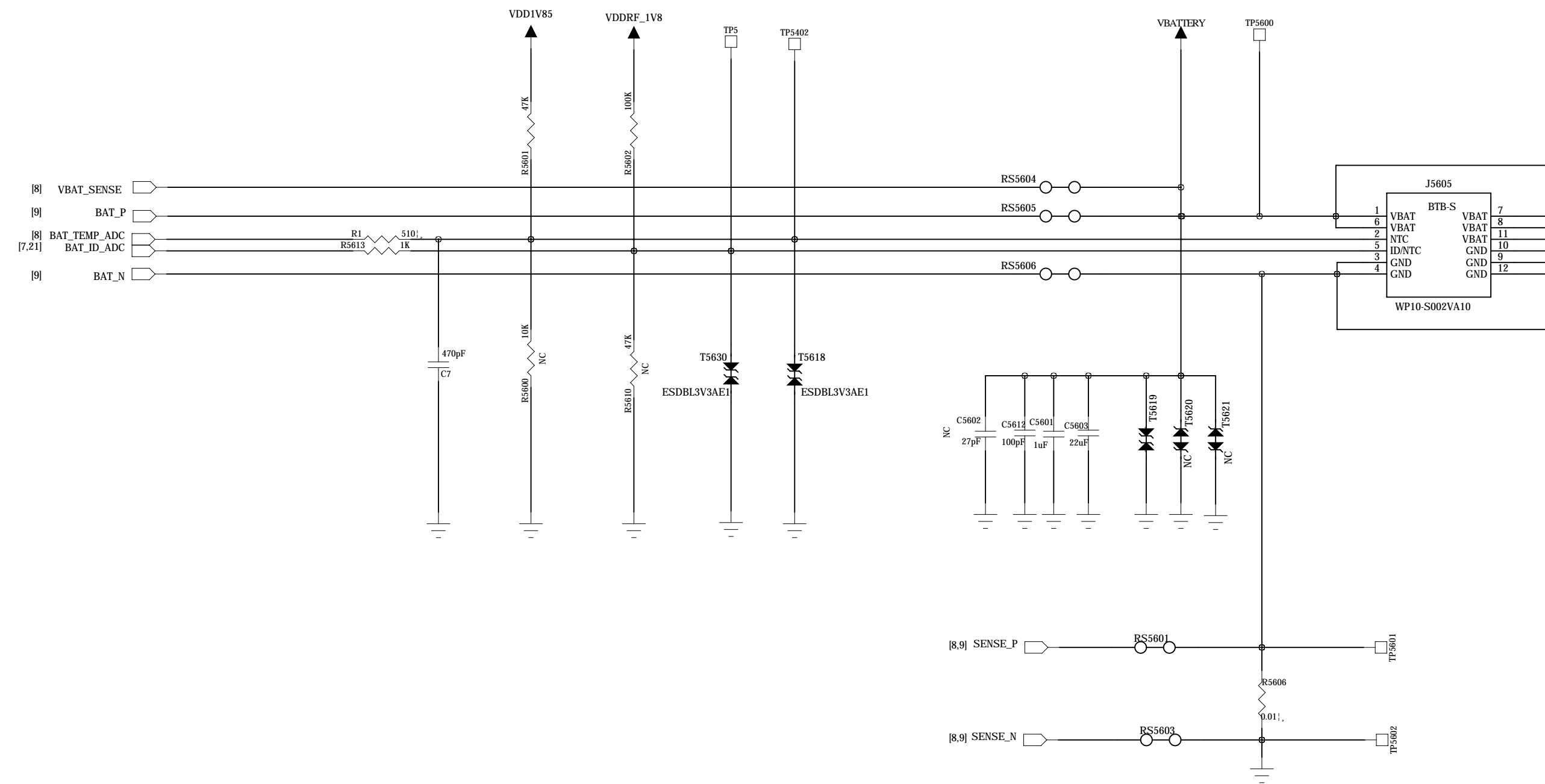
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

MP Test point



		COMPANY: TINNO			
		TITLE: UMS512			
DRAWN: huazhong.xu	DATED: 2021/11/25	CODE:	SIZE:	DRAWING NO:	REV:
CHECKED: <Checked By>	DATED: <Checked Date>	V880BN	D	X675	V1.0
QUALITY CONTROL: <QC By>	DATED: <QC Date>				
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>	SHEET: 48 31		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



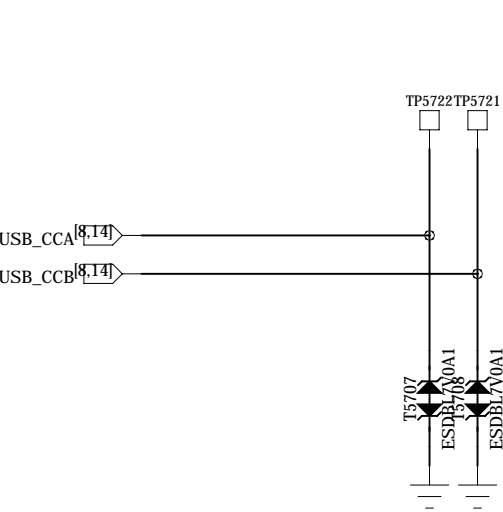
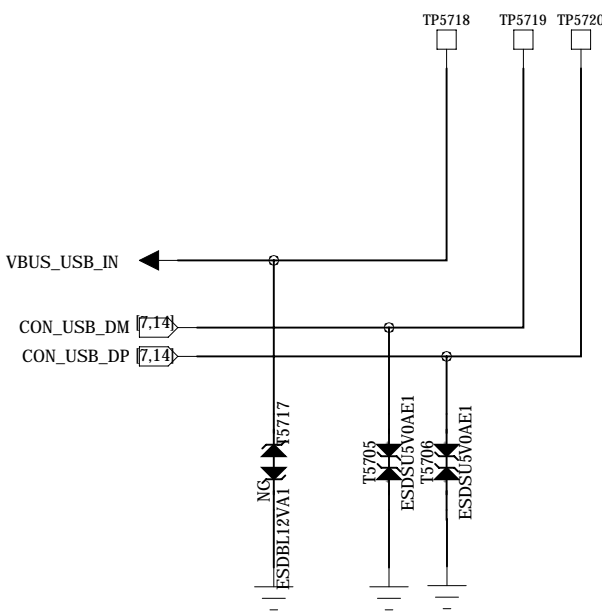
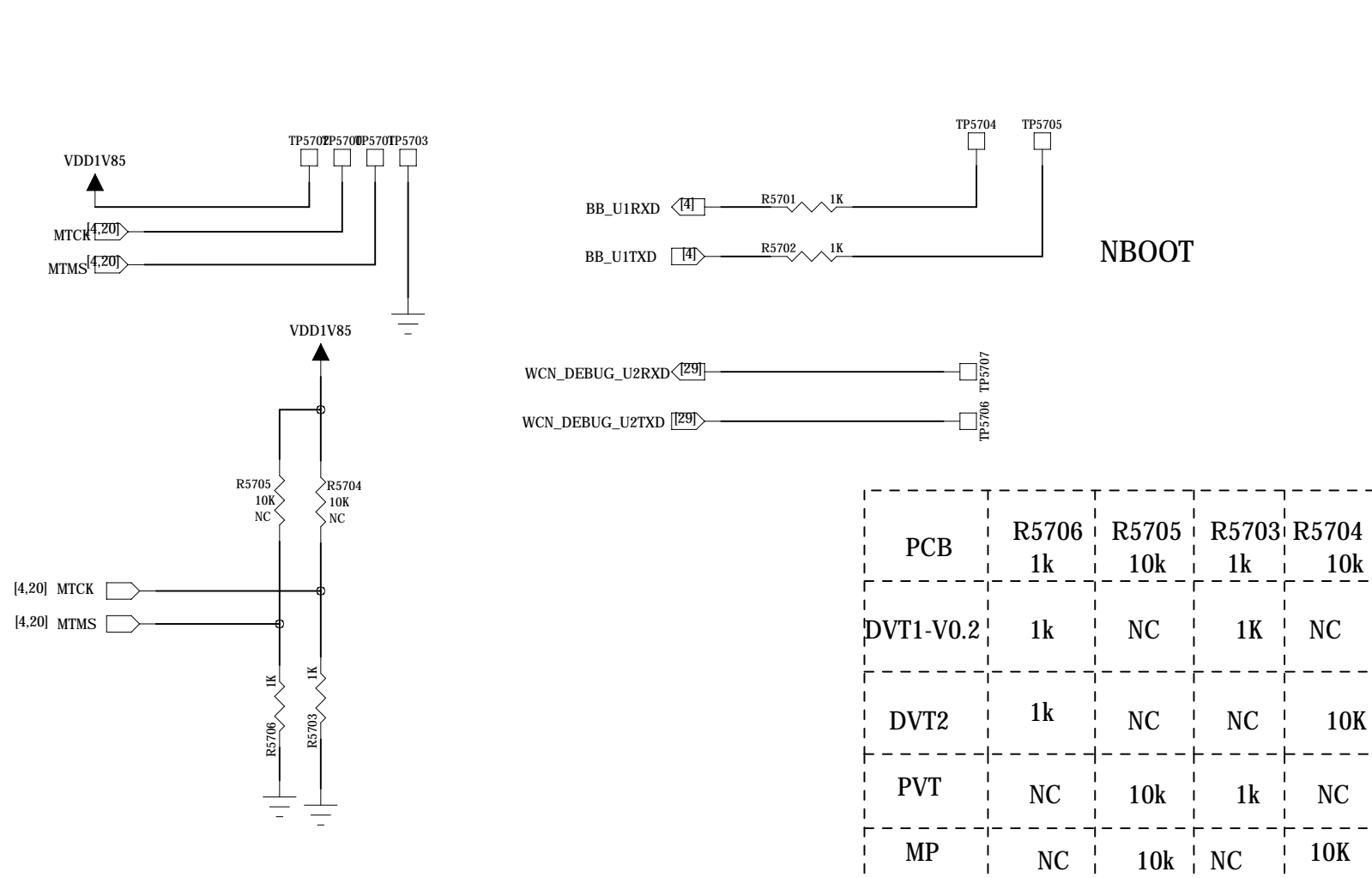
		COMPANY: TINNO			
		TITLE: UMS512			
DRAWN: huazhong.xu	DATE: 2021/11/25				
CHECKED: -<Checked By>	DATE: -<Checked Date>	CODE:	SIZE:	DRAWING NO:	REV:
QUALITY CONTROL: -<QC By>	DATE: -<QC Date>	V880BN	D	X675	V1.0
RELEASED: -<Released By>	DATE: -<Release Date>	SCALE: <Scale>			SHEET: 40 31

Debug Test Point

Fixture Test Point

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

USB

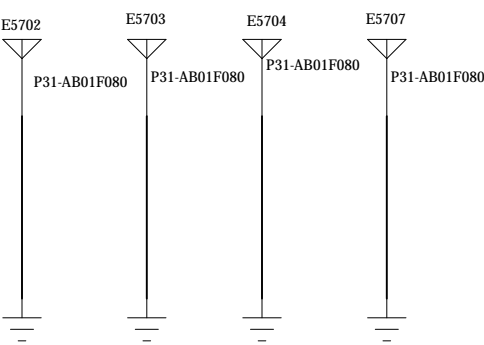


Mark

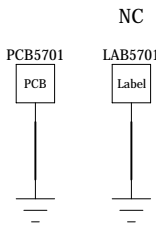
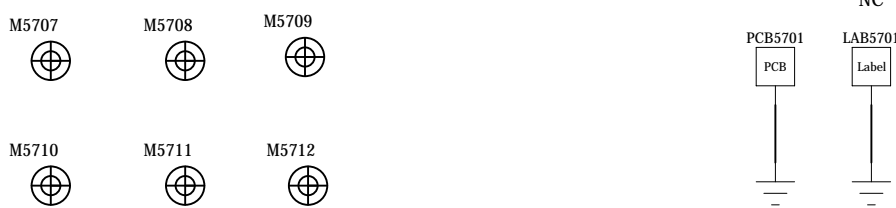
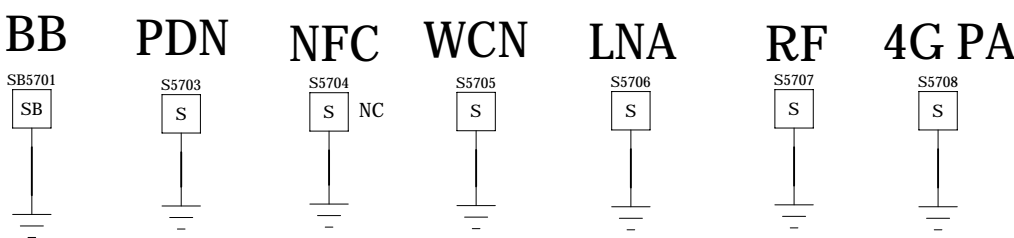
shielding can

Metal Grounding

Top



Bottom



DRAWN:		DATED: 2021/11/25		COMPANY: TINNO			
CHECKED: <Checked By>		DATED: <Checked Date>		TITLE: UMS512			
QUALITY CONTROL: <QC By>		DATED: <QC Date>		CODE: V880BN	SIZE: D	DRAWING NO: X675	REV: V1.0
RELEASED: <Released By>		DATED: <Release Date>		SCALE: <Scale>		SHEET: 20 31	

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

VPA_PMU width>60mil(min width>=20mil,length<=100mils)
VCC1>=10mil;VCC2>=50mil
VPA_PMU PA side load C 6.2UF+-5%

VCC10eVCC2·Ö; a×BİB

close to SR3595D

B40_TX

B41(B38)_TX

B7

B2

B3

B1

B66(B4)

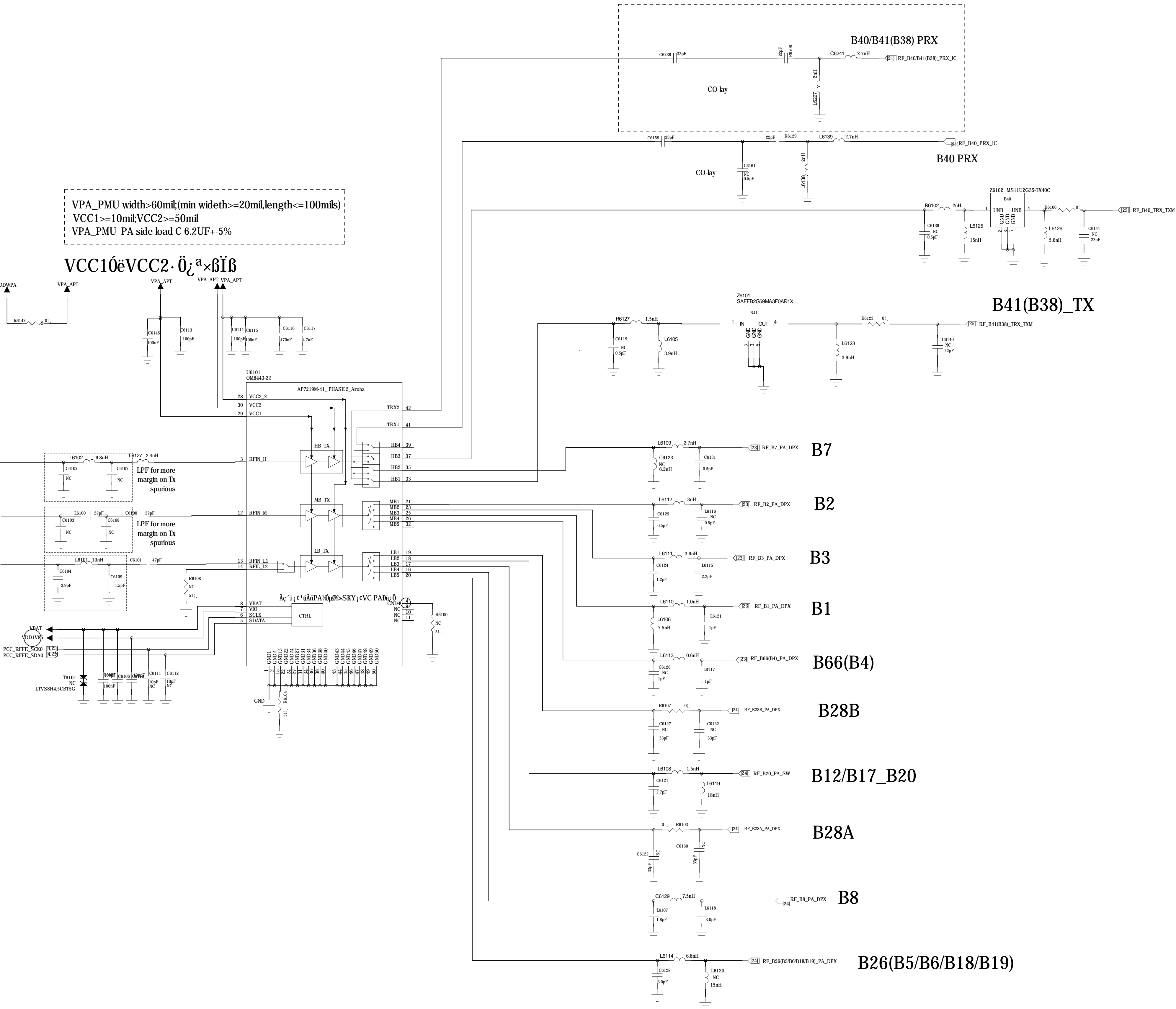
B28B

B12/B17_B20

B28A

B8

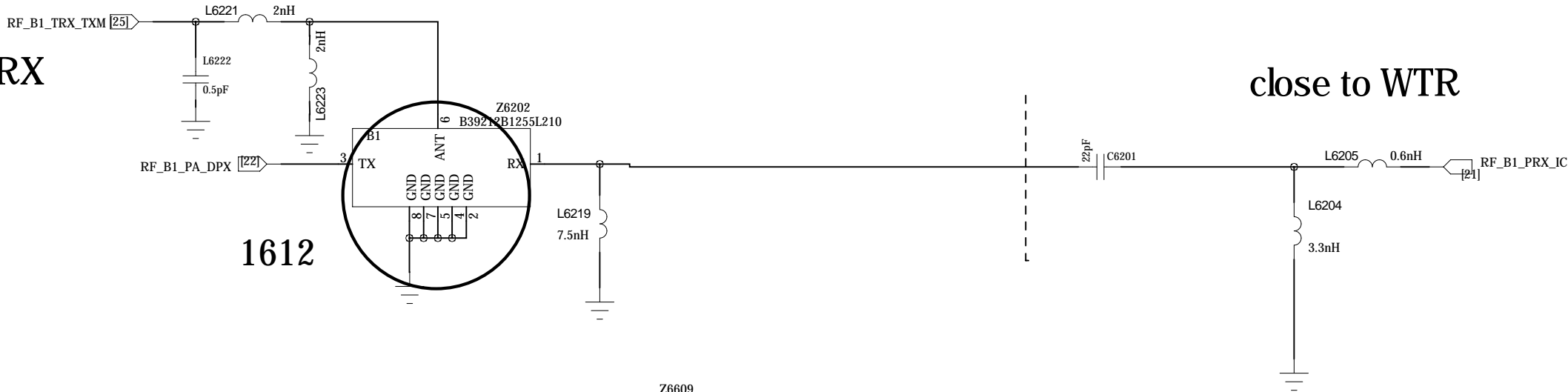
B26(B5/B6/B18/B19)



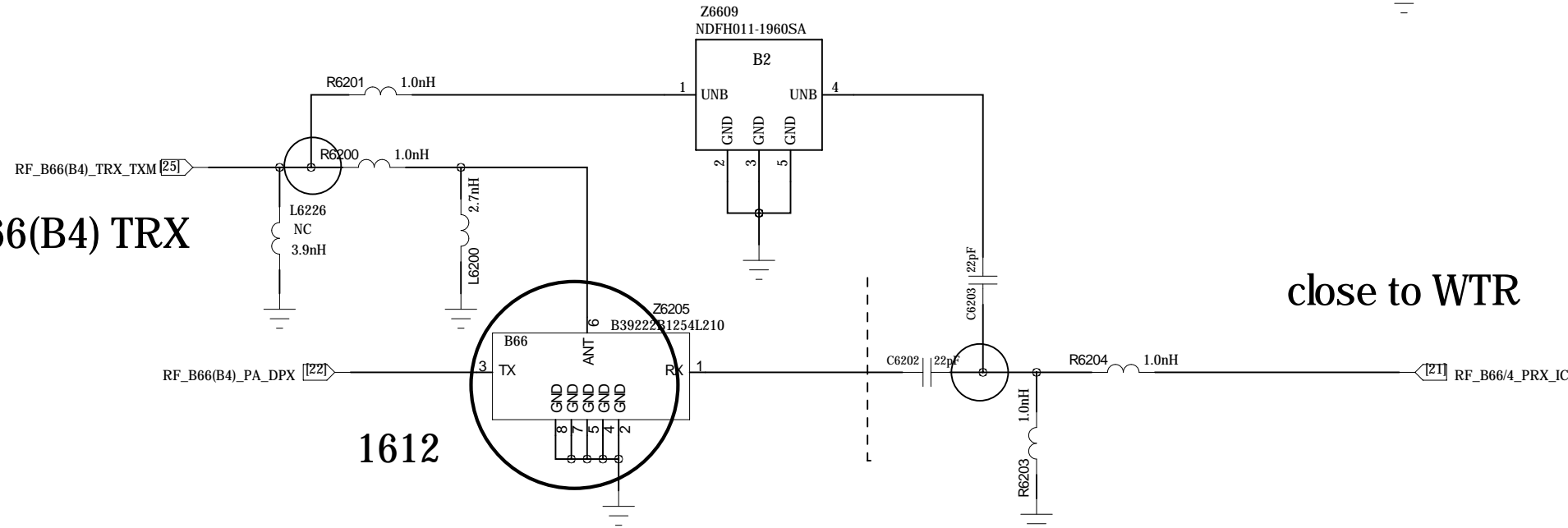
DRAWN: huazhongxu		DATED: 2021/11/25		COMPANY: TINNO			
CHECKED: <Checked By>		DATED: <Checked Date>		TITLE: UMS512			
QUALITY CONTROL: <QC By>		DATED: <QC Date>		CODE: V880BN	SIZE: D	DRAWING NO: X675	REV: V1.0
RELEASED: <Released By>		DATED: <Release Date>		SCALE: <Scale>			SHEET: 22 31

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

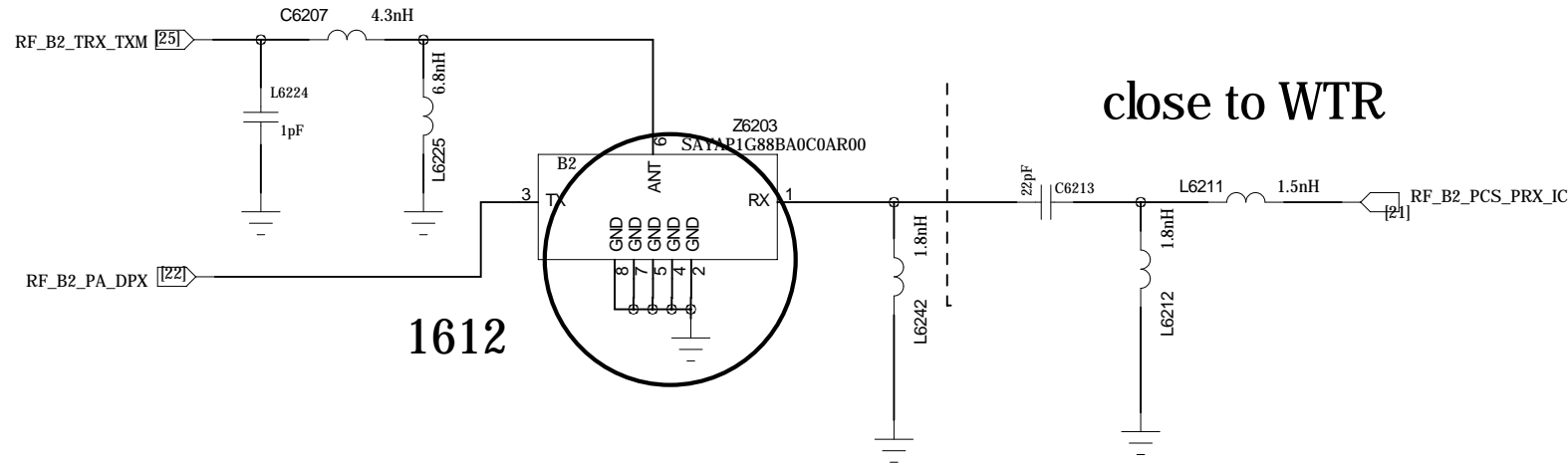
B1 TRX



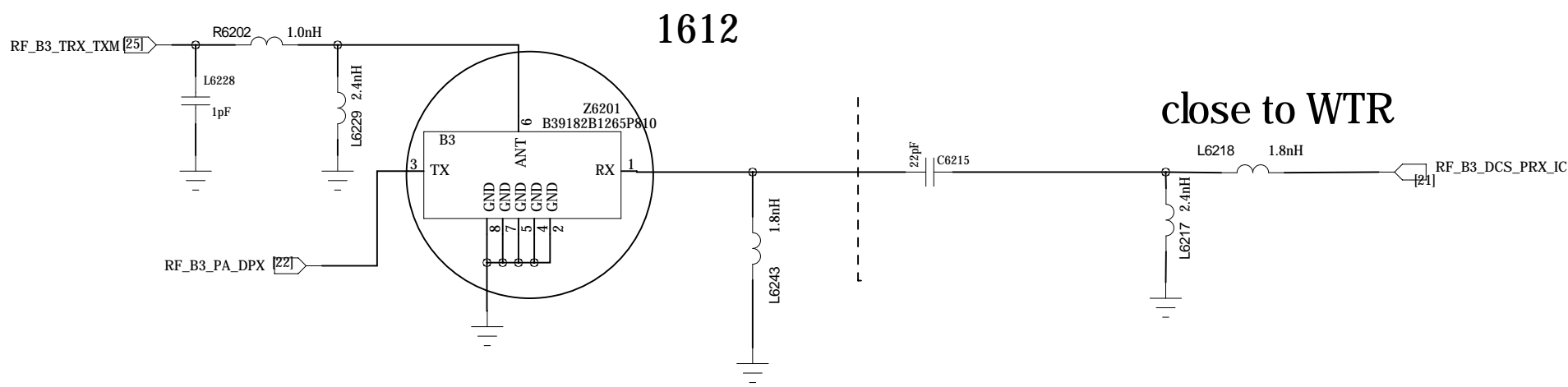
B66(B4) TRX



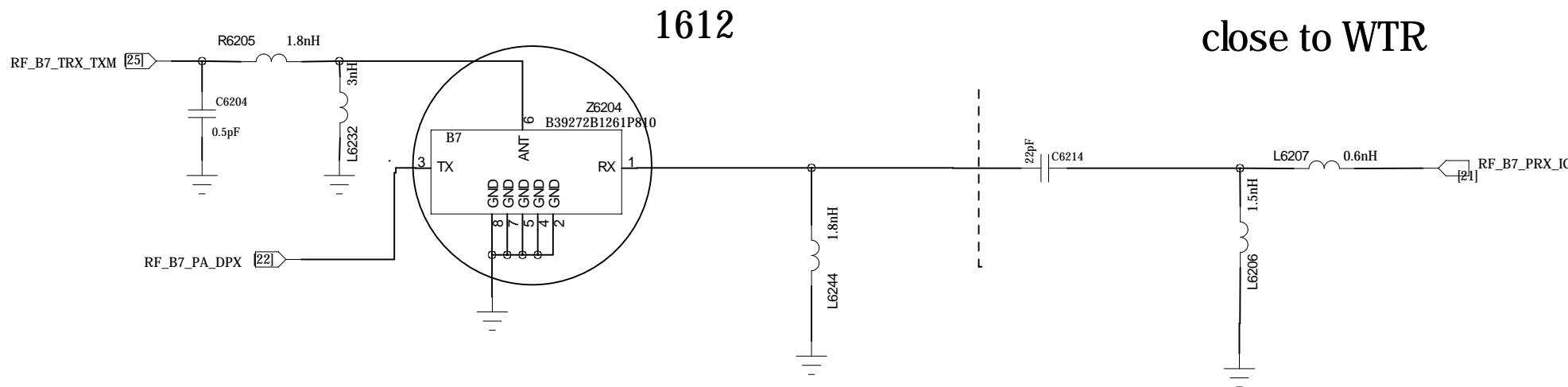
B2 TRX



B3 TRX



B7 TRX



COMPANY: TINNO

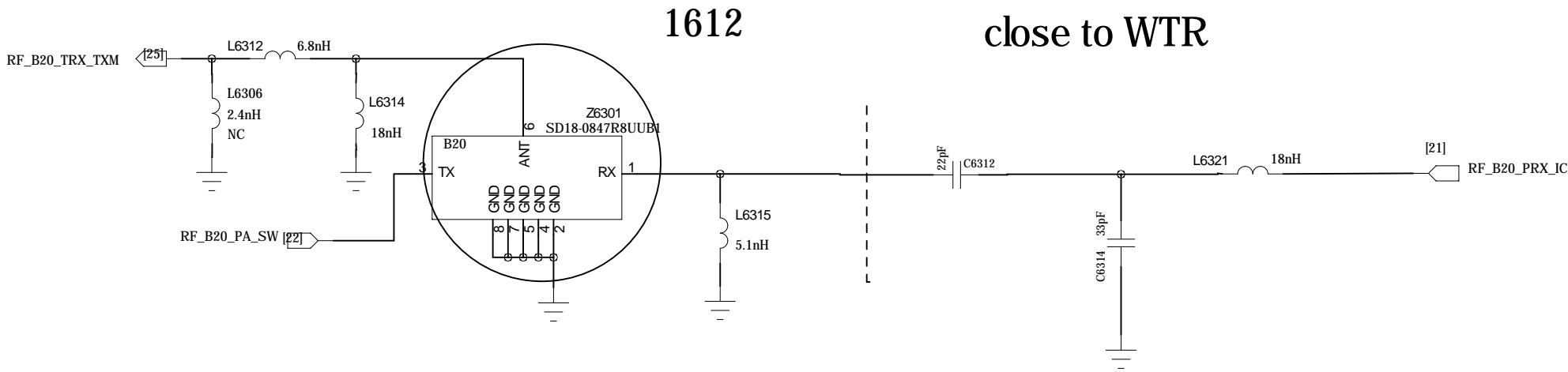
TITLE: UMS512

DRAWN: huazhong.xu	DATED: 2021/11/25
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

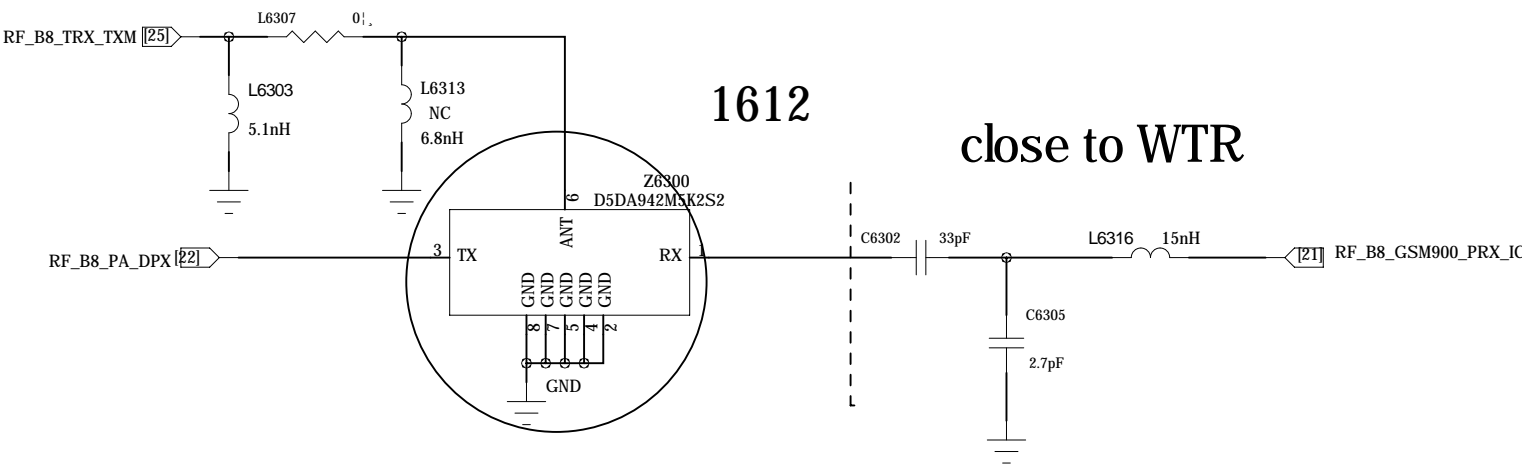
CODE:	SIZE:	DRAWING NO:	REV:
V880BN	D	X675	V1.0
SCALE: <Scale>		SHEET: 28 31	

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

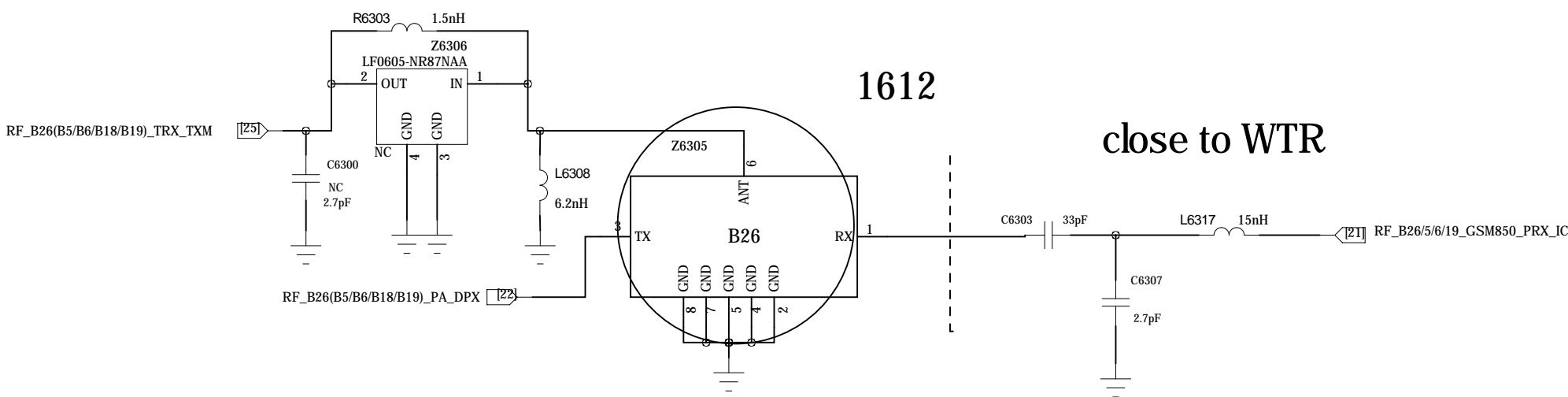
B20 TRX



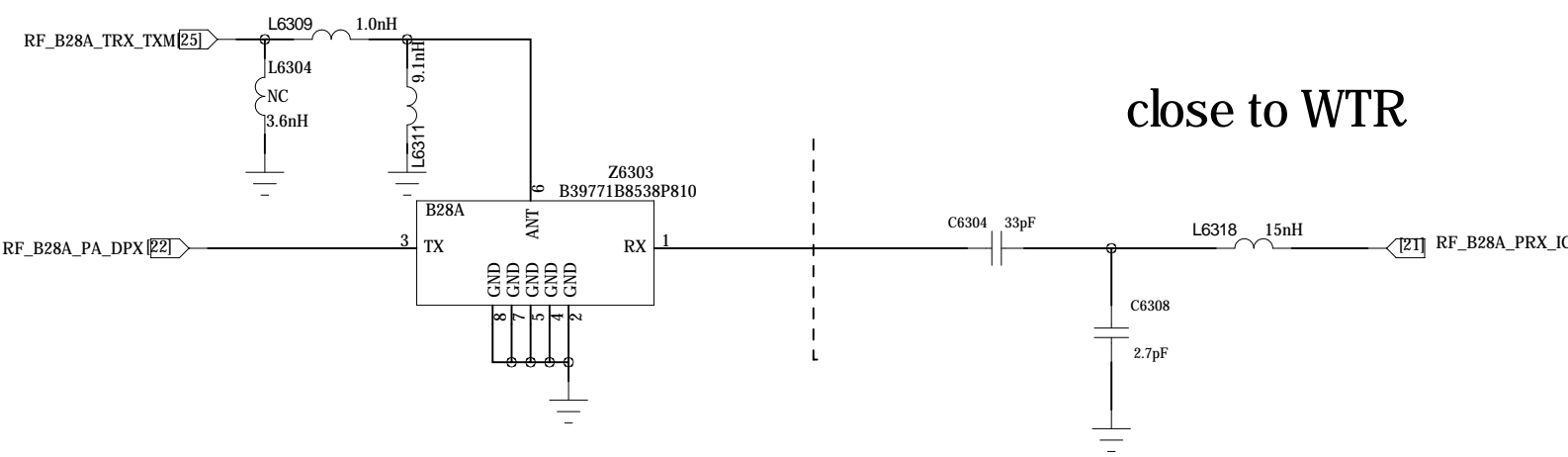
B8 TRX



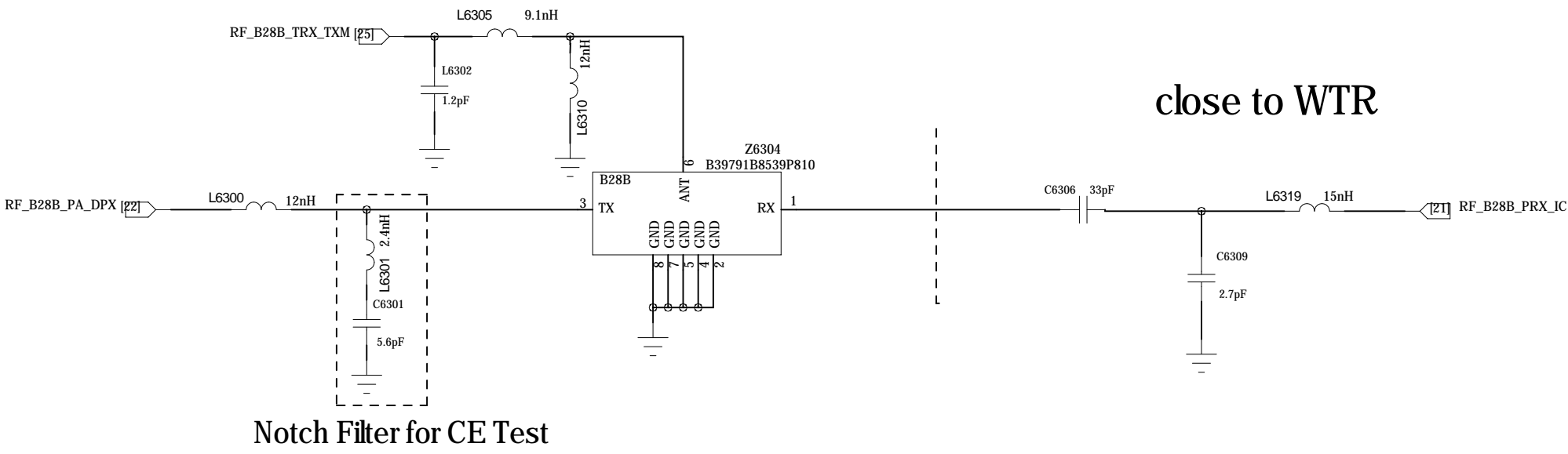
B26(B5/B6/B18/B19) TRX



B28A TRX

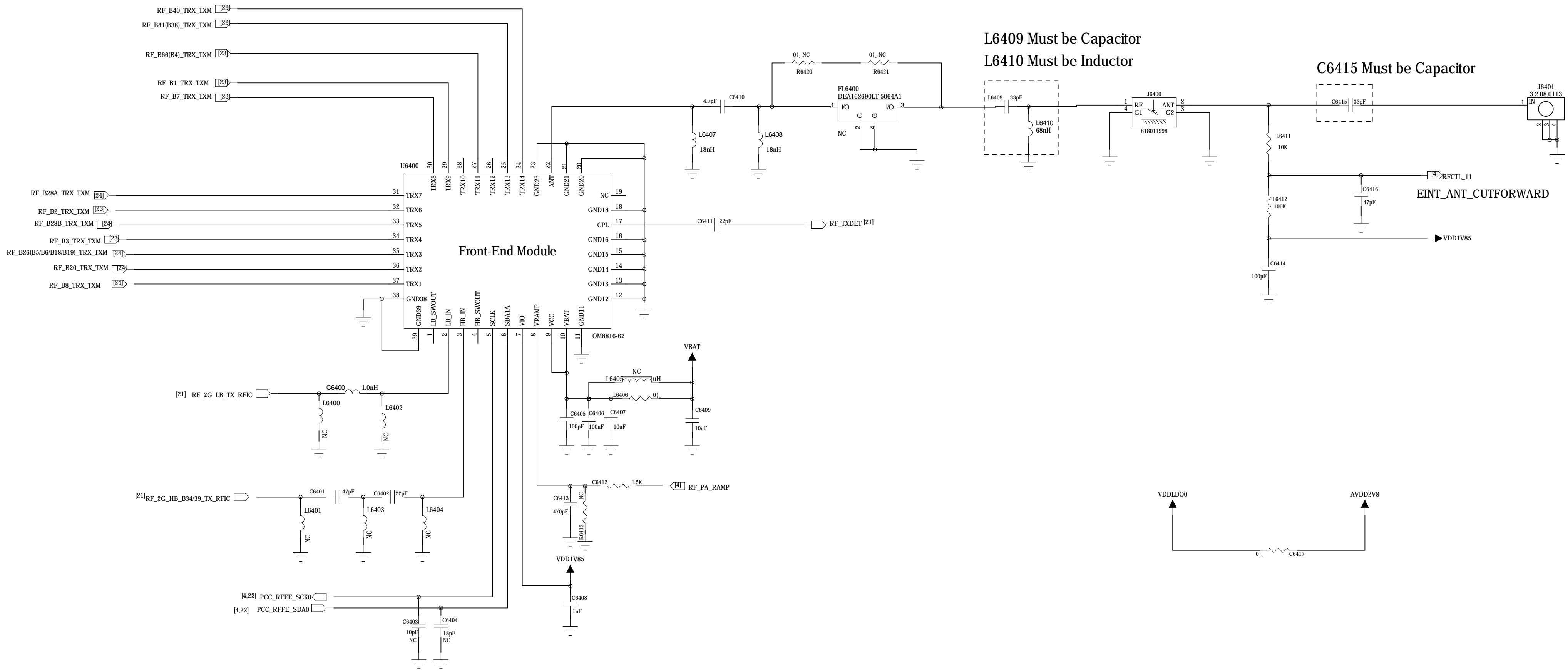


B28B TRX



COMPANY: TINNO			
TITLE: UMS512			
DRAWN: huazhong.xu	DATED: 2021/11/25	CODE: V880BN	
CHECKED: <Checked By>	DATED: <Checked Date>	SIZE: D	DRAWING NO: X675
QUALITY CONTROL: <QC By>	DATED: <QC Date>	REV: V1.0	
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>	
SHEET: 24		31	

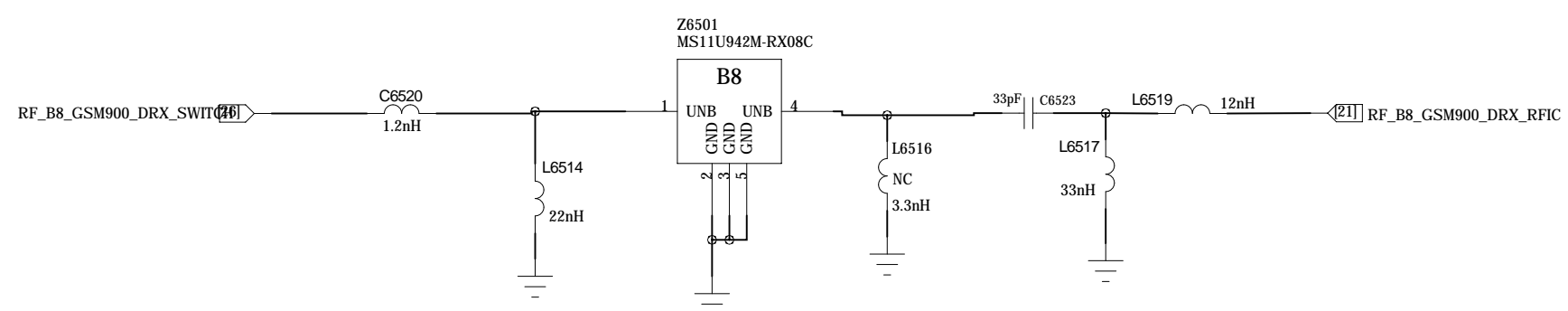
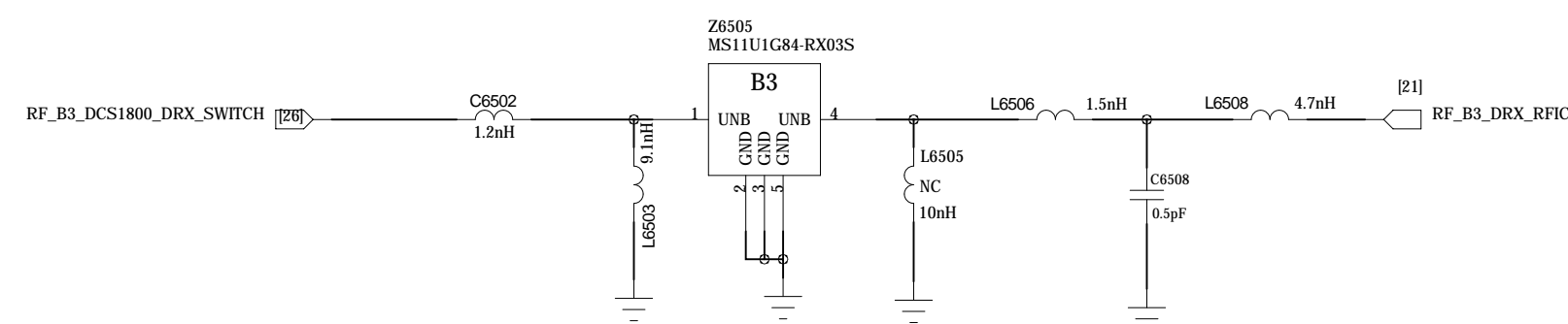
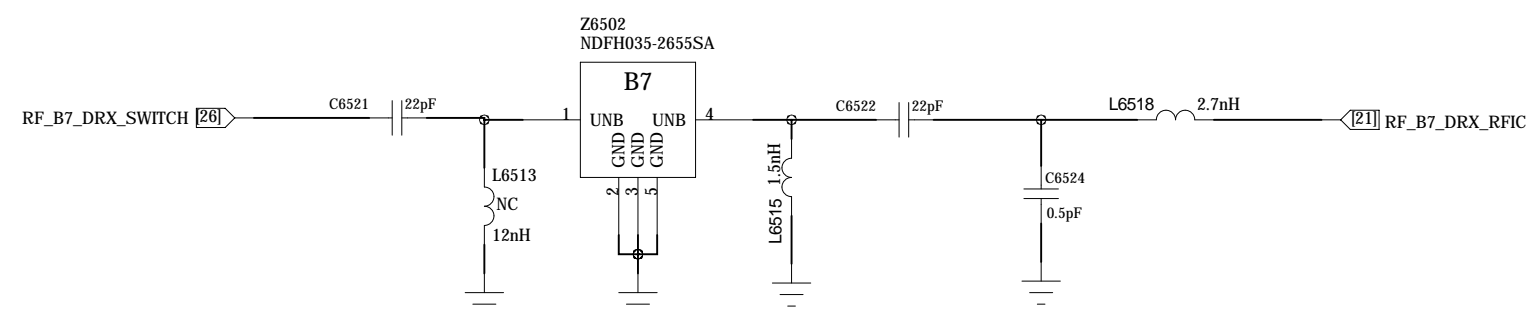
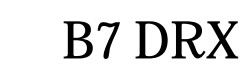
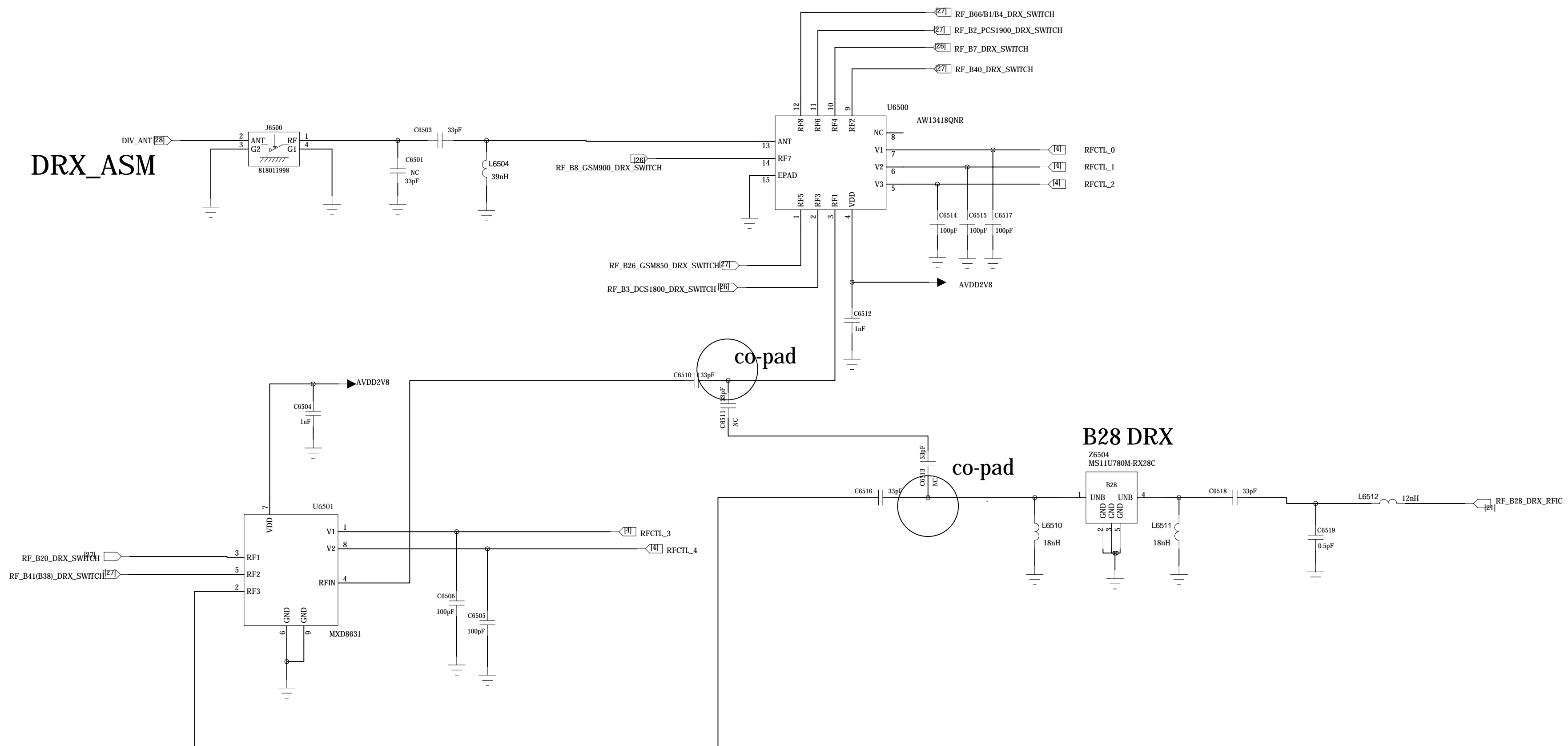
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: TINNO			
TITLE: UMS512			
CODE:	SIZE:	DRAWING NO:	REV:
V880BN	D	X675	V1.0
SCALE: <Scale>		SHEET: 25 31	

DRAWN: huazhong.xu	DATED: 2021/11/25
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

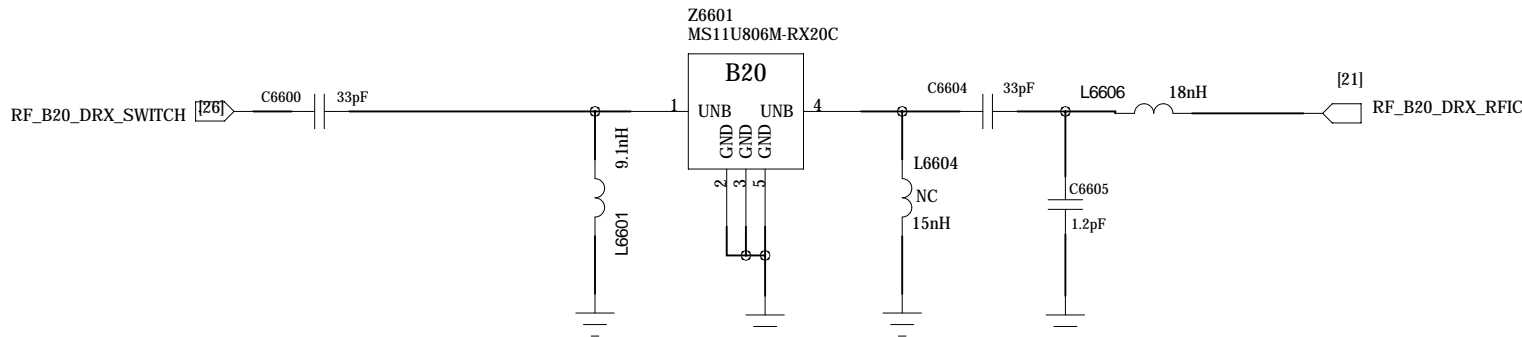


COMPANY:				TINNO			
TITLE:							
-----UMS512-----							
CODE:		SIZE:		DRAWING NO:			REV:
V880BN		D		X675			V1.0
SCALE: <Scale>				SHEET: 26 31			

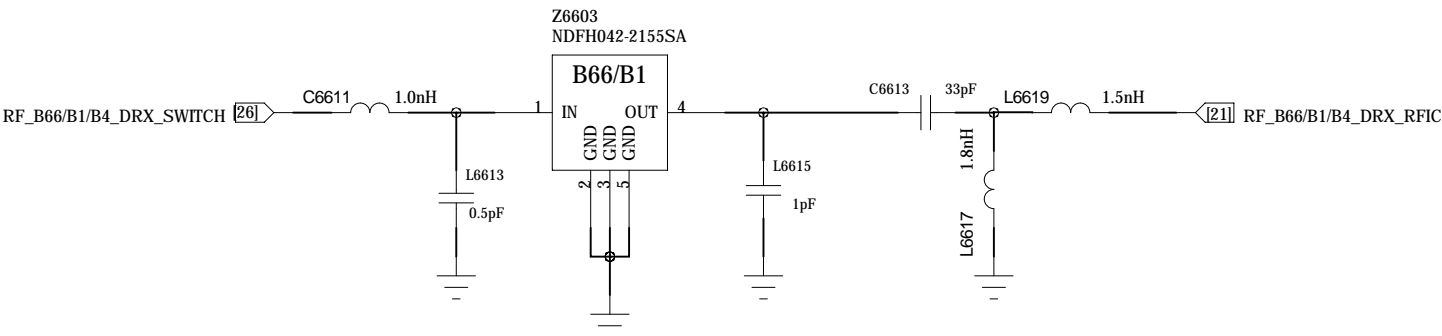
DRAWN: huazhongxu	DATED: 2021/11/25
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

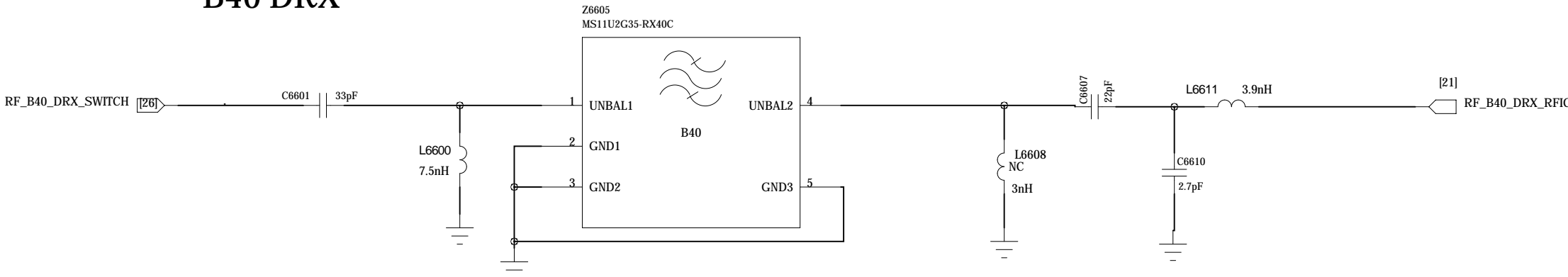
B20 DRX



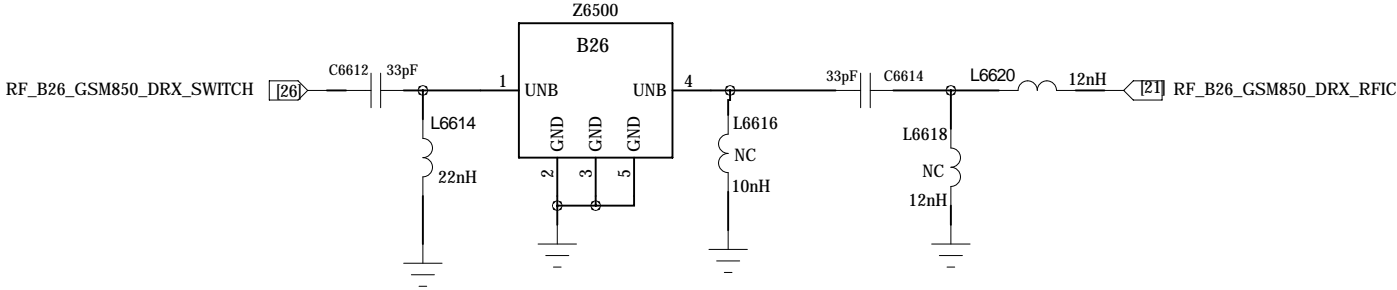
B66(B1/B4) DRX



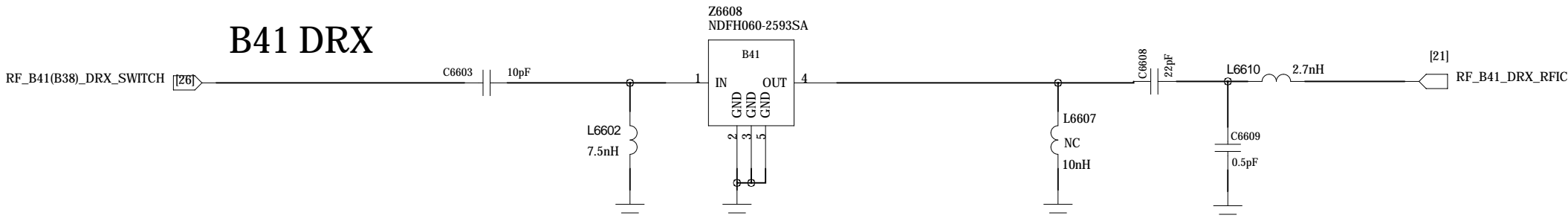
B40 DRX



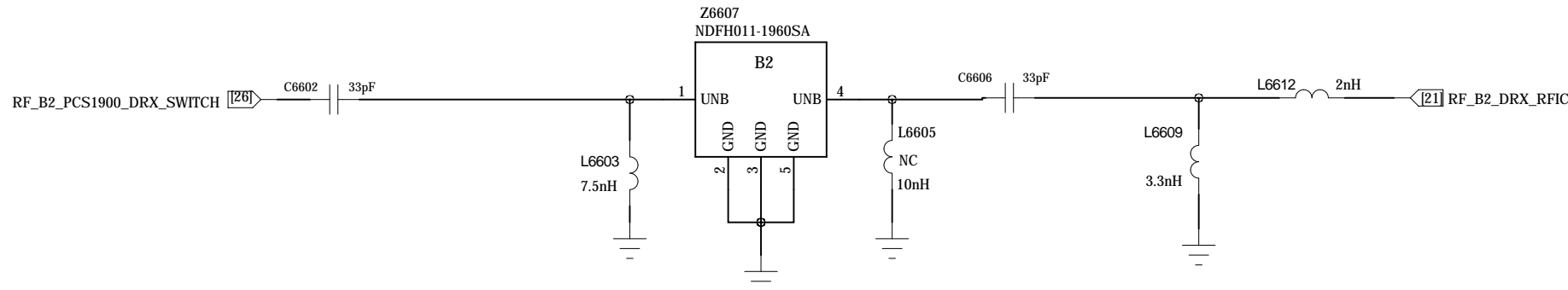
B26(B5/B6/B18/B19) DRX



B41 DRX



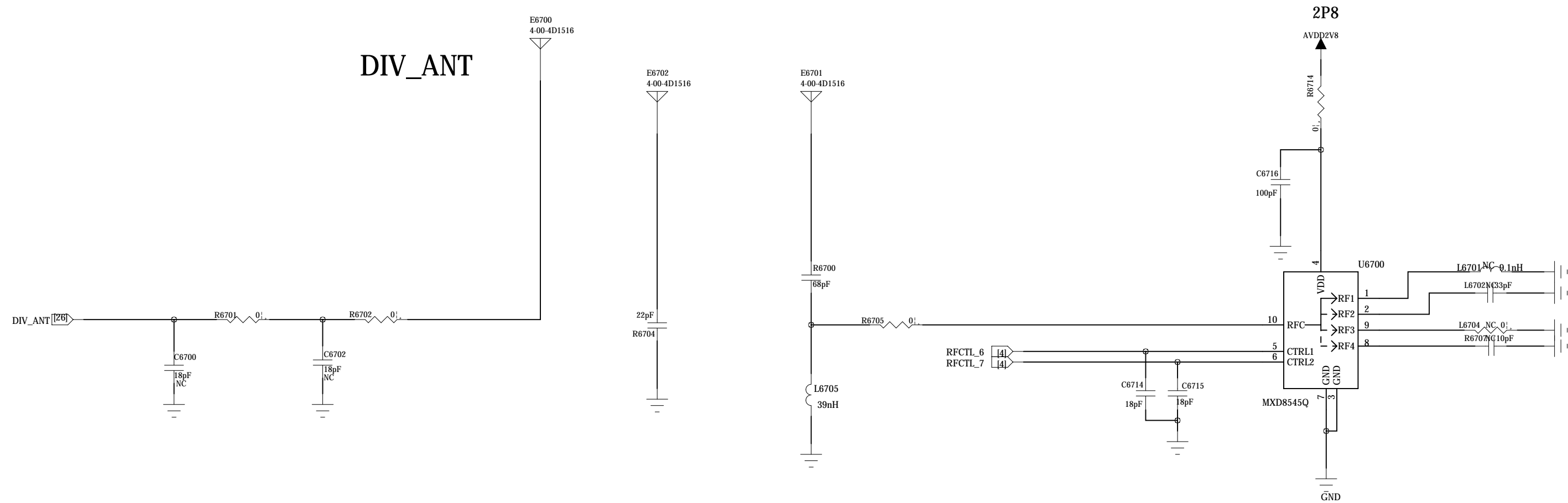
B2 DRX



COMPANY: TINNO			
TITLE: UMS512			
CODE: V880BN	SIZE: D	DRAWING NO: X675	REV: V1.0
SCALE: <Scale>		SHEET 07	31

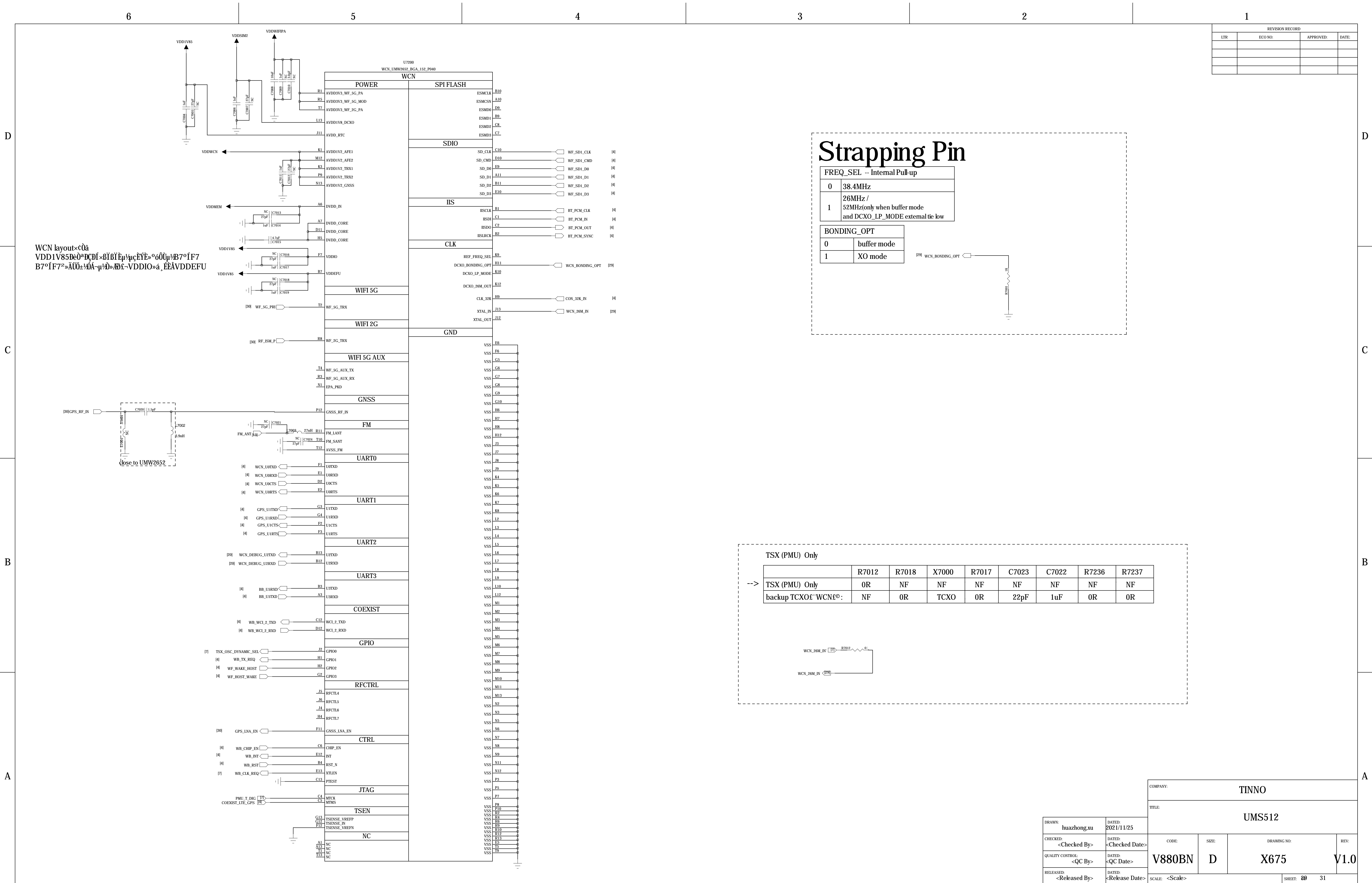
DRAWN: huazhong.xu	DATED: 2021/11/25
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DRAWN: huazhong.xu	DATED: 2021/11/25
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

COMPANY:		TINNO	
TITLE:			
UMS12			
CODE:	SIZE:	DRAWING NO:	REV:
V880BN	D	X675	V1.0
SCALE: <Scale>		SHEET: 08	31



6

5

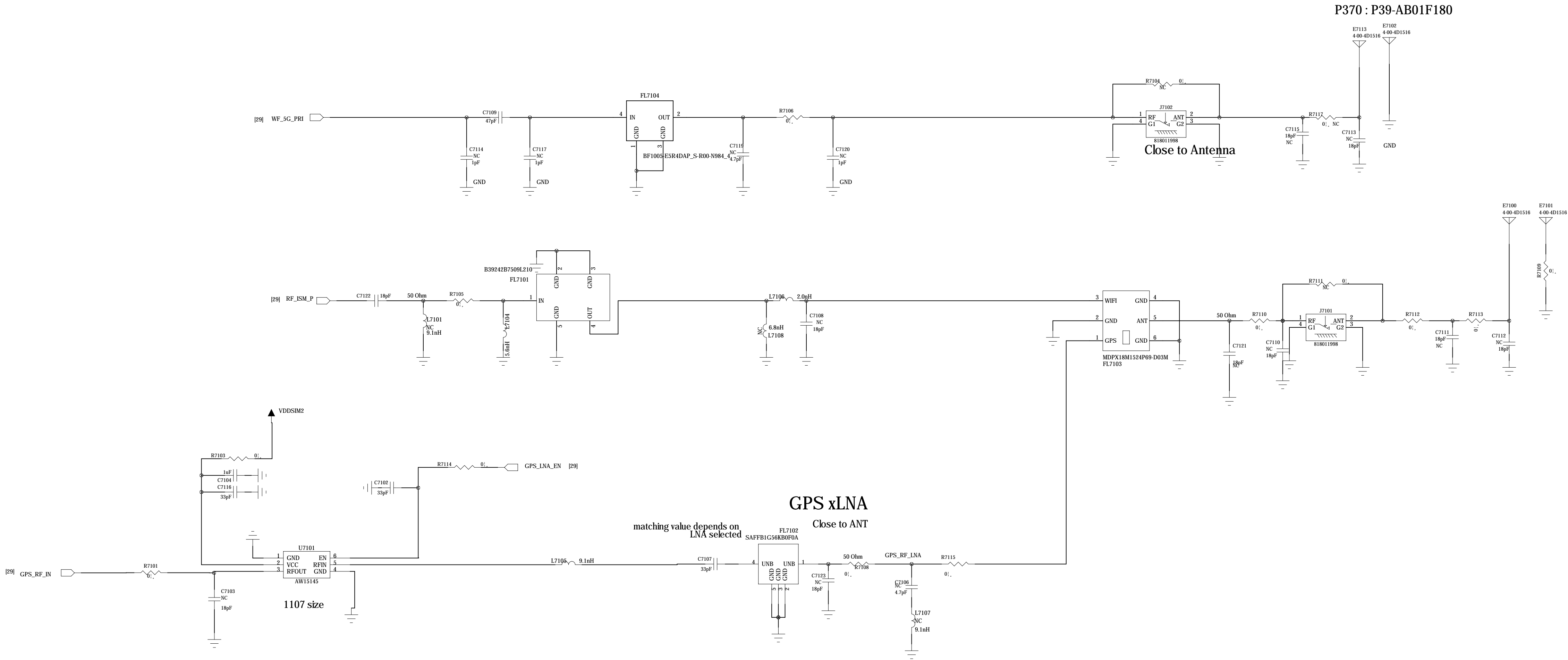
4

3

2

1

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:



DRAWN:	huazhong.xu	DATED:	2021/11/25
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>

COMPANY:		TINNO	
TITLE:		UMS12	
CODE:	SIZE:	DRAWING NO:	REV:
V880BN	D	X675	V1.0
SCALE: <Scale>		SHEET: 30	31

