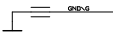


History



Date	Version	Owner	Description
2021/9/8	V1.0	Feng Lu	Original Release
2021/11/23	V1.1	Feng Lu	P5: ADD R1034, R1035, FL1000 CHANGE LCM CIRCUIT
2021/12/22	V1.1A	Feng Lu	P5: ADD QP10 ENL-HWFLASH P6: DELETE C802, L700, CHANGE R802 0402->0201 P6: CHANGE J1001 JRE->LCN, ADD R054 P10: ADD CR1014, DELETE CR1015/CR1016/CR1017 P8/15/16: CHANGE ANT SPRING GEOM
2022/01/14	V1.1B	Feng Lu	P7: ADD C714, PCB CHANGE SIM SHIELD&T CARD GEOM

DEFINITIONS

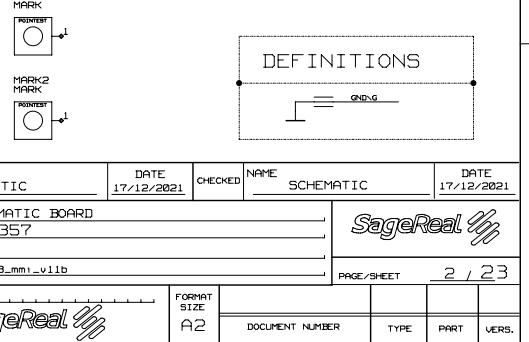
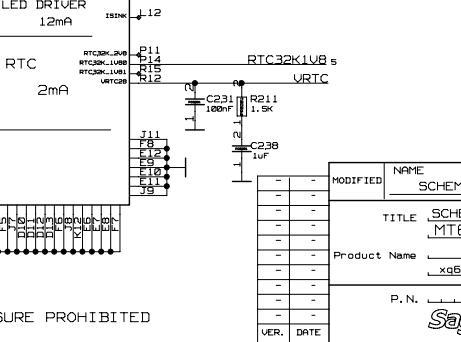
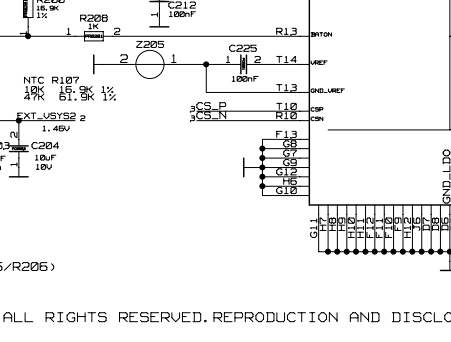
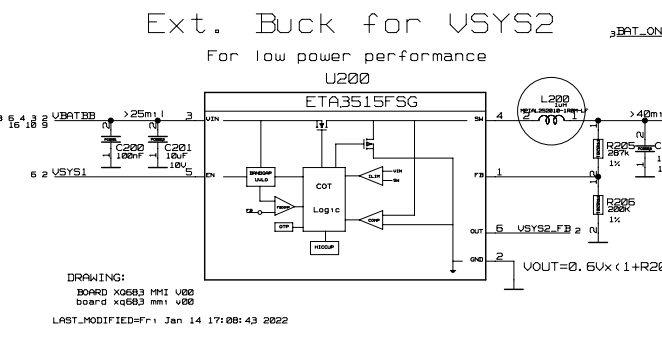
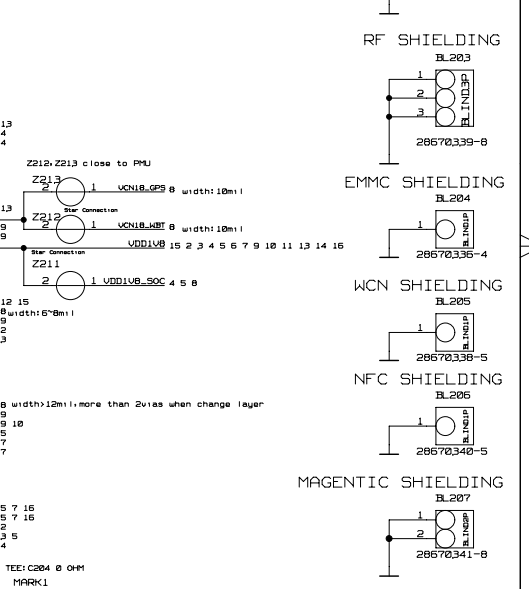
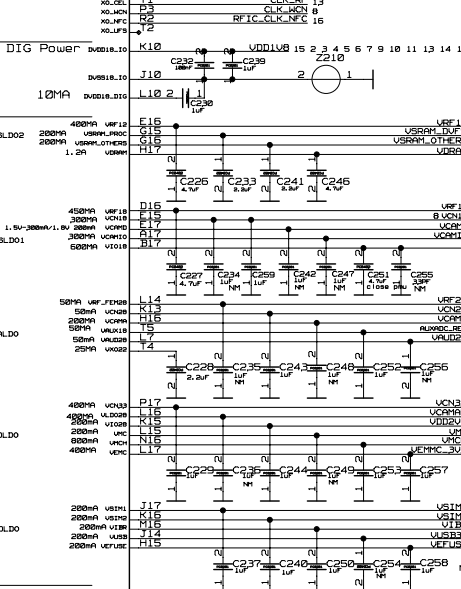
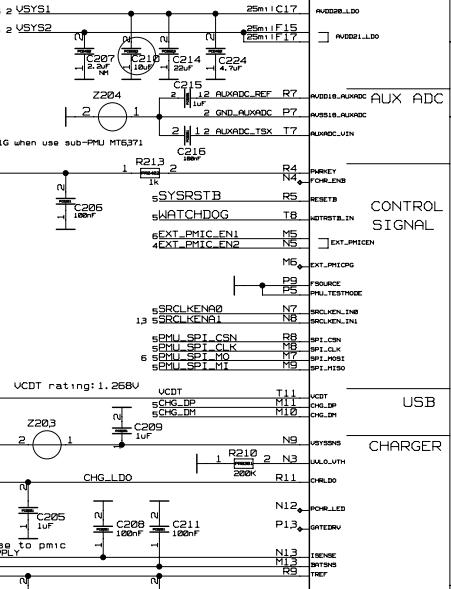
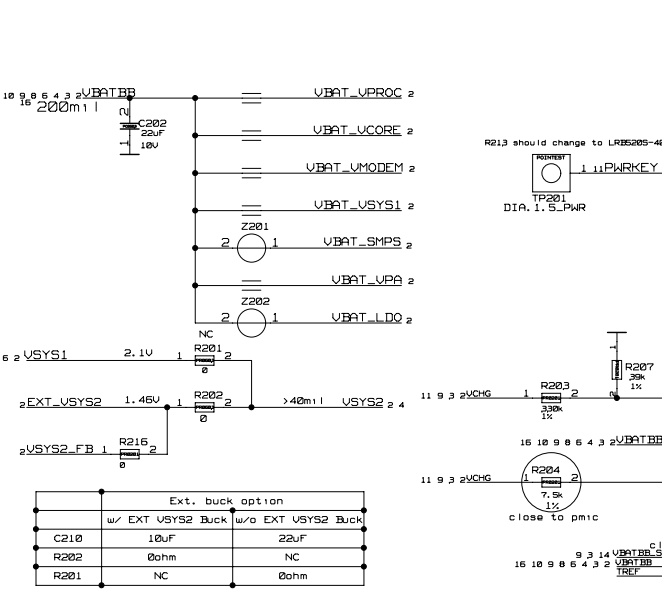
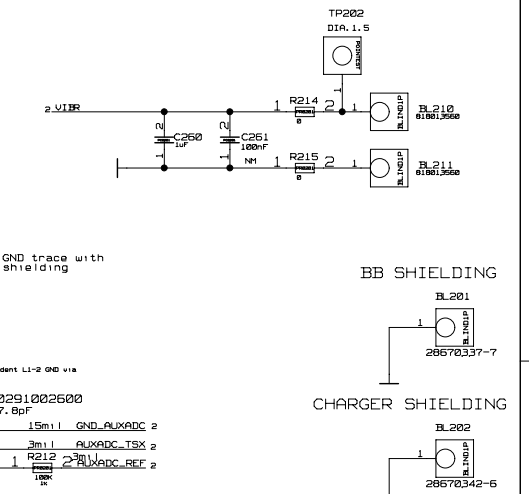
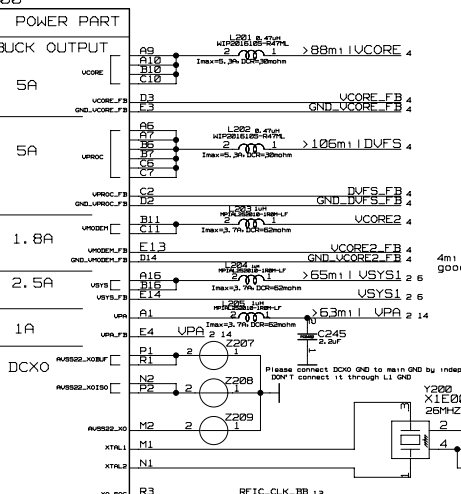
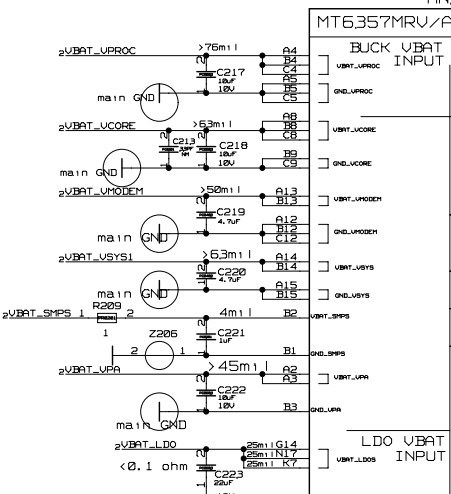


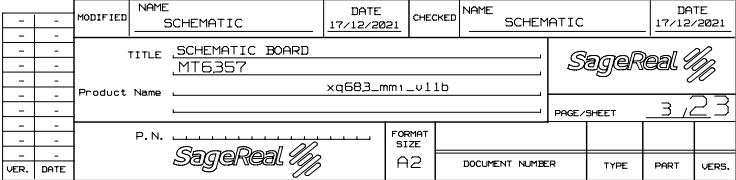
DRAWING:
BOARD XQ583 mm1 v1.1A
board xq583 mm1 v1.1a
LAST_MODIFIED=Fri Jan 14 17:09:34 2022

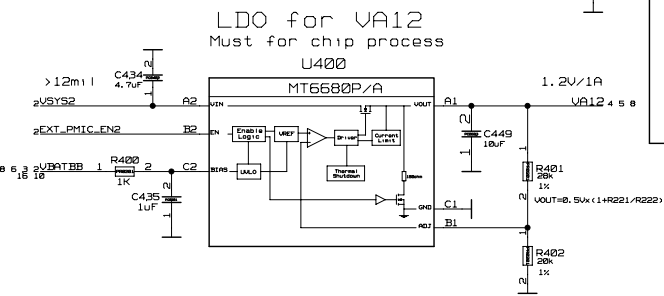
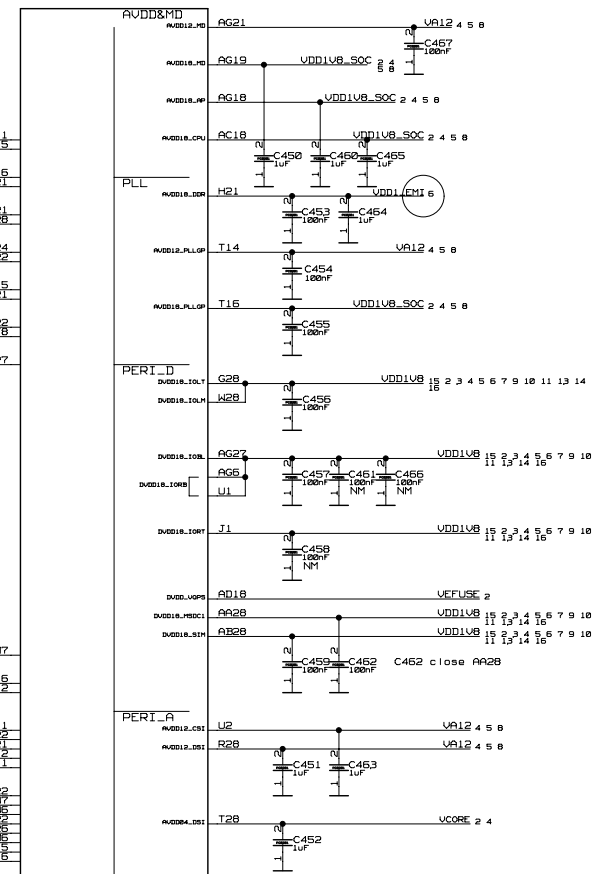
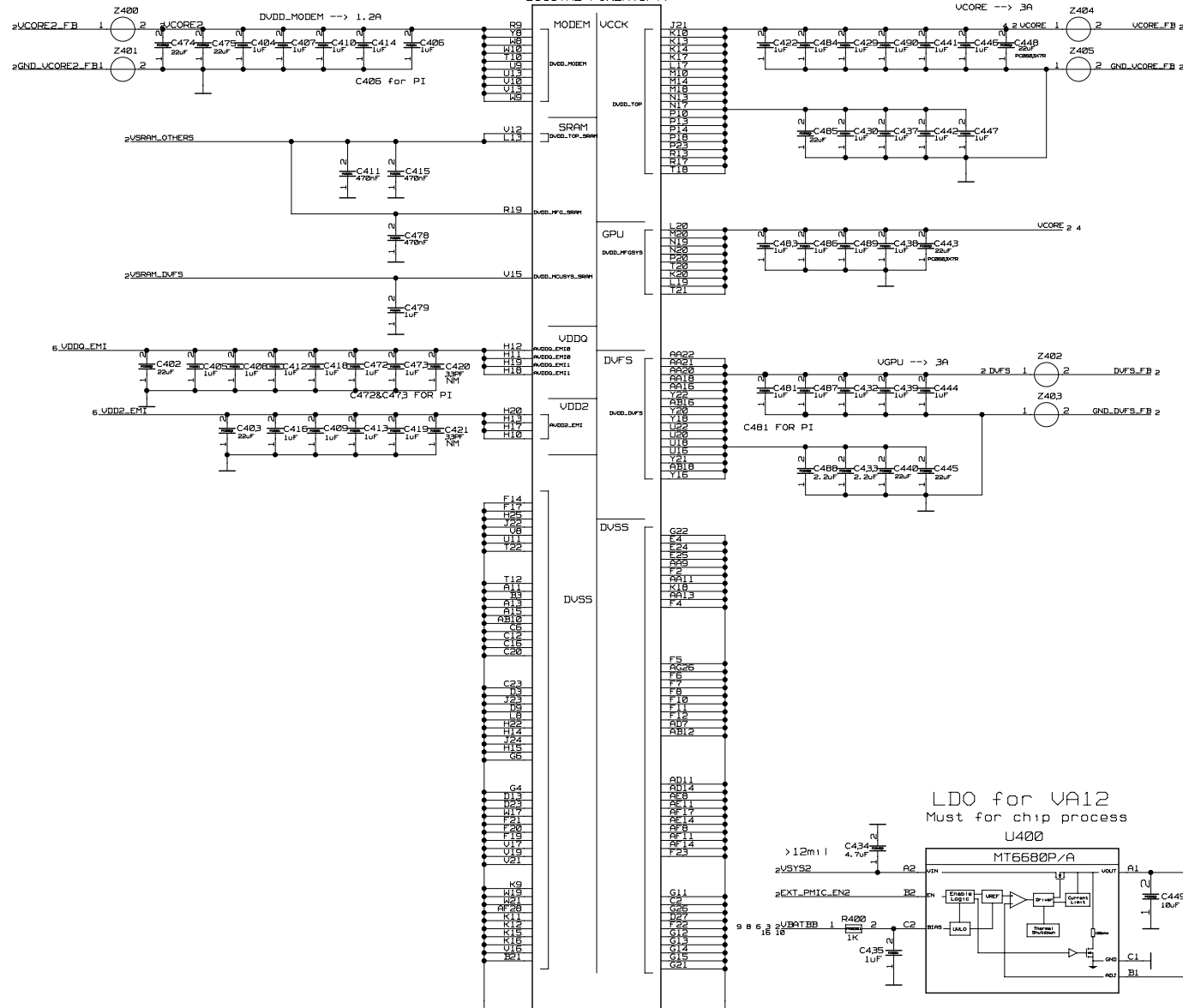
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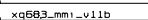
MODIFIED	NAME SCHEMATIC	DATE 17/12/2021	CHECKED	NAME SCHEMATIC	DATE 17/12/2021
TITLE SCHEMATIC BOARD HISTORY					
Product Name xq583_mm1_v11b		PAGE/SHEET 1 / 16			
P. N. 		FORMAT SIZE A2			
VER.	DATE	DOCUMENT NUMBER		TYPE	PART VER.

	Symbol	Vout(V)	I _{MAX} (mA)	Description
Buck	UCORE	0.51875~1.2	5000	SoC, GPU
	UMODEM	0.55~1.2	1000	MODEM
	UPROC	0.51875~1.31	5000	AP Core
	VS1	1.85~2.2	2500	LDO INPUT
	VPA	0.5~3.4	1000	RF
	Symbol	Vout(V)	I _{MAX} (mA)	Description
LDO	VF28	2.8	50	RF FEH
	UX022	2.24	25	DCXO
	URF18	1.81	450	MT6175
	URF12	1.2	400	MT6175
	VFUSE	1.2/1.3/1.5/1.8/2.0/2.8/3.0/3.3	50	FUSE
	VCN33	3.3/3.3/4.3/5.3/5	50	CONNECTIVITY
	VCN28	2.8	300	CONNECTIVITY
	VCN18	1.8	300	CONNECTIVITY
	VCAPA	1.8/2.5/2.7/2.8/2.9/2.95/3.0	300	CAMERA ANALOG POWER
	VCAM10	1.0/1.05/1.1/1.2/1.3/1.5/1.8/2.0/2.1	300	CAMERA 10
	VCAMD	1.0/1.05/1.1/1.2/1.3/1.5/1.8/2.0/2.1	300	CAMERA CORE POWER
	VAUD28	2.8	50	AUDIO
	VAUX18	1.8	25	AUXADC
	VSRAH_PROC	0.75~1.31	200	CPU SRAM
	VSRAH_OTHERS	0.75~1.31	200	OTHERS SRAM
VRTC	VSYS1	1.2/1.3/1.5/1.8/2.0/2.8/3.0/3.3	200	ISIM
	VSYS2	1.7/1.8/1.85/2.76/3.0/3.1	200	ISIM
	VLD028	2.8/3.0	350	1: CAMERA AF POWER
	VI028	2.8	200	2: TOUCH PANEL POWER
	VHC	1.85/2.9/3.0/3.3	200	3: BU IO, SENSOR
	UNCH	2.9/3.0/3.3	200	MEMORY CARD
	VEHC33	2.9/3.0/3.3	400	EMMC CARD
	VSYS1	1.8	200	USB
	VI018	1.8	600	1: BU IO, EMMC
	DVDD18_DIG	1.8	10	PMIC INTERNAL POWER
VRTC	VRTC28	2.8	2	RTC
VIBR	VIBR	1.2/1.3/1.5/1.8/2.0/2.8/3.0/3.3	200	VIBRATOR







---	---	MODIFIED	NAME SCHEMATIC	DATE 17/12/2021	CHECKED	NAME SCHEMATIC	DATE 17/12/2021
---	---	TITLE					SageReal 
---	---	MT6761D					
---	---	Product Name					PAGE/SHEET 4 / 23
---	---	xq683_mm1_v11b					
---	---	P. N.			FORMAT SIZE		
---	---	SageReal 			A2		
VER.	DATE				DOCUMENT NUMBER	TYPE	PART
							VERS.

DRAWING:
BOARD XQ683 MMI V00
board xq683 mm1 v00
LAST_MODIFIED=Fri Jan 14 17:08:44 2022

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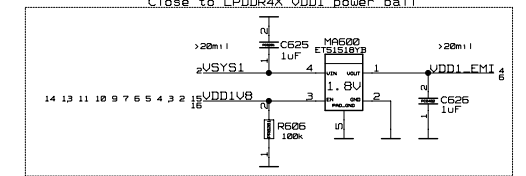
SCH DRAWING A2

MT6610/WEA

Binwin: BMD6X32H2A-16Gb
Foresee: NCLDXC1MC512M32

LPDDR4/LPDDR4X VDD1 1.8V LDO
VDD1_EMI=1.8V

Close to LPDDR4X VDD1 power ball



Z604, Warning FOR Layout

Reserved for LP4 DRAM, VDDQ=1.1V

6 4 VDD2_EMI 1 2 VDDQ_EMI 4 6

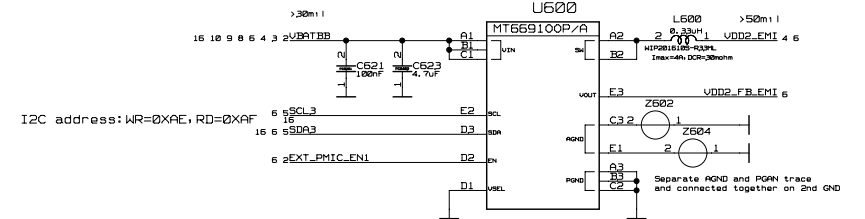
DRAM type	LPDDR4	LPDDR4X
Z605	Paste	NO Paste
U600	Mount	Mount
U601/C622	NC	Mount
R607	Mount	NC

PMU_SPI_MI	PMU_SPI_M0	Booting interface
default=PU	default=PD	DDR MSDC0 pin mux
L0	L0	LPDDR3 follow LPDDR3 Ref SCH.
L0	HI	N/A
HI	L0	LPDDR4X follow LPDDR4X Ref SCH.
HI	HI	LPDDR4 follow LPDDR4 Ref SCH.

Ext. Buck LP4/LP4X VDRAM

VDD2_EMI=1.125V

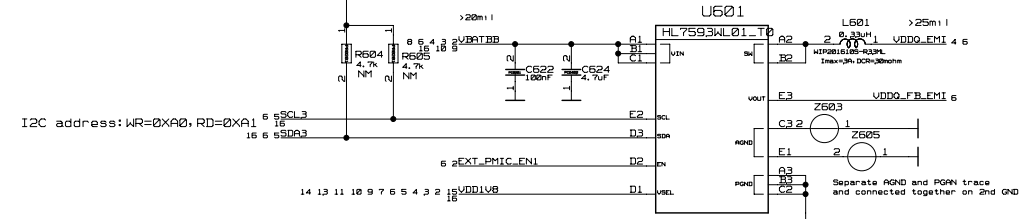
U600



Ext. Buck LP4X VDDQ

VDDQ_EMI=0.6V

U601

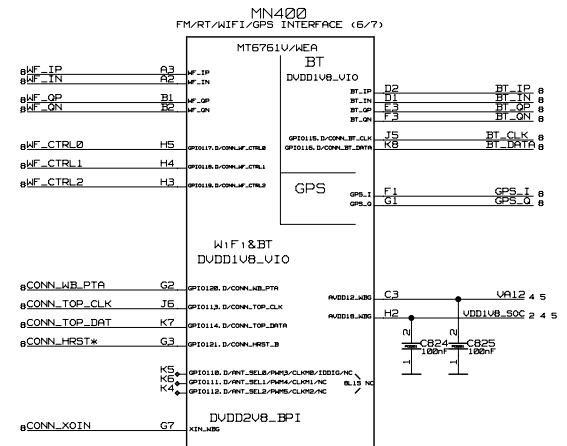
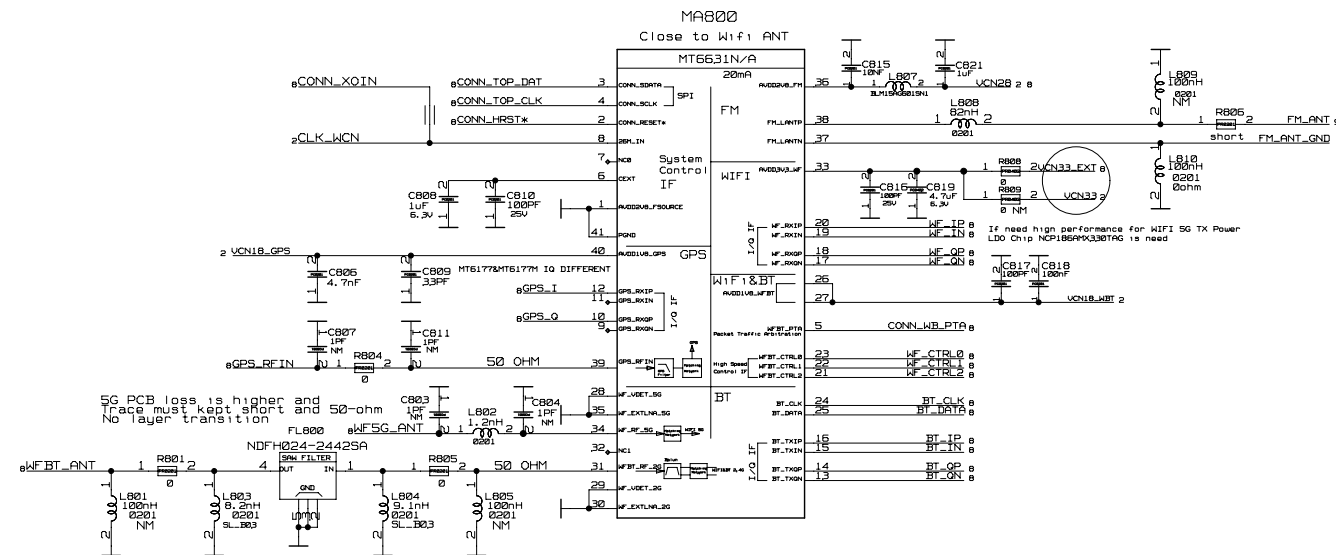
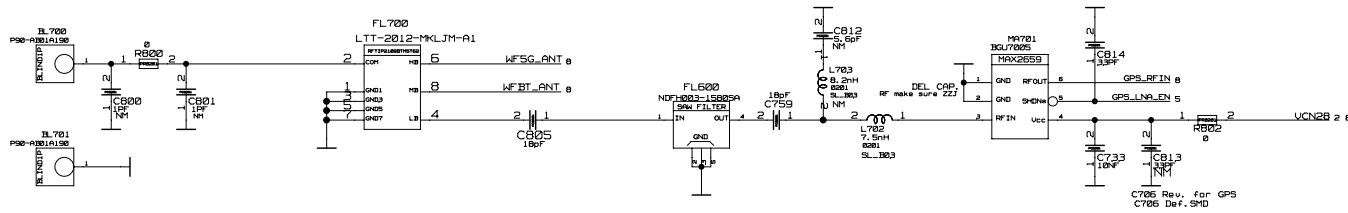


DRAWING:
BOARD X06B3 MM1 U60
board xq6B3 mm1 v60
LAST_MODIFIED=Fri Jan 14 17:08:45 2022

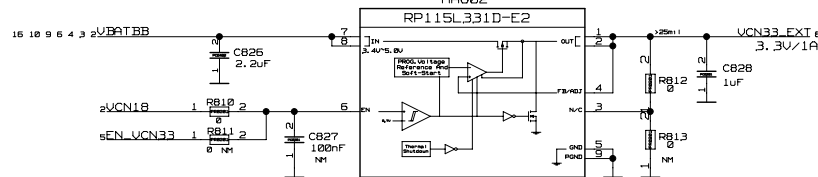
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MODIFIED	NAME	DATE	CHECKED	NAME	DATE
-	SCHEMATIC	17/12/2021	-	SCHEMATIC	17/12/2021
-	TITLE	SCHEMATIC BOARD	-	-	-
-	Product Name	MEMORY LPDDR4/4X	-	-	-
-	-	xq6B3-mm1-v11b	-	-	-
-	P. N.	-	-	-	-
-	FORMAT	A2	-	-	-
-	DOCUMENT NUMBER	-	-	-	-
-	TYPE	-	-	-	-
-	PART	-	-	-	-
-	VER.	-	-	-	-
-	DATE	-	-	-	-

2.4G WIFI/BT/GPS ANT



LDO 3.3V
Must for WiFi 5G application
MA802



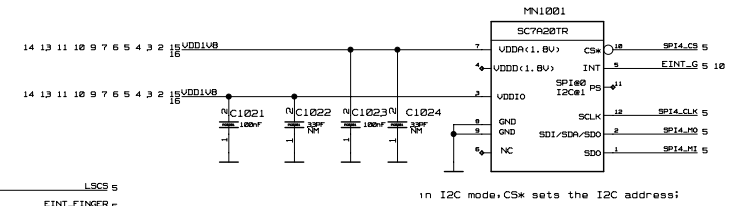
Output current capability: 1A at least

DRAWING:
BOARD X06B3 P01 V00
board x06B3 mm1 v00
LAST_MODIFIED=Fri Jan 14 17:00:46 2022

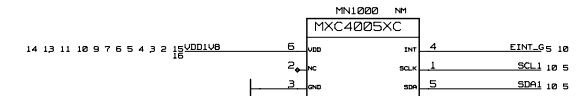
ALL RIGHTS RESERVED. REPRODUCTION AND DISCLOSURE PROHIBITED

MODIFIED	NAME	DATE	CHECKED	NAME	DATE
-	SCHEMATIC	17/12/2021	-	SCHEMATIC	17/12/2021
-	SCHEMATIC BOARD		-		
-	MT6631 (WIFI/BT/GPS/FM)		-		
-	Product Name	xq683_mm1_v11b	-		
-	P. N.		-		
-	FORMAT	A2	-		
-	DOCUMENT NUMBER		-		
-	TYPE		-		
-	PART		-		
-	VER.		-		
-	DATE		-		

3D SENSOR

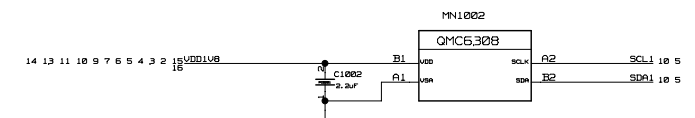


Fake 3D SENSOR
(Glass)





	ILED Default	ILED MAX	ULED	BOM
LTR-569HL5-02	100mA	185mA	2, 6U-4, 6U	R1003=H#, C1004, R1001, C1007, MA1001=mount
MN789120DN	15mA	200mA	2, 6U-4, 5U	C1004, R1001, C1007, MA1001=H#, R1003=mount
STK30562	100mA	200mA	2, 6U-3, 6U	R1003=H#, C1004, R1001, C1007, MA1001=mount

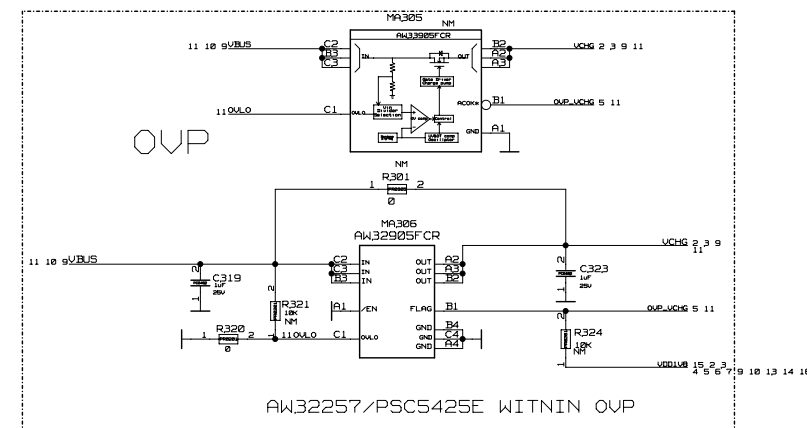
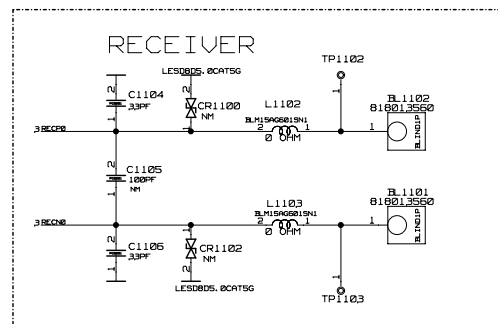
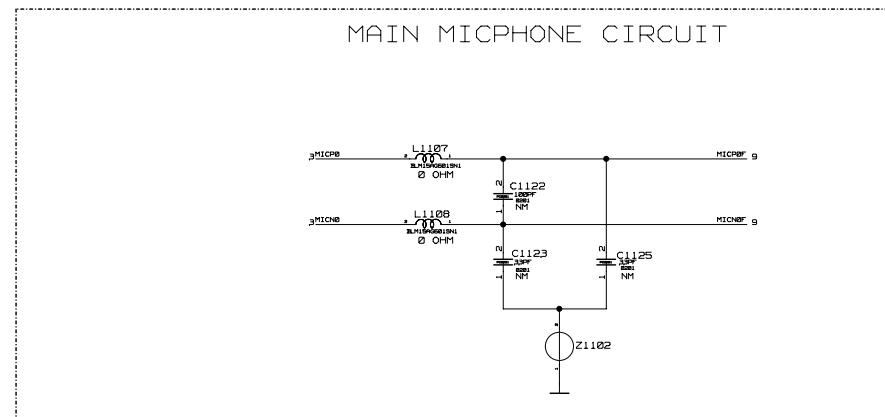
Magnetic





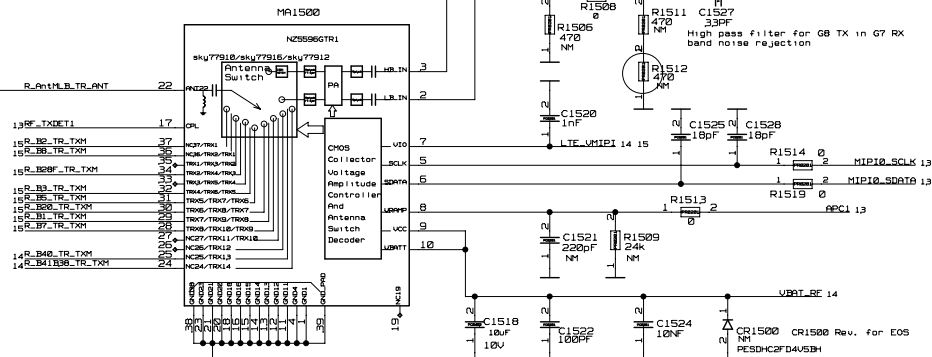
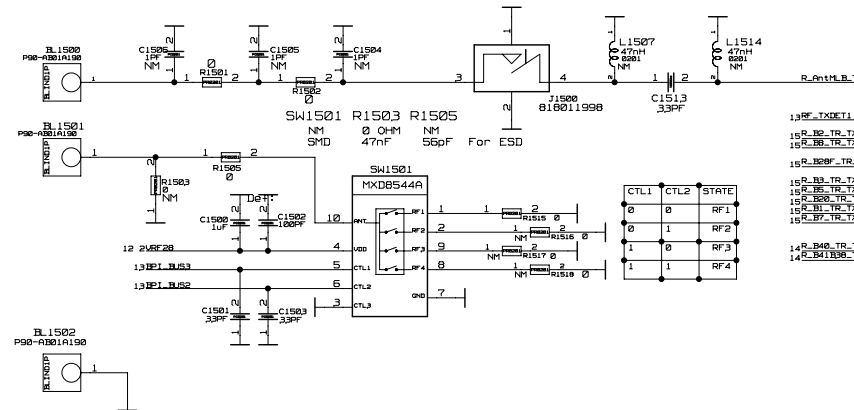
	I2C	IC	WR ADDRESS	RD ADDRESS
	I2C0	CTP		
	I2C1	MT78912+1VC4805		
		QMCE300		
	I2C2	Rear CAM		
	I2C3	NFC		
		MT669100P/A		
		MT66912XP/A		
	I2C4	Front CAM		
	I2C5	BS5682D		
		OC91375		
		OC92131B		
	I2C6	Rear Sub CAM		

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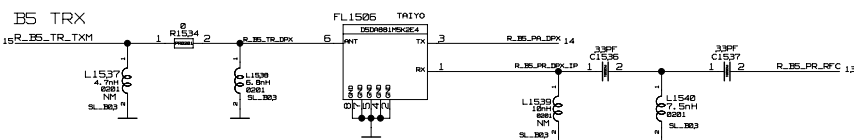
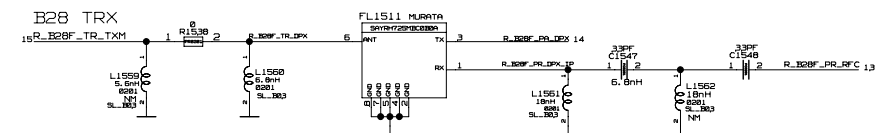
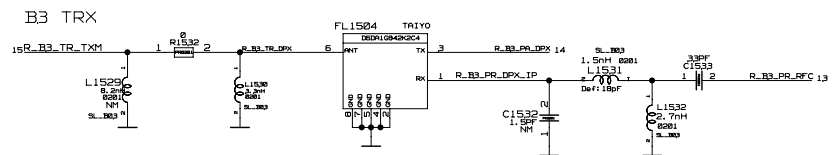
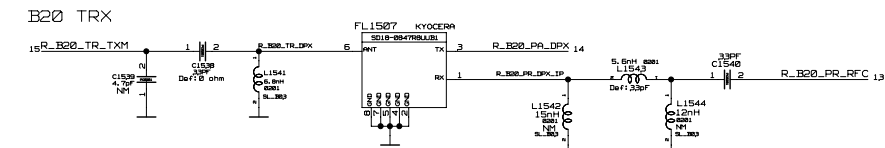
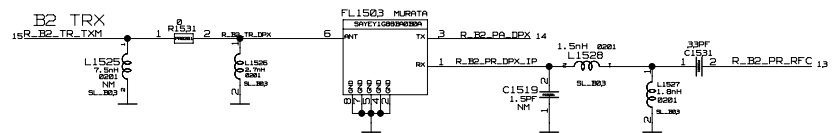
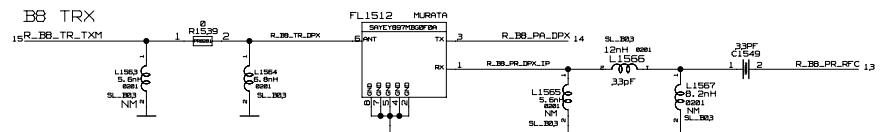
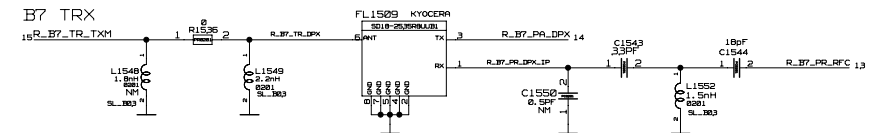
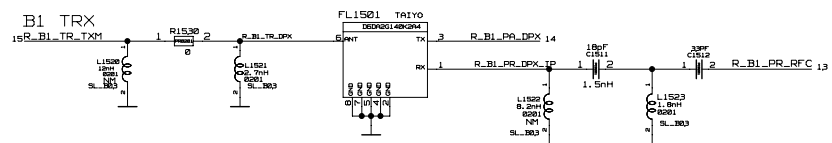
MODIFIED		NAME SCHEMATIC	DATE 17/12/2021	CHECKED	NAME SCHEMATIC	DATE 17/12/2021								
TITLE SCHEMATIC BOARD														
SENSOR&KEY														
Product Name		xq683-mm1-v11b												
P.N.		PAGE/SHEET 10/23												
		FORMAT SIZE A2	<table><tr><td></td><td></td><td></td><td></td></tr><tr><td>DOCUMENT NUMBER</td><td>TYPE</td><td>PART</td><td>VERS.</td></tr></table>								DOCUMENT NUMBER	TYPE	PART	VERS.
DOCUMENT NUMBER	TYPE	PART	VERS.											
VER.	DATE													



-	-	MODIFIED	NAME	SCHEMATIC		DATE	17/12/2021		CHECKED	NAME	SCHEMATIC		DATE	17/12/2021	
-	-		TITLE	SCHEMATIC BOARD						SageReal 					
-	-		AUDIO&OVF												
-	-		Product Name	xq683_mmi_v11b						PAGE/SHEET 11/23					
-	-														
-	-		P. N.							FORMAT SIZE A2					
-	-		SageReal 						DOCUMENT NUMBER				TYPE	PART	VERS
VER.	DATE														





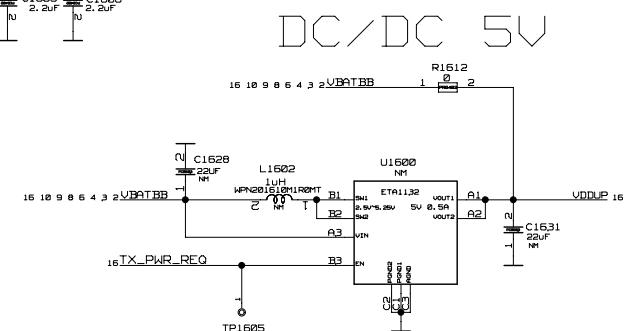
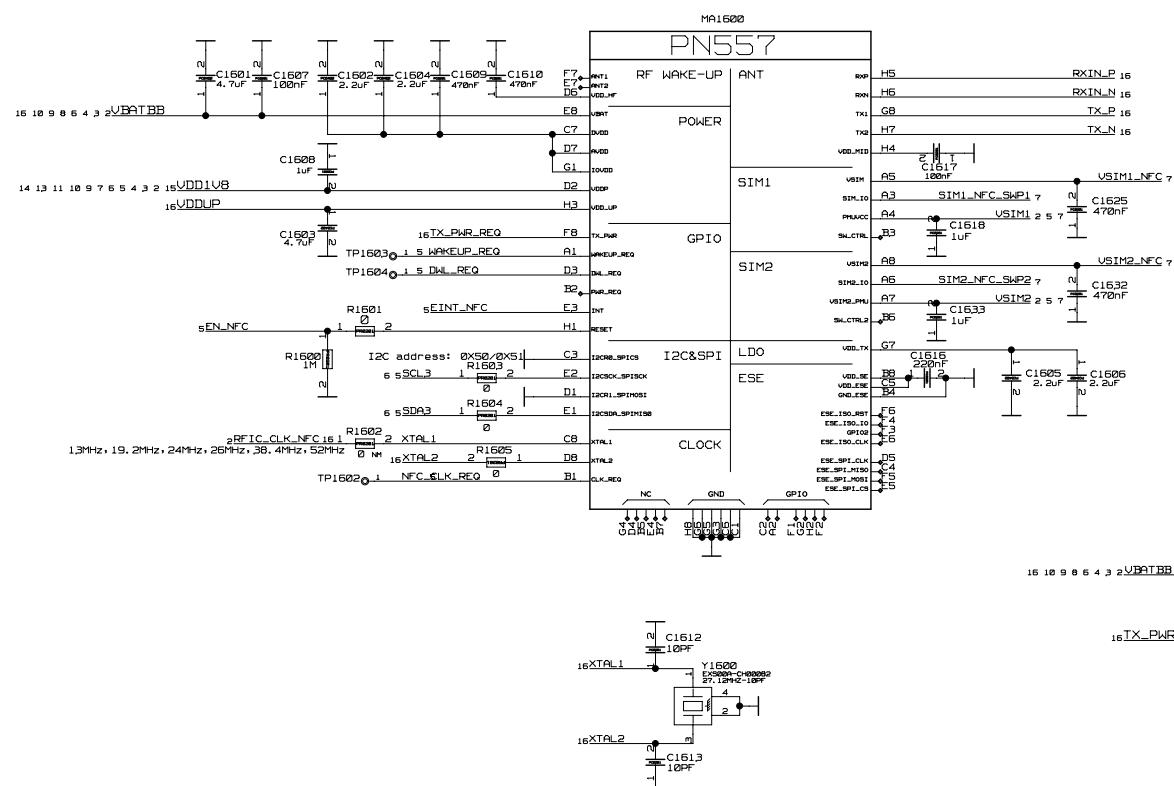
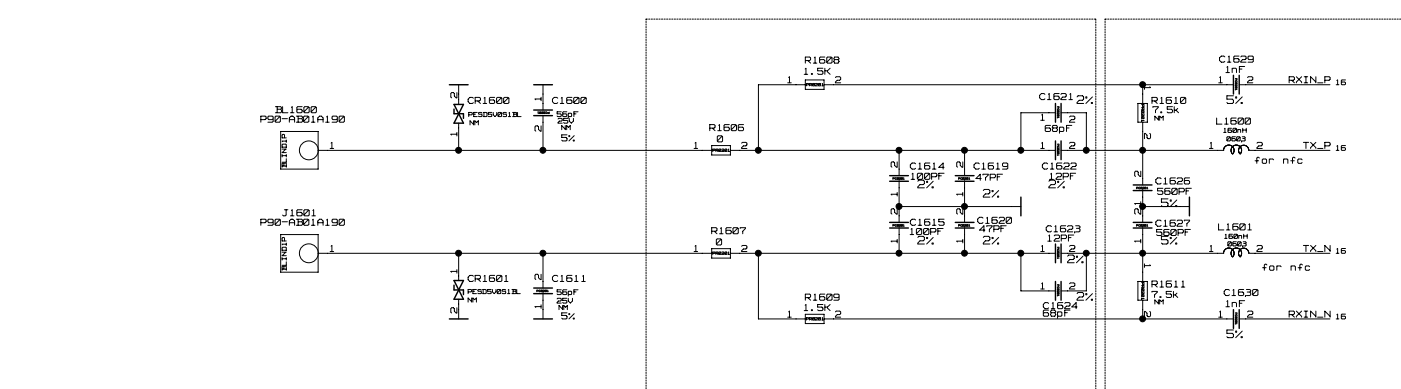
Register#	address	bits(mode control)
State	D7---D0	
standby	0x00	
LB GHSK Tx	0x14	
HB GHSK Tx	0x1C	
LB EDGE Tx	0x15	
HB EDGE Tx	0x1D	
TDS/TDD LTE Tx	0x1D	
TrRx1	0x05	
TrRx2	0x04	
TrRx3	0x01	
TrRx4	0x09	
TrRx5	0x11	
TrRx6	0x19	
TrRx7	0x08	
TrRx8	0x10	
isolation	0x18	
isolation	0x0C	
isolation	0x0D	





```
DRAWING:
BOARD XQ583 MMI V00
board xq583 mm1 v00
LAST_MODIFIED=Fri Jan 14 17:08:43 2022
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MODIFIED		NAME SCHEMATIC		DATE 17/12/2021		CHECKED		NAME SCHEMATIC		DATE 17/12/2021	
		TITLE SCHEMATIC BOARD TXM									
		Product Name xq883-mm1-v11b									
		P.N.						FORMAT SIZE A2			
											
								DOCUMENT NUMBER		TYPE	
VER. DATE											



-	-	MODIFIED	NAME SCHEMATIC	DATE 17/12/2021	CHECKED	NAME SCHEMATIC	DATE 17/12/2021
-	-	TITLE SCHEMATIC BOARD					SageReal 
-	-	NFC					
-	-	Product Name	xq683_mm_v11b				PAGE/SHEET 16 / 23
-	-						
-	-	P. N.				FORMAT SIZE A2	
VER.	DATE					DOCUMENT NUMBER	TYPE PART VERS.

Title: Basenet Report
Design: xq683_mmi_v11b
Date: Jan 14 17:09:29 2022

Base nets and synonyms for
xq683.lib, XQ683_MMI_V11B(xq683.lib, xq683_mmi_v11b(sch_1
>>)



Base Signal Location((Zone1Idir))

A0_A 6A3 6B4
A0_B 6A3 6A4
A1_A 6A3 6B4
A1_B 6A3 6A4
A2_A 6A3 6A4
A2_B 6A3 6A4
A3_A 6A3 6A4
A3_B 6A3 6A4
A4_A 6A3 6B4
A4_B 6A3 6A4
A5_A 6A3 6B4
A5_B 6A3 6A4
ACCDDET 3B4
APC1 13B4 15A1
AUDIO_PA_ID 13B3 13B3
AUDIO_PA_SDN 5B4 9B4
AUD_CLK_MISO 3A3 5C4
AUD_CLK_MOSI 3A3 5C4
AUD_DAT_MISO0 3A3 5D4
AUD_DAT_MISO1 3A3 5D4
AUD_DAT_MISO0 3B3 5D2 5D4
AUD_DAT_MOSI1 3B3 5D4
AUD_SYNC_MISO 3A3 5D4
AUD_SYNC_MOSI 3A3 5D4
AUXADC_REF 2B1 2B3 2C2
AUXADC_TSX 2B1 2B3
AUX_IN0_NTC 5A2 13D4
AUX_IN1_NTC 5A2 14D3
AUX_IN4_CHG 3D3 5A2 9B3
AUX_REFN 3B2 9B4
AVSS2B_AUD 3A3 3A4 3B4 9B4
B1_DRX_SAW_IN 12B4
B1_DRX_SAW_OUT 12B4
B1_DRX_SW 12A3 12B4
B1_DRX_TRA 12B3 13A3
B3_DRX_SAW_IN 12B4
B3_DRX_SAW_OUT 12B4
B3_DRX_SW 12A3 12B4
B3_DRX_TRA 12B3 13A3
B7_DRX_SAW_IN 12C4
B7_DRX_SAW_OUT 12C4
B7_DRX_SW 12A3 12C4
B7_DRX_TRA 12C3 13B3
B8_DRX_SAW_IN 12C4
B8_DRX_SAW_OUT 12C4
B8_DRX_SW 12A3 12C4
B8_DRX_TRA 12C3 13B3
B20_DRX_SAW_OUT 12D4
B20_DRX_SW 12A3 12D4
B20_DRX_TRA 12D3 13A3
B26F_DRX_SAW_IN 12C2
B26F_DRX_SAW_OUT 12C2
B26F_DRX_SW 12A3 12C2
B26F_DRX_TRA 12C1 13B3
B40_DRX_SAW_IN 12B2
B40_DRX_SAW_OUT 12B2
B40_DRX_SW 12A3 12B2
B40_DRX_TRA 12B1 13A3
B41B3B_DRX_SAW_IN 12B2
B41B3B_DRX_SAW_OUT 12B2
B41B3B_DRX_SW 12A3 12B2
B41B3B_DRX_TRA 12B1 13A3
BAT_ID 3C4 5A2
BAT_ON 2D3 3C3
BPI_BUS2 13A3 15B4
BPI_BUS3 13A3 15B4
BPI_BUS6 9B4 13A3
BPI_BUS7 9B4 13A3
BPI_BUS8 12A3 13A3
BPI_BUS9 12B3 13A3
BPI_BUS10 12B3 13A3
BT_CLK 8B1 8C3
BT_DATA 8B1 8C3
BT_IN 8B1 8C3
BT_IP 8B1 8C3
BT_ON 8B1 8C3

BT_OP 8B1 8C3
CAMCLKB 5A1 9B1
CAMCLKF 5B1 9C2
CAMCLK_SUB 5C1 9D1
CAMPDNB 5A1 9A1
CAMPDNB_SUB 5A4 9D1
CAMPDNF 5B1 9B2
CAMRSTB 5A1 9A1
CAMRSTF 5B1 9C2
CAM_IDB 5A4 9A1
CAM_IDF 5C4 9B2
CHG_DM 2C3 5B3
CHG_DP 2C3 5B3
CHG_LDO 2C3
CHG_LED 9B3 9C2
CKE0_A 6B4 6C3
CKE0_B 6A4 6C3
CKE1_A 6B4 6C3
CKE1_B 6A4 6C3
CLKC_A 6A4 6C3
CLKC_B 6B4 6C3
CLKT_A 6A4 6C3
CLKT_B 6B4 6C3
CLK_RF 2B2 13C1
CLK_WCN 2B2 8B4
CONN_HRST* 8B4 8C2
CONN_TOP_CLK 8B4 8C2
CONN_TOP_DAT 8B4 8C2
CONN_WB_PTA 8C2 8C3
CS0_A 6A4 6B3
CS0_B 6B4 6C3
CS1_A 6A4 6B3
CS1_B 6B4 6C3
CS_N 2D3 3C4
CS_P 2D3 3D4
CTOUCH_SHDN 5A4 9B2
D0_A 6A3 6A4
D0_B 6A4 6B3
D1_A 6A3 6A4
D1_B 6A4 6B3
D2_A 6A3 6A4
D2_B 6A4 6B3
D3_A 6A3 6A4
D3_B 6A4 6B3
D4_A 6A3 6A4
D4_B 6B3 6B4
D5_A 6A3 6A4
D5_B 6A4 6B3
D6_A 6A3 6A4
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D12_A 6A4 6B3
D12_B 6A4 6B3
D13_A 6A4 6B3
D13_B 6A4 6B3
D14_A 6A4 6B3
D14_B 6A4 6B3
D15_A 6A4 6B3
D15_B 6A4 6B3
DQ0_A 6B4 6C3
DQ0_B 6B4 6C3
DQ01_A 6B4 6C3
DQ01_B 6B4 6D3
DQ50C_A 6B4 6C3
DQ50C_B 6B4 6C3
DQ50T_A 6B4 6C3
DQ50T_B 6B4 6C3
DQ51C_A 6B4 6C3
DQ51C_B 6B4 6C3
DQ51T_A 6B4 6C3
DQ51T_B 6B4 6C3
DVFS 2A2 4B2
DVFS_FB 2A2 4B2
DWL_REQ 5A4 16B3
EINT_CTP 5A4 9B2

EINT_FINGER 5A4 10A2
EINT_G 5A4 10A1 10B1
EINT_HR_HEADSET 3B4 9B4
EINT_NFC 5A4 16C3
EINT_PS 5A4 10A4
EINT_RAMDUMP 5A4
EINT_SIM1 5A3 7B3
EINT_SIM2 5A3 7B3
EINT_SWCHG 3A2 5A4
EINT_TCARD 5A4 7B3 7C3
EMI_RESET* 6B4 6B4
EMMC_CLK 5B3 7A4
EMMC_CMD 5B3 7A4
EMMC_DAT0 5C3 7A4
EMMC_DAT1 5C3 7A4
EMMC_DAT2 5C3 7A4
EMMC_DAT3 5C3 7A4
EMMC_DATA4 5C3 7A4
EMMC_DATA5 5B3 7A4
EMMC_DATA6 5B3 7A4
EMMC_DATA7 5C3 7A4
EMMC_DSL 5B3 7B4
EMMC_RST 5B3 7A4
EN_CHG 3A2 5B4
EN_CHG_LED 5B4 9C3
EN_DM_0V6 3C1 5A4
EN_DP_0V6 3C2 5A4
EN_DP_3V3 3B1 5A4
EN_FLASH 5B4 9C2
EN_HWFLASH 5A4 9C3
EN_LCD_BL 5B2 9D4
EN_NFC 5A4 16C4
EN_VCN33 5B4 8D3
EXT_PMIC_EN1 2B3 6C2 6D2
EXT_PMIC_EN2 2B3 4C3
EXT_VSYS2 2C4 2D3
FM_ANT 8B2 9B3
FM_ANT_GND 8B2
GND_AUXADC 2B1 2B3
GND_DVFS_FB 2A2 4B2
GND_VCORE2_FB 2A2 4A4
GND_VCORE_FB 2A2 4A2
GPS_I 8B1 8B3
GPS_LNA_EN 5C3 8A2
GPS_Q 8B1 8C3
GPS_RF_IN 8A2 8C4
HSMIC 9B4 11A3
HSPKL 3B4 11A4
HSPKR 3B3 9B4
HSPKR 3B3 9B4
KCOL0 5C3 11C4
KCOL1 5C3 11D4
LCD_CABC 9B2 9D4
LCD_ID 5A2 9A3
LCD_RSTB 5B2 9A2
LEDN 9B4 9D3
LEDP 9A3 9D3
LPTE 5B2 9A2
LSClk 5C4 10A2
LSCS 5C4 10A2
LTE_DET_BB_IN 13A4 13C1
LTE_DET_BB_IP 13A4 13C1
LTE_DET_BB_ON 13B4 13C1
LTE_DET_BB_OP 13B4 13C1
LTE_DRX_BB_IN 13A4 13B1
LTE_DRX_BB_IP 13A1 13A4
LTE_DRX_BB_ON 13A4 13B1
LTE_DRX_BB_OP 13A4 13B1
LTE_PRX_BB_IN 13A4 13B1
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LTE_PRX_BB_ON 13A4 13B1
LTE_PRX_BB_OP 13A4 13B1
LTE_TX_BB_IN 13A4 13C1

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MODIFIED	NAME SCHEMATIC	DATE 17/12/2021	CHECKED	NAME SCHEMATIC	DATE 17/12/2021
TITLE SCHEMATIC BOARD					
Product Name xq683_mmi_v11b		PAGE/SHEET 17 / 23			
P. N.		FORMAT SIZE A2	DOCUMENT NUMBER TYPE PART VERS.		
					

LTE_TX_BB_IP 13A4 13C1
LTE_TX_BB_QN 13A4 13C1
LTE_TX_BB_QP 13A4 13C1
LTE_VMIPI 14B1 15A1 15A2
15A1 2B1 2B2 3A2 3A3 3C4 3D3
4B1 4B1 4B1 4B1 4B1 5B2 5D2
6A1 6B1 6C2 6D2 7A3 7C3 9A3
9B3 10A2 10A2 10A3 10A4 10A4
10B2 10C2 11D1 13B1 13B3 13B3
13B4 13B4 13D3 14C2 16B4
3B4
MICBIAS1 3A4 9B3
MICBIASP 3A4 11A2
MICON0 9B3 11A1
MICON0F 3B4 11A4
MICON0 3A4 11A2
MICON0F 9B3 11A1
MIPI0_SCLK 13B3 15A1
MIPI0_SDATA 13B3 15A1
MIPI1_SCLK 13B3 14B1
MIPI1_SDATA 13B3 14B1
MIPI_RCN 5A1 9B2
MIPI_RCNLA 5B1 9C1
MIPI_RCP 5A1 9B2
MIPI_RCP_A 5B1 9C1
MIPI_RDN0 5A1 9A2
MIPI_RDN0_A 5B1 9C1
MIPI_RDN1 5A1 9A2
MIPI_RDN1_A 5B1 9B1
MIPI_RDN2 5A1 9A2
MIPI_RDN2_A 5B1 9B1
MIPI_RDN3 5A1 9A2
MIPI_RDN3_A 5B1 9B1
MIPI_RDP0 5A1 9A2
MIPI_RDP0_A 5B1 9C1
MIPI_RDP1 5A1 9A2
MIPI_RDP1_A 5B1 9C1
MIPI_RDP2 5A1 9A2
MIPI_RDP2_A 5B1 9B1
MIPI_RDP3 5A1 9A2
MIPI_RDP3_A 5B1 9B1
MIPI_TCN 5A2 9A4
MIPI_TCP 5A2 9A4
MIPI_TDN0 5A2 9A4
MIPI_TDN1 5A2 9B4
MIPI_TDN3 5A2 9B4
MIPI_TDN3 5A2 9A4
MIPI_TDP0 5A2 9A4
MIPI_TDP1 5A2 9A4
MIPI_TDP2 5A2 9B4
MIPI_TDP3 5A2 9A4
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MN700P12 7B4
MN700P13 7B4

MN700P14 7B4
NFC_CLK_REQ 5B4 16C3
OVL0 11C2 11C2
OVP_VCHG 5C4 11C1 11C1
PCB_VER0 13B3 13B4
PCB_VER1 13B3 13B4
PCB_VER2 13B3 13B4
PMU_SPI_CLK 2C3 5B4
PMU_SPI_CSN 2C3 5B4 5D2
PMU_SPI_MI 2C3 5B4
PMU_SPI_MO 2C3 5B4 6B1
PWKEY 2B3 11D4
RECNO 3B3 11D3
RECP0 3B3 11D3
RESET_FINGER 5C4 10A3
RFIC0_BSI_CLK 13B1 13B3
RFIC0_BSI_D0 13B1 13B3
RFIC0_BSI_D1 13B1 13B3
RFIC0_BSI_EN 13B1 13B3
RFIC_CLK_BB 2B2 13B4
RFIC_CLK_NFC 2B2 16C3
RF_TXDET1 13C3 15A3
RTC32K1V8 2D2 5D4
RXIN_N 16A1 16B2
RXIN_P 16A1 16B2
R_ANTMLB_DR_ANT 12A3
R_ANTMLB_TR_ANT 15A3
R_B1_PA_DPX 14C4 15B3
R_B1_PR_DPX_IP 15B3
R_B1_PR_RFC 13B3 15B3
R_B1_TR_DPX 15B4
R_B1_TR_TXM 15A3 15B4
R_B2_PA_DPX 14C4 15C3
R_B2_PR_DPX_IP 15C3
R_B2_PR_RFC 13B3 15C3
R_B2_TR_DPX 15C4
R_B2_TR_TXM 15A3 15C4
R_B3_PA_DPX 14B4 15C3
R_B3_PR_DPX_IP 15C3
R_B3_PR_RFC 13B3 15C3
R_B3_TR_DPX 15C4
R_B3_TR_TXM 15A3 15C4
R_B5_PA_DPX 14C4 15D3
R_B5_PR_DPX_IP 15D3
R_B5_PR_RFC 13B3 15D3
R_B5_TR_DPX 15D4
R_B5_TR_TXM 15A3 15D4
R_B7_PA_DPX 14A4 15B1
R_B7_PR_DPX_IP 15B1
R_B7_PR_RFC 13B3 15B1
R_B7_TR_DPX 15B2
R_B7_TR_TXM 15A3 15B2
R_B8_PA_DPX 14D4 15B1
R_B8_PR_DPX_IP 15C1
R_B8_PR_RFC 13B3 15C1
R_B8_TR_DPX 15B2
R_B8_TR_TXM 15A3 15B2
R_B20_PA_DPX 14D4 15C1
R_B20_PR_DPX_IP 15C1
R_B20_PR_RFC 13B3 15C1
R_B20_TR_DPX 15C2
R_B20_TR_TXM 15A3 15C2
R_B20F_PA_DPX 14D4 15C1
R_B20F_PR_DPX_IP 15C1
R_B20F_PR_RFC 13B3 15C1
R_B20F_TR_DPX 15C2
R_B20F_TR_TXM 15A3 15C2
R_B40_TRX_PA 14B3
R_B40_TR_TXM 14B4 15B3
R_B41B36_PRX_PA 14A3
R_B41B36_PRX_RFC 13B3 14A4
R_B41B36_TRX_PA 14B3
R_B41B36_TR_TXM 14B4 15B3


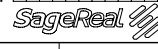
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MODIFIED	NAME SCHEMATIC	DATE 17/12/2021	CHECKED	NAME SCHEMATIC	DATE 17/12/2021
TITLE SCHEMATIC BOARD					
Product Name xq683_mm1_v11b		PAGE/SHEET 18 / 23			
P. N.		FORMAT SIZE A2	DOCUMENT NUMBER TYPE PART VERS.		
VER.	DATE				

R_HBL_TX_PA	14B2
R_HBL_TX_RFC	13C3 14B1
R_LLB1_TX_RFC	13C3 15A1
R_LB2_TX_PA	14C2
R_LB2_TX_RFC	13C3 14C1
R_LB3_TX_PA	15A2
R_MBL_TX_PA	14B2
R_MBL_TX_RFC	13C3 14B1
R_MB2_TX_RFC	13C3 15A1
R_MB3_TX_PA	15A2
SCL0	5C3 9B2
SCL1	10C4 5C3 10A4 10B1 10C1
SCL2	5A1 9A1
SCL3	5D3 6C2 6C2 16C3
SCL4	5B1 9C2
SCL5	3A2 5D3 9C4 9D2
SCL6	5D3 9D1
SDA0	5C3 9B2
SDA1	10C4 5D3 10A4 10B1 10C1
SDA2	5A1 9A1
SDA3	5D3 6C2 6D2 16C3
SDA4	5B1 9C2
SDA5	3A2 5D3 9C4 9D2
SDA6	5D3 9C1
SDI	5C4 10A2
SDO	5C4 10B2
SD_CLK	5C3 7C2
SD_CMD	5C3 7C2
SD_DAT0	5C3 7C2
SD_DAT1	5C3 7D2
SD_DAT2	5C3 7C2
SD_DAT3	5C3 7C2
SIM1_CLK	5A3 7A2
SIM1_IO	5A3 7A1
SIM1_NFC_SwP1	7A1 16B2
SIM1_RST	5A3 7A2
SIM2_CLK	5B3 7B2
SIM2_IO	5B3 7B1
SIM2_NFC_SwP2	7B1 16C2
SIM2_RST	5A3 7B2
SPI2_CLK	5C4 9B2
SPI2_CS	5C4 9A3
SPI2_MI	5C4 9A3
SPI2_MO	5C4 9B2
SPI4_CLK	5C4 10A1
SPI4_CS	5C4 10A1
SPI4_MI	5C4 10A1
SPI4_MO	5C4 10A1
SRCLKENW0	2C3 5B4
SRCLKENW1	2C3 5B4 13B1
SYSRSTB	2B3 5B4
TREF	2C3
TX_N	16A1 16B2
TX_P	16A1 16B2
TX_PWR_REQ	16B3 16C2
TYPEC_CC	9B3 10D4
URXD0	5C4
USB_DM	3C1 5B3
USB_DM_1	3C2 9B3 10C4
USB_DP	3C1 5B3
USB_DP_1	3C2 9B3 10D4
USB_ID	5B3 9B3
UTXD0	5C4
VA12	4A1 4A1 4C1 4C1 4C2 5B2 8C1
VAUD28	2C2 3A3
VBATBB	2B4 2C3 2C3 2D4 3A1 4C3 6B2 6C2 8D3 9B3 9B4 9C4 9D4 10B4 10B4 16B4 16C1 16C2 2B4 2A3 2B4 2A3 2B4 2A3 2B4 2A3 2C4 2B3
VBATBB_SUPPLY	14A1 2C3 3A1 3A1 3C2 9C3 14A1 14B2 15B1
VBAT_LDO	2B3 2C4
VBAT_SHPS	2A3 2B4
VBUS	9B4 10C4 11C2 11C2
VCAPA	2C2 9A1 9B2 9C1
VCAPAF	2C2 9A1
VCAPD	2B2 9A1 9B2
VCAPIO	2B2 9A1 9B2
VCDT	2C3
VCHG	2C4 2C4 3A2 9B2 11C1 11C1
VCN18	2B2 8D3

VCN18_GPS	2B1 8B4
VCN18_WBT	2B1 8C2
VCN28	2C2 8A2 8B2
VCN33	2C2 8B2
VCN33_EXT	8B2 8D2
VCORE	2A2 4A2 4A2 4C1
VCORE2	2A2 4A4
VCORE2_FB	2A2 4A4
VCORE_FB	2A2 4A2
VDD1V8_SOC	2C1 4A1 4A1 4A1 4A1 5B2 8C1
VDD1_LMI	4A1 6A1 6A2
VDD2V8	2C2 9B4 10A3 10B4
VDD2_LMI	4B4 6A1 6A2 6B1 6B3
VDD2_FB_LMI	6A2 6C1
VDDQ_LMI	4B4 6A1 6B2 6B4 6C1
VDDQ_FB_LMI	6B2 6C1
VDDUP	16B4 16C1
VDRAM	2B2
VEFUSE	2C2 4B1
VENNMC_3V3	2C2 7A3
VIBR	2A1 2C2
VMC	2C2 5C3
VMDCH	2C2 7C3
VPA	2A2 2A2 14A1 14A1 14A1 14B1 14A1 14A1 10A4 10B4
VPS	10A4 10B4
URF12	2B2 13A1
URF18	2B2 13A1
URF28	2C2 12B3 15B4
VRTC	2D2
VSD	7C2 7C2
VSIM1	2C2 5A2 7A2 16B2
VSIM1_NFC	7A2 16B2
VSIM2	2C2 5A2 7B2 16C2
VSIM2_NFC	7B2 16B2
VSN	9A3 9C3
VSN_EN	5B4 9C4
VSP	9A3 9C3
VSP_EN	5B4 9C4
VSRAM_DVFS	2B2 4A4
VSRAM_OTHERS	2B2 4A4
VSYS1	2A2 2A2 2B3 2C4 2D4 6A1
VSYS2	2B3 2C4 4C3
VSYS2_FB	2C4 2D3
VUSB33	2C2 3B1 3B2 5B2
WAKEUP_REQ	5A4 16B3
WATCHDOG	2B3 5B4
WFSQ_ANT	9A3 9C4
WFBT_ANT	9A3 9C4
WF_CTRL0	8B2 9C3
WF_CTRL1	8B2 9C3
WF_CTRL2	8B2 9C3
WF_IN	8B2 8B3
WF_IP	8B2 8B3
WF_QN	8B2 8B3
WF_QP	8B2 8B3
XTAL1	16C3 16D3
XTAL2	16C3 16D3

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MODIFIED	NAME SCHEMATIC	DATE 17/12/2021	CHECKED	NAME SCHEMATIC	DATE 17/12/2021
TITLE SCHEMATIC BOARD					
Product Name xq683_mm1_v11b			PAGE/SHEET 19 / 23		
P. N.		FORMAT SIZE A2	DOCUMENT NUMBER		
		TYPE			
VER.		DATE		PART	
				VERS.	

Title: Cref Part Report
Design: xq583_mml_v11b
Date: Jan 14 17:09:29 2022

B.201	B.INDIP	[2A1]
B.202	B.INDIP	[2B1]
B.203	B.INDIP	[2B1]
B.204	B.INDIP	[2B1]
B.205	B.INDIP	[2C1]
B.206	B.INDIP	[2C1]
B.207	B.INDIP	[2C1]
B.210	B.INDIP	[2A1]
B.211	B.INDIP	[2A1]
B.700	B.INDIP	[8A4]
B.701	B.INDIP	[8A4]
B.1100	B.INDIP	[11C4]
B.1101	B.INDIP	[11D3]
B.1102	B.INDIP	[11D3]
B.1103	B.INDIP	[11D4]
B.1104	B.INDIP	[11D4]
B.1105	B.INDIP	[11D4]
B.1106	B.INDIP	[11C4]
B.1107	B.INDIP	[11C4]
B.1108	B.INDIP	[11C4]
B.1109	B.INDIP	[11C4]
B.1500	B.INDIP	[15A4]
B.1501	B.INDIP	[15A4]
B.1502	B.INDIP	[15B4]
B.1500	B.INDIP	[16A3]
C200	PC0201	[2D4]
C201	PC0603XSR	[2D4]
C202	PC0603XSR	[2B4]
C203	PC0603XSR	[2D3]
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P. N.		SageReal		DOCUMENT NUMBER	
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TITLE		SCHEMATIC BOARD			
Product Name		xq683_mml-v11b		PAGE/SHEET 21 / 23	
P. N.				FORMAT SIZE A2	
VER.	DATE	DOCUMENT NUMBER		TYPE	PART
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TITLE SCHEMATIC BOARD					
Product Name xq683_mmi_v11b		PAGE/SHEET 22 / 23			
P. N. 		FORMAT SIZE A2	DOCUMENT NUMBER TYPE PART VERS.		
VER.	DATE				

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Product Name xq683_mm1_v11b			PAGE/SHEET 23 / 23		
P. N.			FORMAT SIZE A2	DOCUMENT NUMBER	TYPE PART VERS.
SageReal					
VER.	DATE				

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