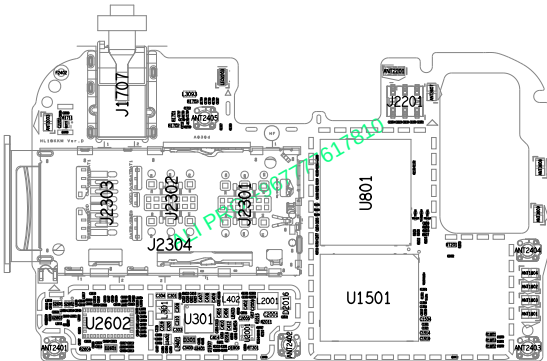


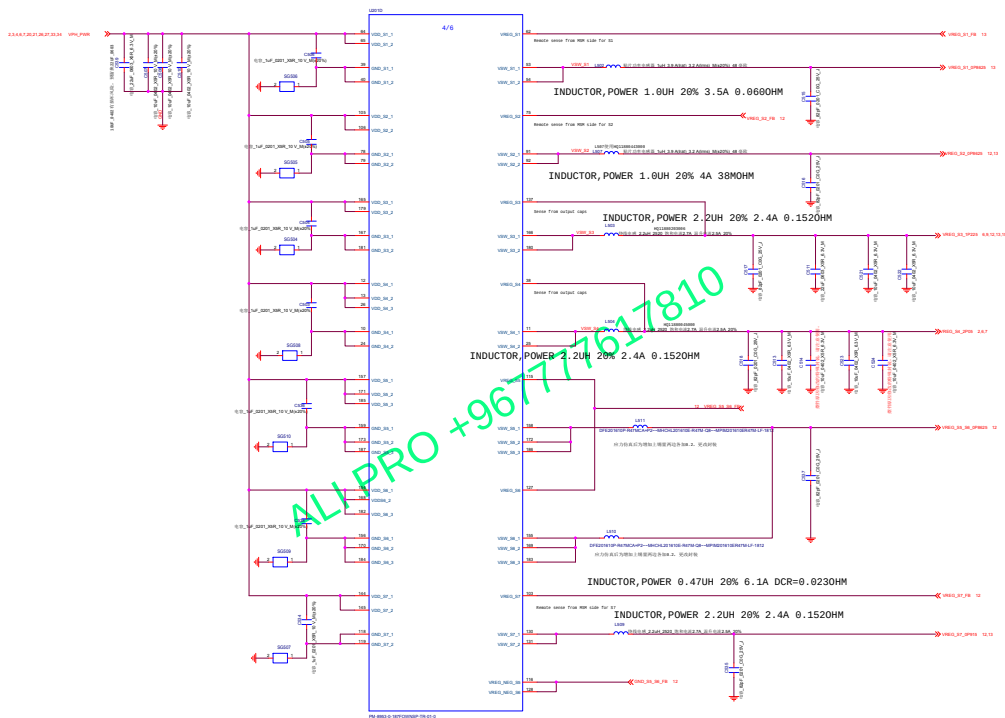


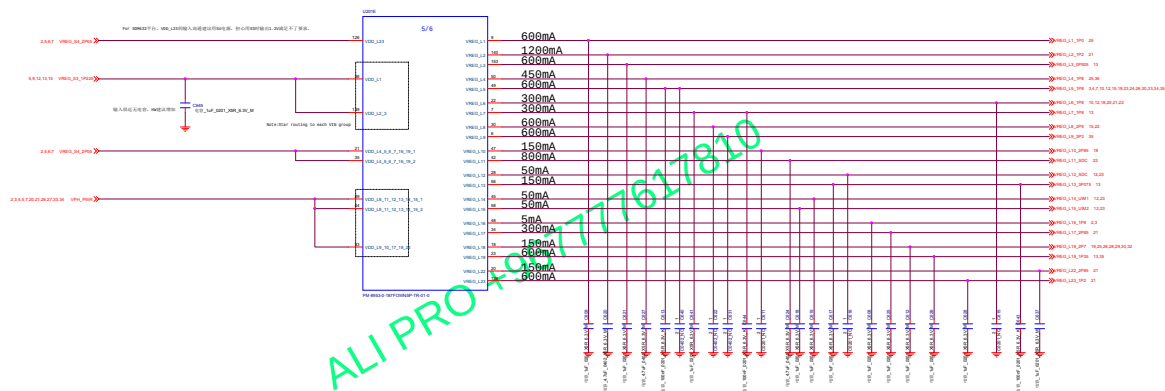


Platform:SDM632+PM8953+PMI632

- | | |
|--------------------------------|-----------------|
| 01.Contents | 31.DRX_LB |
| 02. PM8953 Control | 32.DRX_MHB |
| 03. PMI632 Charge | 33.QET4101 |
| 04. PMI632 Power | 34.ONSEMI PTIC |
| 05. PM8953 SMPS | 35.WCN_WCN3615 |
| 06. PM8953 LDO | 36.WLAN/GPS_FEM |
| 07. PM8953 CODEC | 37.NFC |
| 08. SDM632 Control | |
| 09. SDM632 EBI | |
| 10. SDM632 GPIO | |
| 11. SDM632 MIPI and RF Control | |
| 12. SDM632 Power1 | |
| 13. SDM632Power2 | |
| 14. SDM632 GND | |
| 15. MCP | |
| 16. Battery | |
| 17. MIC/REC/Headset | |
| 18. Fingerprint/Sidekey | |
| 19. FM LNA/B2B | |
| 20. LCD/CTP | |
| 21. Camera | |
| 22. Sensor/Flash | |
| 23. SIM/TF | |
| 24. Test Points/Sheilding | |
| 25. TRANSCEIVER_WTR2965 | |
| 26. TX_MMBPA_QM56020 | |
| 27. TRX_RF5212A/RF5216 | |
| 28. TRX_LB | |
| 29. TRX_MHB | |
| 30. DRX_SKY13418 | |

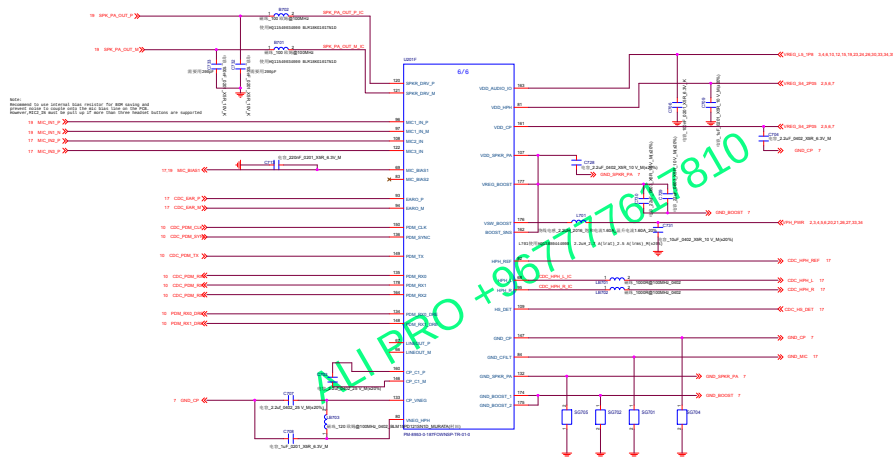
ALI PRO +967777617810

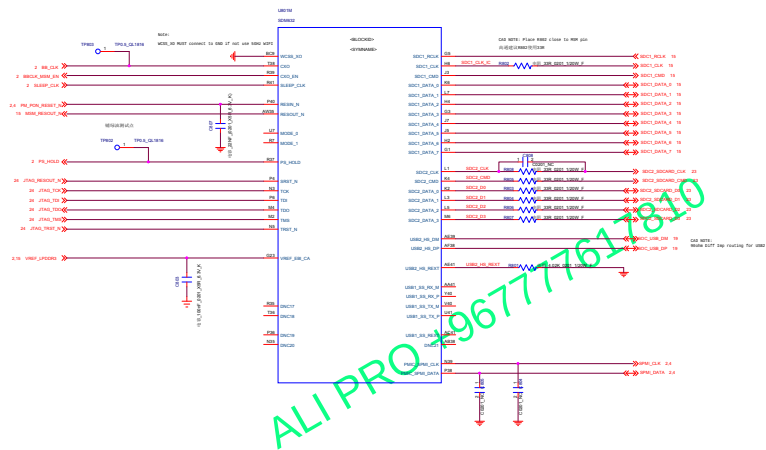




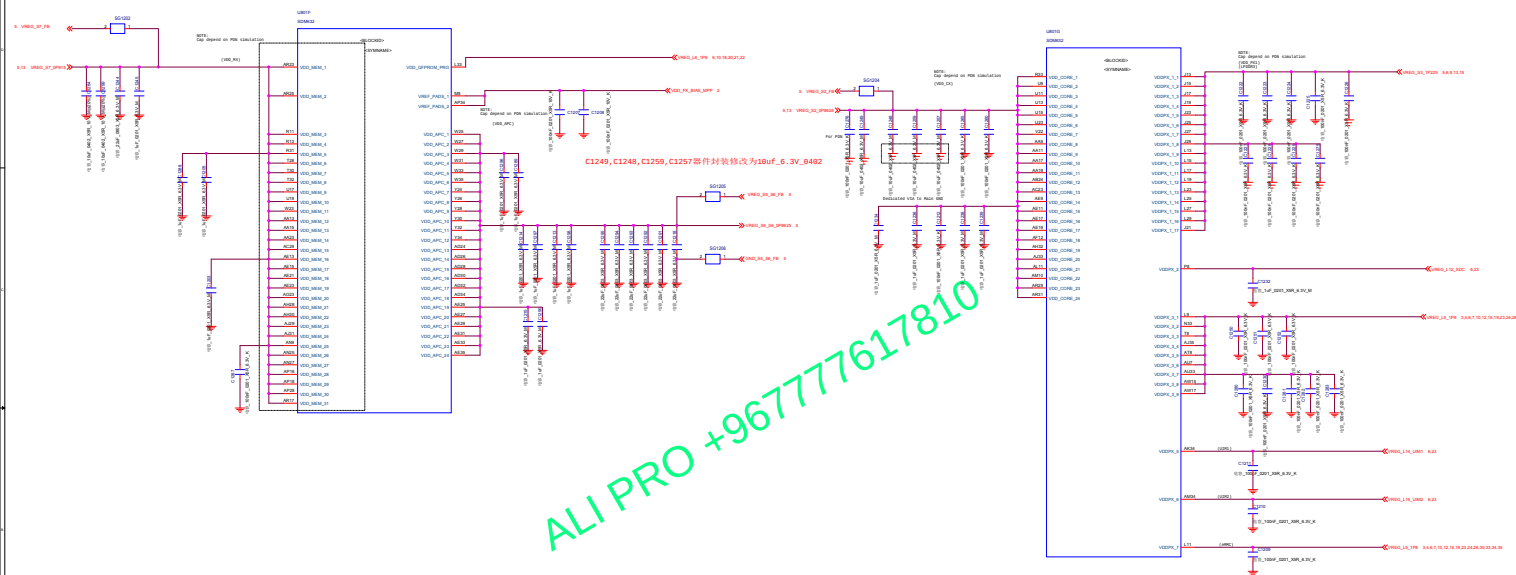
L22 (C637) /L4(C627) /L5(C640) /L6(C615)/L8(C622)/L9(C631)/L10(C611)/L12(C616)/L13(C617)/L14(C618)/L15(C619)/L17(C625)/L18(C612)

Capless 电容NC不

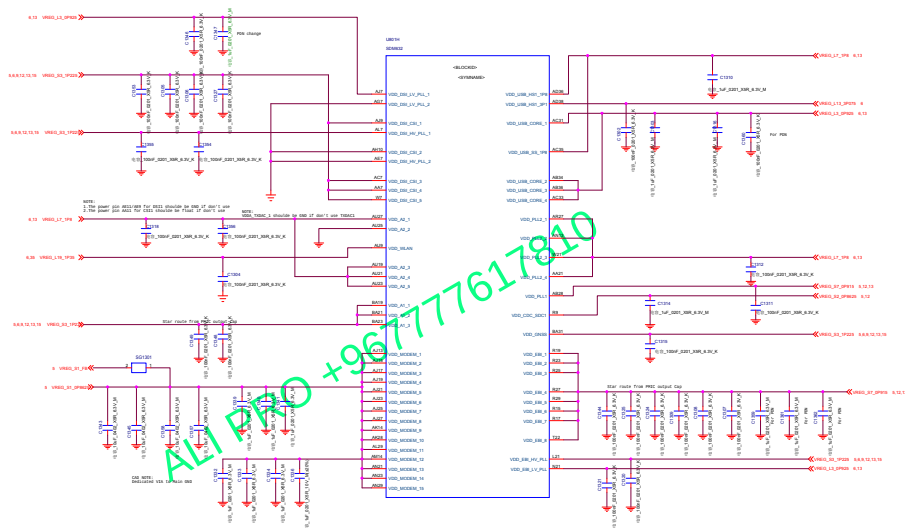
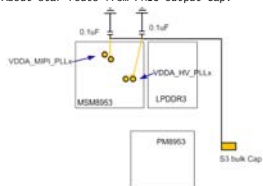


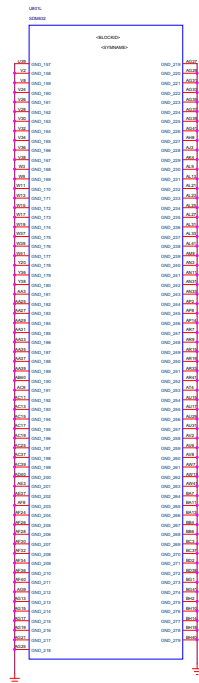
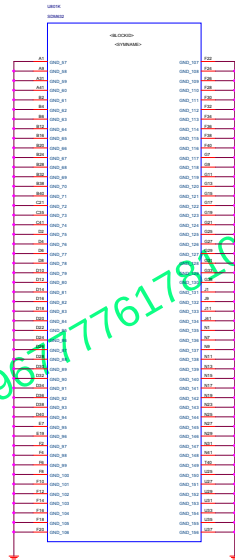
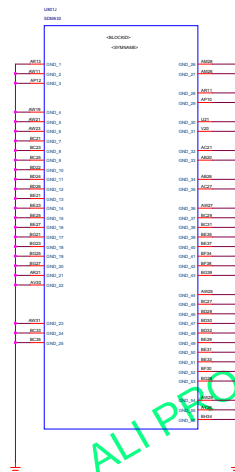
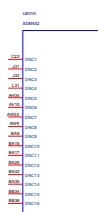


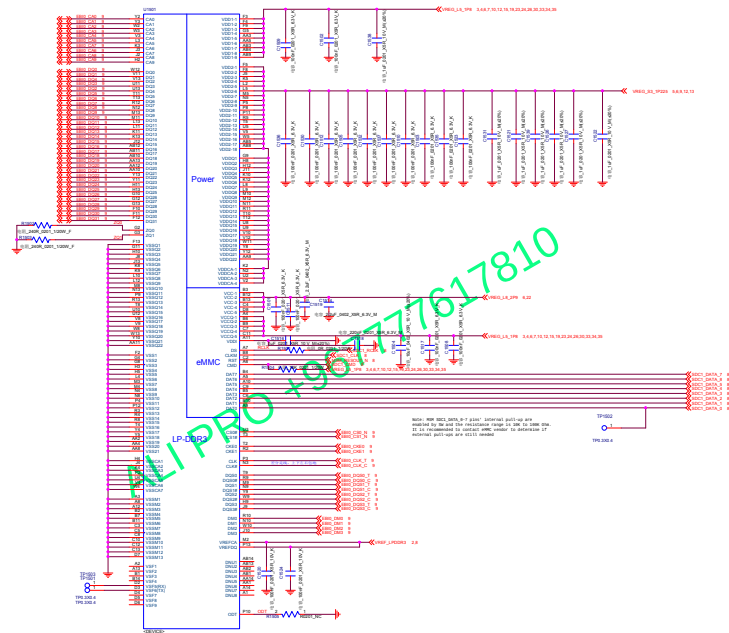




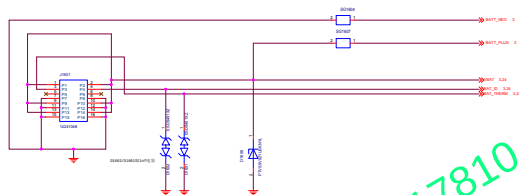
CAD NOTE:
About star route from PMIC output Cap:







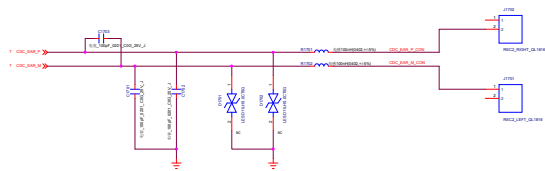
VBAT CONNECTOR



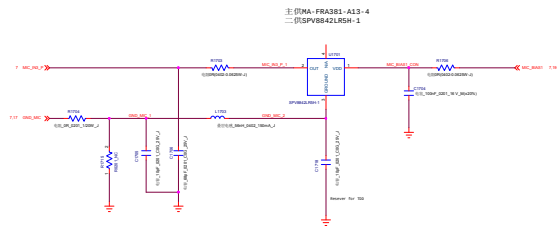
OVP
(二选一兼容设计)--放KB板上

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RECEIVER

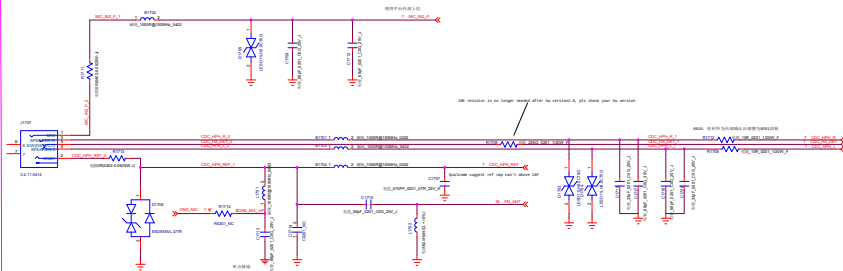


SUB MIC



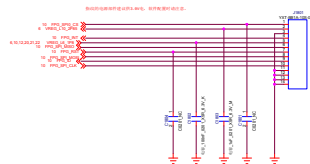
SMART PA

Headset



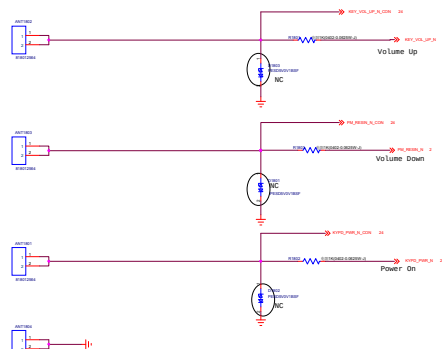
ALI PRO +967777617810

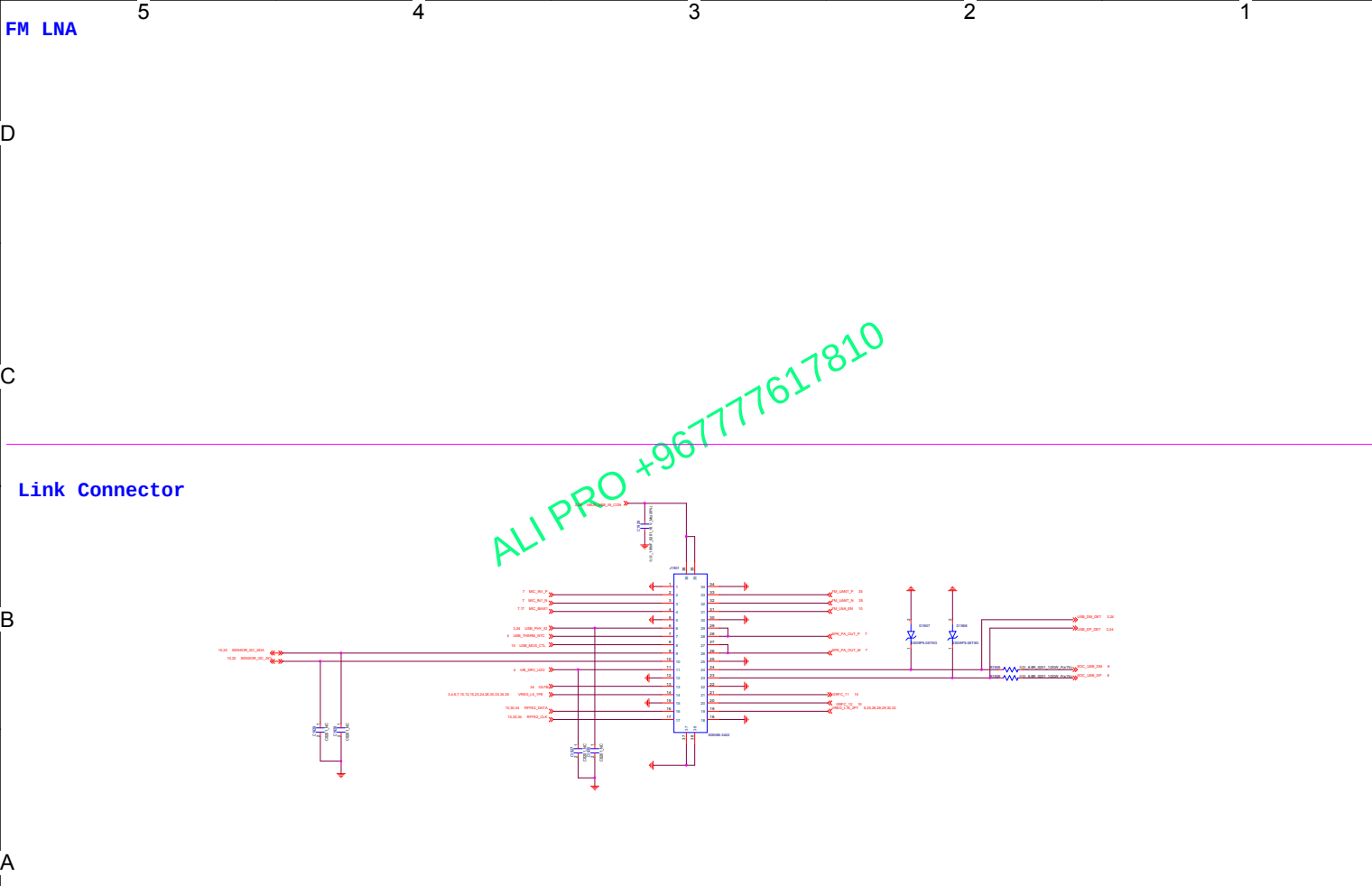
Fingerprint

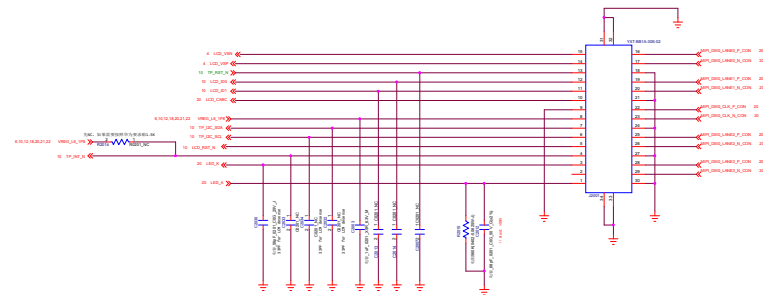


Side Key

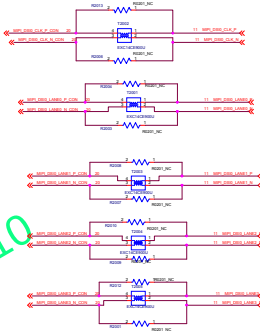
ALI PRO +967777617810





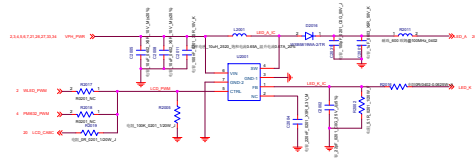


LCD MIPI Common Mode Filter



First using OR ,Second using EMI Filter.

LCM Backlight LED Driver



Main Rear Camera

D

C

Front Camera

B

A

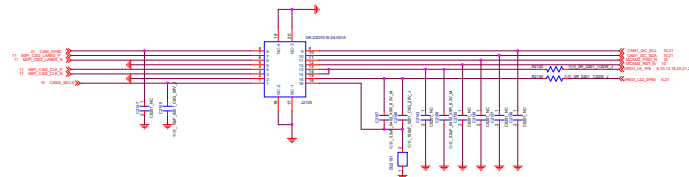
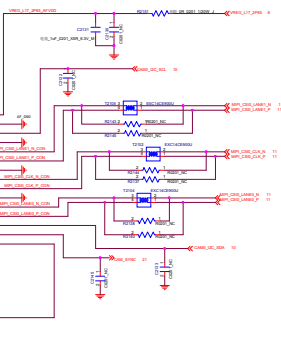
4

3

Sub Rear Camera 2

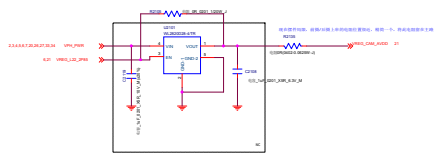
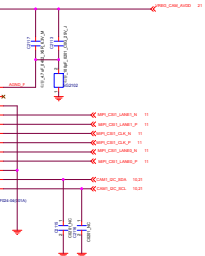
2

1



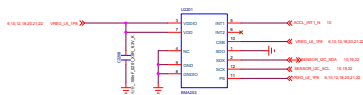
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后摄 &前摄 LDO(AVDD)

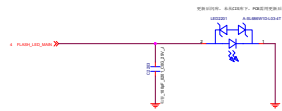


G SENSOR

BMA253 I2C: 0x18(SD0=GD) Write:0x30 Read:0x31
KXT13-1056 I2C: 0x88(SD0=GD) Write:0x1C Read:0x1D (曼谷无此选型)
M3416-Pi Write:0x08 Read:0x09

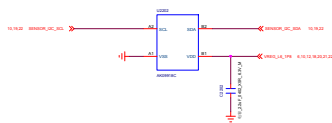


Rear Flash



Magnetic Sensor

AK8969 I2C: Write:0x18 Read:0x19
AF6133 I2C: Write:0x18 Read:0x19 (电压需要1.2-2.0V电压)
点能平台在comf中uid不同, I2C地址一样, 会根据不同uid调用各自的入口函数

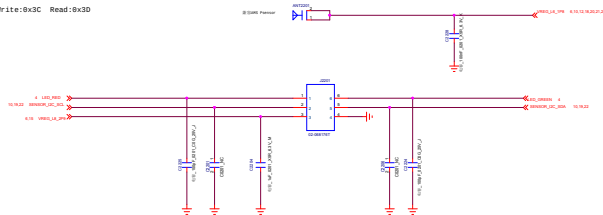


Front Flash



ALS/P SENSOR & RGB

PA22A80001: Write:0x3C Read:0x3D



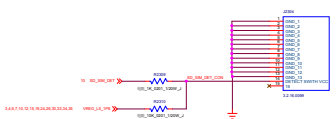
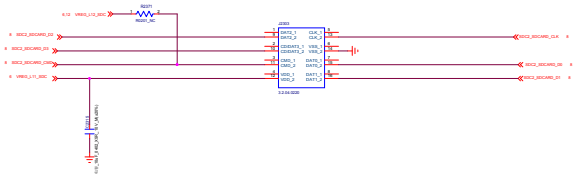
SIM/T卡三选三卡座 SD卡

D

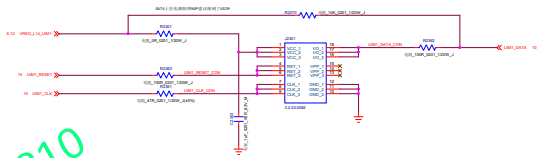
C

卡座

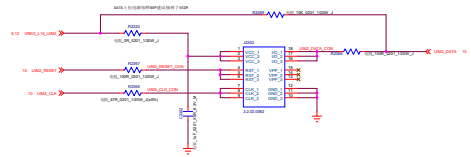
B



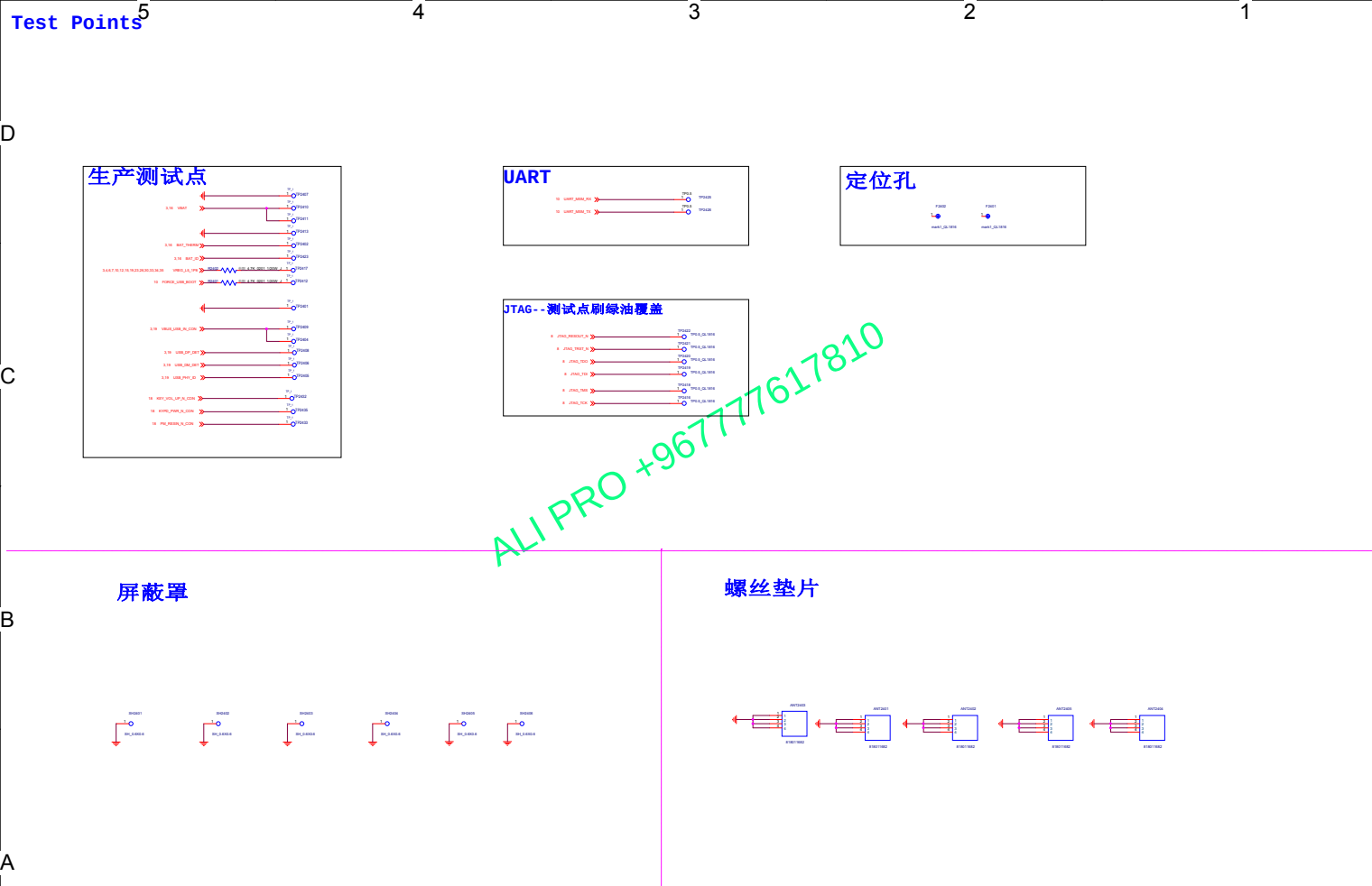
NANO SIM1



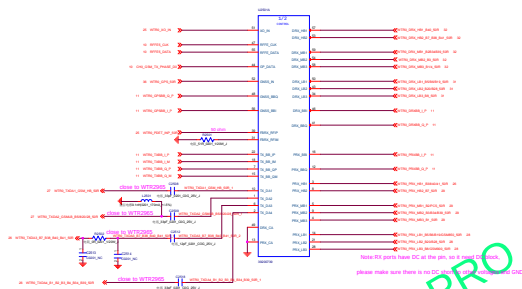
NANO SIM2



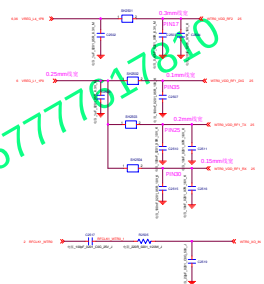
ALI PRO +967777617810



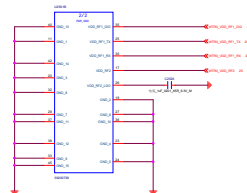
WTR 2965_A



WTR 2965 Power

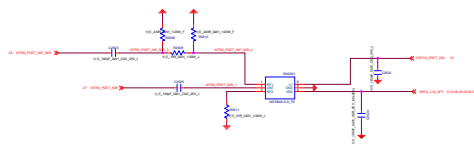


WTR 2965_B

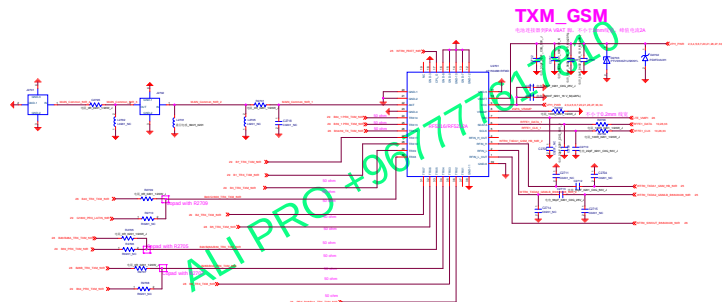


WTR 2965 Power_DET

Switch is required on FORCE_RTP path to avoid GSM CRFS issue



MAIN_ANT



TRX_LB

B5 TRX



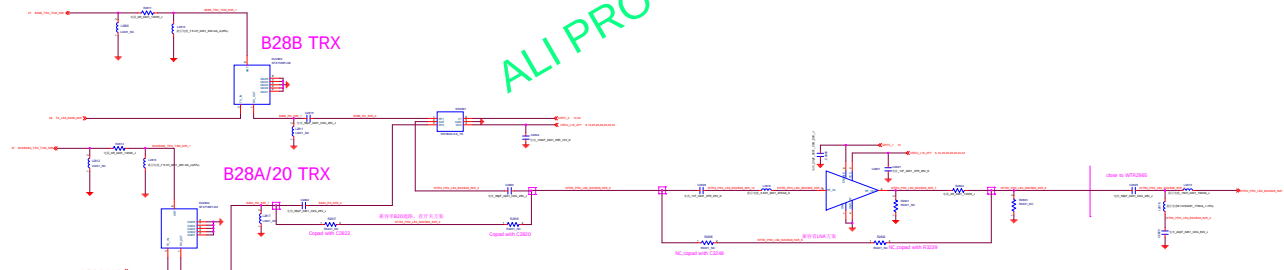
B8 TRX



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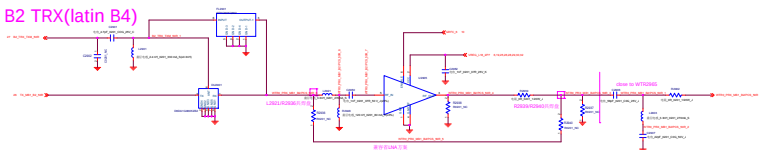
B28B TRX

B28A/20 TRX

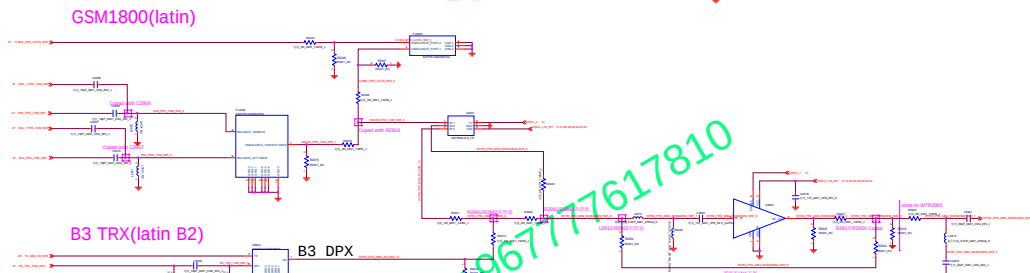


TRX_MB

B2 TRX(latin B4)



GSM1800(latin)



B3 TRX(latin B2)

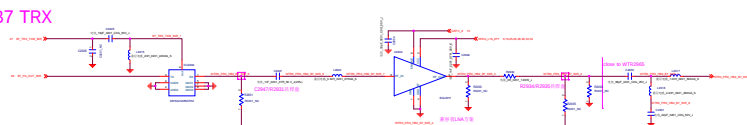
ALI PRO #96777617810

B1 TRX



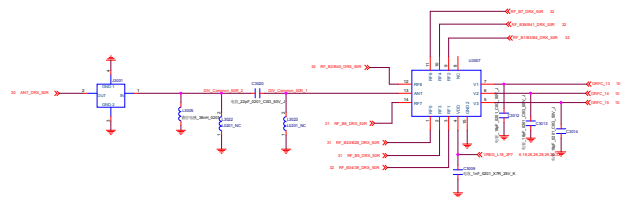
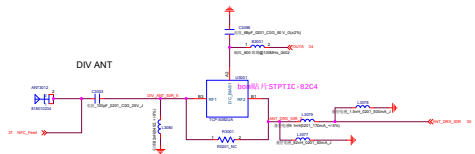
TRX_HB

B7 TRX

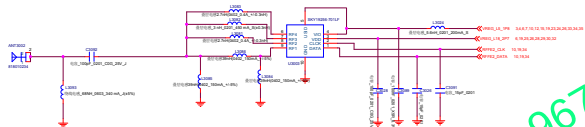


DIV_ANT

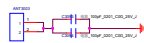
分集馈电脚



分集LB Ground



分集远地脚 NFC与分集预留



分集区域TOP面与A壳接地弹片

Diagram illustrating a multi-channel signal processing system, likely a video or audio interface, showing multiple input/output channels and their connections to a central processing unit.

The diagram shows a central processing unit (CPU) connected to multiple input/output channels. The channels are labeled CH1, CH2, CH3, and CH4. Each channel has a corresponding output labeled OUT1, OUT2, OUT3, and OUT4. The connections are color-coded: red for CH1, blue for CH2, green for CH3, and yellow for CH4. The outputs are labeled with their respective frequencies: OUT1 (100MHz), OUT2 (100MHz), OUT3 (100MHz), and OUT4 (100MHz). The diagram also shows a central bus structure with multiple input/output channels and their connections to a central processing unit.

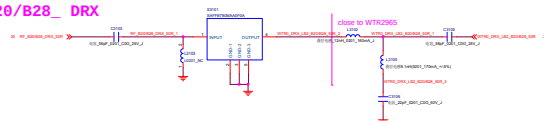
Key components and connections include:

- CH1 (Red):** Input/Output channel 1, connected to OUT1 (100MHz).
- CH2 (Blue):** Input/Output channel 2, connected to OUT2 (100MHz).
- CH3 (Green):** Input/Output channel 3, connected to OUT3 (100MHz).
- CH4 (Yellow):** Input/Output channel 4, connected to OUT4 (100MHz).
- Central Bus:** A central structure connecting the channels to the CPU.
- CPU:** The central processing unit, connected to the central bus.

The diagram is overlaid with a large green diagonal watermark reading "ALI PRO +967777617810".

DRX_LB

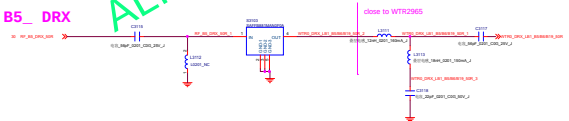
B20/B28_ DRX



B8_ DRX



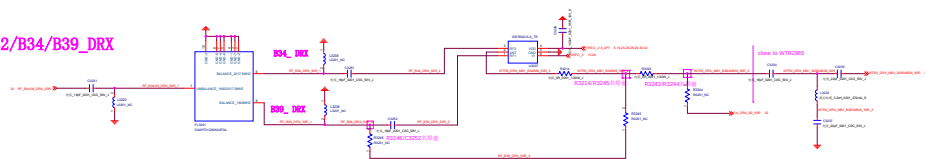
B5_ DRX



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DRX_MB

B2/B34/B39_DRX

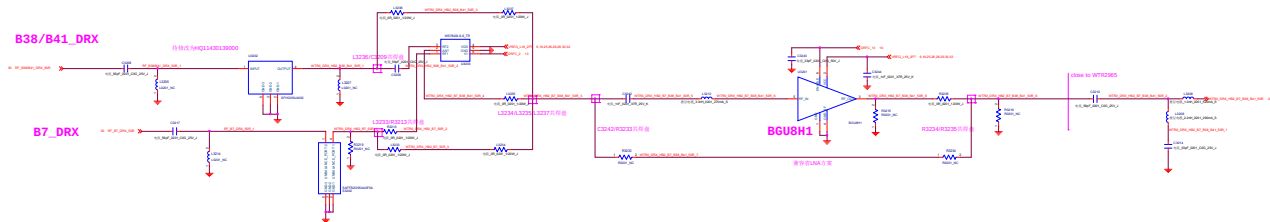


B1 (B4) _B3 DRX



DRX_HB

B38/B41_DRX

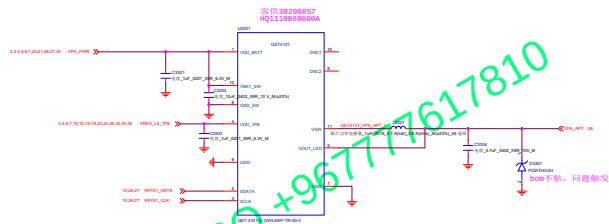


B7_DRX

B2/B48_DRX

ALI PRO +96777617810

QET4101



ONSEMI PTIC

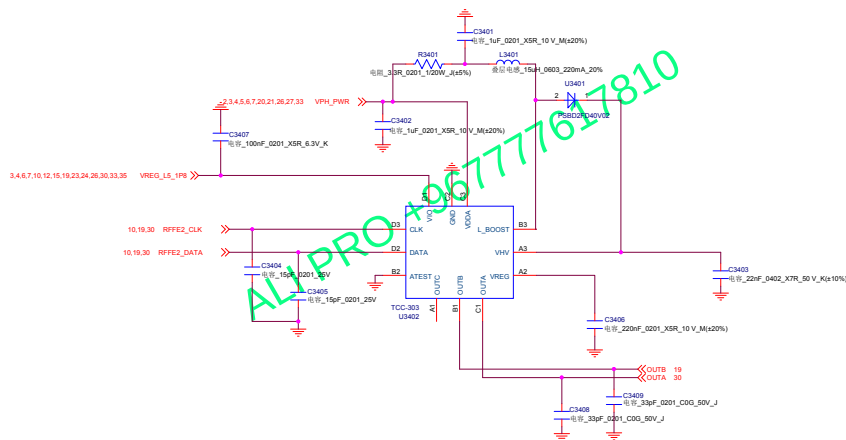
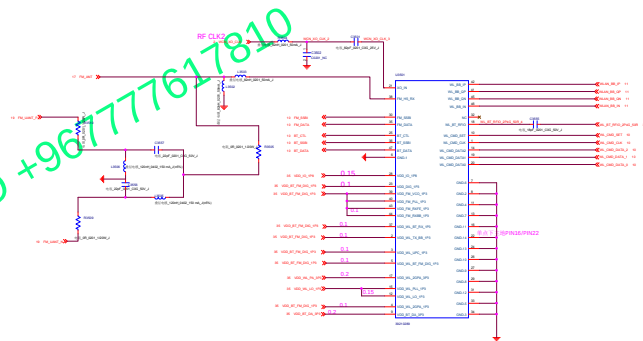
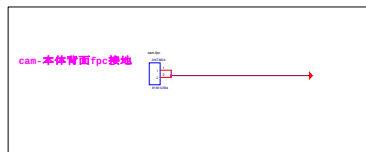


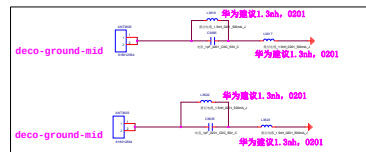
Figure 3: SSB calculation on 100 MHz. The diagram illustrates a signal processing flow for SSB calculation. It starts with a 100 MHz input, followed by a 0.25mm waveguide section, a 0.15mm waveguide section, a 0.4mm waveguide section, and a 0.3mm waveguide section. The signal is then split into two paths: one leading to a 0.2mm waveguide section and another leading to a 0.2mm waveguide section. The final output is a 0.2mm waveguide section. The diagram is labeled "Figure 3: SSB calculation on 100 MHz" and "Figure 3: SSB calculation on 100 MHz".



WLAN/GPS_FEM



所需无支架铜片接地弹片

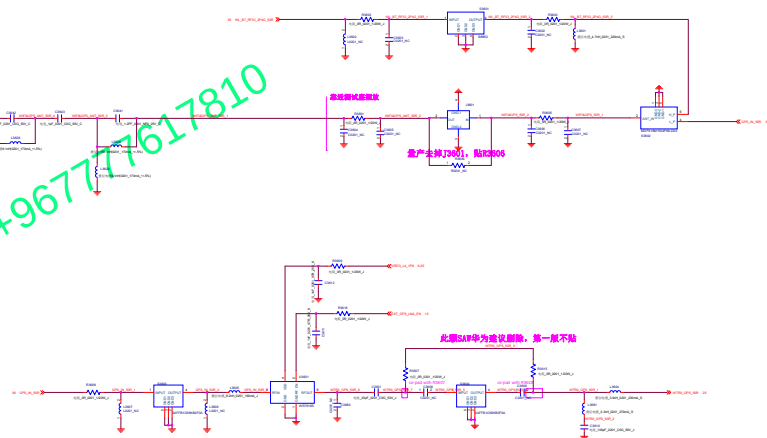


三合一接地脚G12

三合一锁电脚



ALI PRO +967771617810



5

4

3

2

1

D

C

B

A



ALI PRO +967777617810